TWL6032 Fully Integrated Power Management with Power Path and Battery Charger

1 Device Overview

1.1 Features

- Five Highly Efficient Buck Converters
  - One 3 MHz, 0.6 to 2.1 V at 5.0 A, DVS-Capable
  - One 6 MHz, 0.6 to 2.1 V at 2.5 A, DVS-Capable
  - Three 6 MHz, 0.6 to 2.1 V at 1.1 A, One Is DVS-Capable
  - Extended Mode for Higher Output Voltages
- 11 General-Purpose Low-Dropout Voltage Regulators (LDOs)
  - Six 1.0 to 3.3 V at 0.2 A With Battery or Preregulated Supply:
    - One Can be Used as Vibrator Driver
    - One 1.0 to 3.3 V at 50 mA With Battery or Preregulated Supply
    - One Low-Noise 1.0 to 3.3 V at 50 mA With Battery or Preregulated Supply
    - One 3.3 V at 100 mA USB LDO
    - Two LDOs for TWL6032 Internal Use
- USB OTG Module:
  - ID Detection, Accessory Charger Adapter (ACA) Support
  - Accessory Detection Protocol (ADP) Support
- Backup Battery Charger
- 12-Bit Sigma-Delta Analog-to-Digital Converter (ADC) With 19 Input Channels:
  - Seven External Input Channels
- 13-Bit Coulomb Counter With Four Programmable Integration Periods
- Low-Power Consumption:
  - 8 µA in BACKUP State
  - 20 µA in WAIT-ON State
  - 110 µA in SLEEP State, With Two DC-DC Converters Active
- Real-Time Clock (RTC) With Timer and Alarm Wake-Up:
  - Three Buffered 32-kHz Outputs
- SIM and SD/MMC Card Detections
- Two Digital PWM Outputs
- Thermal Monitoring:
  - High-Temperature Warning
  - Thermal Shutdown
- Control:
  - Configurable Power-Up and Power-Down Sequences (OTP Memory)
  - Configurable Sequences Between SLEEP and ACTIVE States (OTP Memory)
  - Three Digital Output Signals that can be Included in the Startup Sequence to Control External Devices
  - Two Inter-Integrated Circuit (I^2C) Interfaces
  - All Resources Configurable by I^2C
- System Voltage Regulator and Battery Charger With Power Path From USB:
  - Input Current Limit to Comply With USB Standard
  - 3-MHz Switched-Mode Regulator With Integrated Power FET for up to 2.0-A Current
  - Dedicated Control Loop for Battery Current and Voltage
  - External Low-Ohmic FET for Power Path and Battery Charging
  - Boost Mode Operation for USB OTG
  - Supplement Mode to Deliver Current From Battery During Power Path Operation
  - Charger for Single-Cell Li-Ion and Li-Polymer Battery Packs
  - Safety Timer and Reset Control
  - Thermal Protection
  - Input/Output Overvoltage Protection
  - Charging Indicator LED Driver
  - Compliant With:
    - USB 2.0
    - OTG and EH 2.0
    - USB Battery Charging 1.2
    - YD/T 1591-2006
    - Japanese Battery Charging Guidelines (JEITA)
- Battery Voltage Range From 2.5 to 5.5 V
- Package 5.21-mm × 5.36-mm 155-Pin WCSP

1.2 Applications

- Mobile Phones and Smart Phones
- Tablets
- Gaming Handsets
- Portable Media Players
- Portable Navigation Systems
- Handheld Devices

1.3 Description

The TWL6032 device is an integrated power-management integrated circuit (PMIC) for applications powered by a rechargeable battery. The device provides five configurable step-down converters with a current capability of up to 5.0 A for memory, processor core, I/O, auxiliary, preregulation for LDOs, and so forth. The device also contains nine LDO regulators for external use that can be supplied from a battery or a preregulated supply. The power-up and power-down controller is configurable and can support any power-up or power-down sequence (programmed in OTP memory). The RTC provides three 32-kHz clock outputs: seconds, minutes, hours, day, month, and year information; as well as alarm wakeup and timer. The TWL6032 device supports 32-kHz clock generation based on a crystal oscillator.

The device integrates a switched-mode system supply regulator from a USB connector. The switched-mode regulator includes power paths from the USB and battery with supplemental mode for immediate startup, even with an empty battery. The battery switch uses an external low-ohmic PMOS transistor allowing minimal serial resistance during fast charging and when operating from battery. The device can also be used without the external PMOS transistor; in this case, the battery is always tied to the system supply and the switched-mode regulator is used for battery charging.

The TWL6032 device is available in a 155-pin WCSP package, 5.21 mm × 5.36 mm with a 0.4-mm ball pitch.

Figure 1-1 shows the TWL6032 device block diagram.

Table 1-1. Device Information(1)

PART NUMBER	PACKAGE	BODY SIZE
TWL6032A1Bx	YFF (155)	5.21 mm × 5.36 mm
TWL6032A2Bx	YFF (155)	5.21 mm × 5.36 mm

(1) For more information, see Section 3, *Mechanical Packaging and Orderable Information*.

1.4 Functional Block Diagram

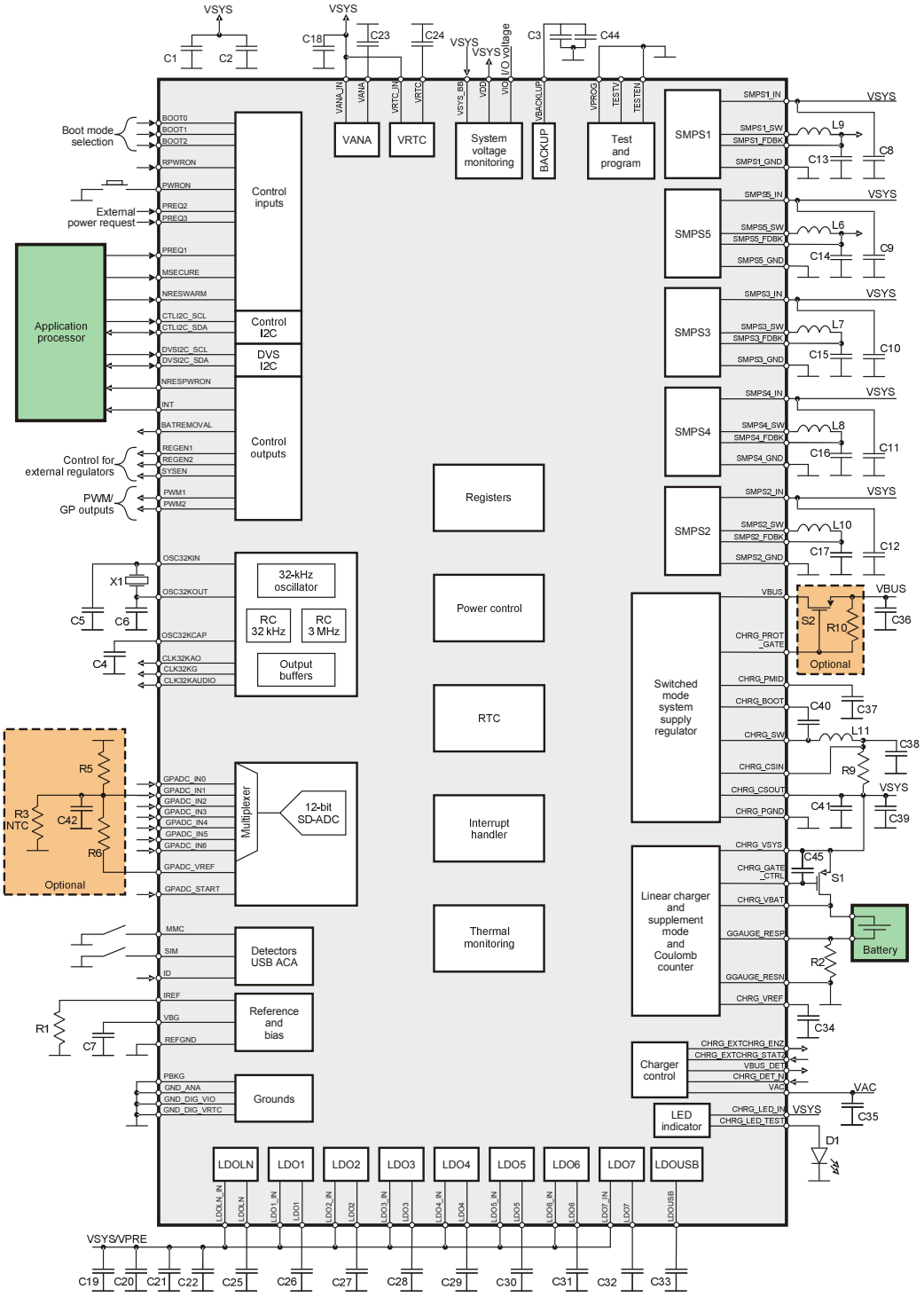


Figure 1-1. TWL6032 Device Block Diagram

For the complete TWL6032 data sheet (SWCS057), contact your TI sales representative.

2 Revision History

NOTE: Page numbers for previous revisions may differ from page numbers in the current version.

Changes from Revision A (September 2012) to Revision B	Page
• Changed document to standard TI format.	1

3 Mechanical Packaging and Orderable Information

3.1 Packaging Information

The following pages include mechanical packaging and orderable information. This information is the most current data available for the designated devices. This data is subject to change without notice and revision of this document. For browser-based versions of this data sheet, refer to the left-hand navigation.

PACKAGING INFORMATION

Orderable part number	Status (1)	Material type (2)	Package | Pins	Package qty | Carrier	RoHS (3)	Lead finish/ Ball material (4)	MSL rating/ Peak reflow (5)	Op temp (°C)	Part marking (6)
TWL6032A1B4YFFR	Obsolete	Production	DSBGA (YFF) | 155	-	-	Call TI	Call TI	-40 to 85	TWL6032 A1B4
TWL6032A1B4YFFT	Obsolete	Production	DSBGA (YFF) | 155	-	-	Call TI	Call TI	-40 to 85	TWL6032 A1B4
TWL6032A1B6YFFR	Obsolete	Production	DSBGA (YFF) | 155	-	-	Call TI	Call TI	-40 to 85	TWL6032 A1B6
TWL6032A2B0YFFR	Obsolete	Production	DSBGA (YFF) | 155	-	-	Call TI	Call TI	-40 to 85	TWL6032 A2B0
TWL6032A2B0YFFT	Active	Production	DSBGA (YFF) | 155	250 | SMALL T&R	Yes	SNAGCU	Level-1-260C-UNLIM	-40 to 85	TWL6032 A2B0
TWL6032A2B0YFFT.A	Active	Production	DSBGA (YFF) | 155	250 | SMALL T&R	Yes	SNAGCU	Level-1-260C-UNLIM	-40 to 85	TWL6032 A2B0
TWL6032A2B4YFFR	Active	Production	DSBGA (YFF) | 155	3000 | LARGE T&R	Yes	SNAGCU	Level-1-260C-UNLIM	-40 to 85	TWL6032 A2B4
TWL6032A2B4YFFR.A	Active	Production	DSBGA (YFF) | 155	3000 | LARGE T&R	Yes	SNAGCU	Level-1-260C-UNLIM	-40 to 85	TWL6032 A2B4
TWL6032A2B4YFFT	Active	Production	DSBGA (YFF) | 155	250 | SMALL T&R	Yes	SNAGCU	Level-1-260C-UNLIM	-40 to 85	TWL6032 A2B4
TWL6032A2B4YFFT.A	Active	Production	DSBGA (YFF) | 155	250 | SMALL T&R	Yes	SNAGCU	Level-1-260C-UNLIM	-40 to 85	TWL6032 A2B4
TWL6032A2B6YFFR	Active	Production	DSBGA (YFF) | 155	3000 | LARGE T&R	Yes	SNAGCU	Level-1-260C-UNLIM	-40 to 85	TWL6032 A2B6
TWL6032A2B6YFFR.A	Active	Production	DSBGA (YFF) | 155	3000 | LARGE T&R	Yes	SNAGCU	Level-1-260C-UNLIM	-40 to 85	TWL6032 A2B6
TWL6032A2B7YFFR	Active	Production	DSBGA (YFF) | 155	3000 | LARGE T&R	Yes	SNAGCU	Level-1-260C-UNLIM	-40 to 85	TWL6032 A2B7
TWL6032A2B7YFFR.A	Active	Production	DSBGA (YFF) | 155	3000 | LARGE T&R	Yes	SNAGCU	Level-1-260C-UNLIM	-40 to 85	TWL6032 A2B7
TWL6032A2B7YFFT	Active	Production	DSBGA (YFF) | 155	250 | SMALL T&R	Yes	SNAGCU	Level-1-260C-UNLIM	-40 to 85	TWL6032 A2B7
TWL6032A2B7YFFT.A	Active	Production	DSBGA (YFF) | 155	250 | SMALL T&R	Yes	SNAGCU	Level-1-260C-UNLIM	-40 to 85	TWL6032 A2B7

Orderable part number	Status (1)	Material type (2)	Package | Pins	Package qty | Carrier	RoHS (3)	Lead finish/ Ball material (4)	MSL rating/ Peak reflow (5)	Op temp (°C)	Part marking (6)
TWL6032A2B8YFFR	Obsolete	Production	DSBGA (YFF) | 155	-	-	Call TI	Call TI	-40 to 85	TWL6032 A2B8
TWL6032A2B8YFFT	Obsolete	Production	DSBGA (YFF) | 155	-	-	Call TI	Call TI	-40 to 85	TWL6032 A2B8
TWL6032A2BEYFFT	Active	Production	DSBGA (YFF) | 155	250 | SMALL T&R	Yes	SNAGCU	Level-1-260C-UNLIM	-40 to 85	TWL6032 A2BE
TWL6032A2BEYFFT.A	Active	Production	DSBGA (YFF) | 155	250 | SMALL T&R	Yes	SNAGCU	Level-1-260C-UNLIM	-40 to 85	TWL6032 A2BE

(1) **Status:** For more details on status, see our product life cycle.

(2) **Material type:** When designated, preproduction parts are prototypes/experimental devices, and are not yet approved or released for full production. Testing and final process, including without limitation quality assurance, reliability performance testing, and/or process qualification, may not yet be complete, and this item is subject to further changes or possible discontinuation. If available for ordering, purchases will be subject to an additional waiver at checkout, and are intended for early internal evaluation purposes only. These items are sold without warranties of any kind.

(3) **RoHS values:** Yes, No, RoHS Exempt. See the TI RoHS Statement for additional information and value definition.

(4) **Lead finish/Ball material:** Parts may have multiple material finish options. Finish options are separated by a vertical ruled line. Lead finish/Ball material values may wrap to two lines if the finish value exceeds the maximum column width.

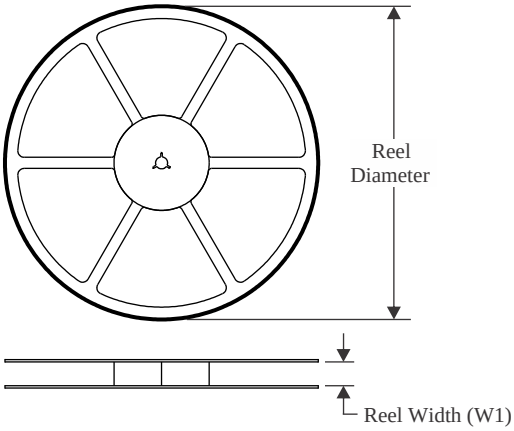
(5) **MSL rating/Peak reflow:** The moisture sensitivity level ratings and peak solder (reflow) temperatures. In the event that a part has multiple moisture sensitivity ratings, only the lowest level per JEDEC standards is shown. Refer to the shipping label for the actual reflow temperature that will be used to mount the part to the printed circuit board.

(6) **Part marking:** There may be an additional marking, which relates to the logo, the lot trace code information, or the environmental category of the part.

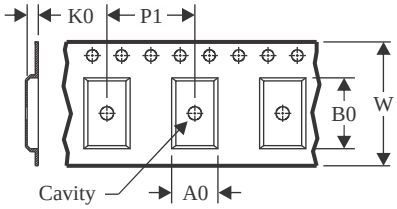
Multiple part markings will be inside parentheses. Only one part marking contained in parentheses and separated by a "~" will appear on a part. If a line is indented then it is a continuation of the previous line and the two combined represent the entire part marking for that device.

TAPE AND REEL INFORMATION

REEL DIMENSIONS

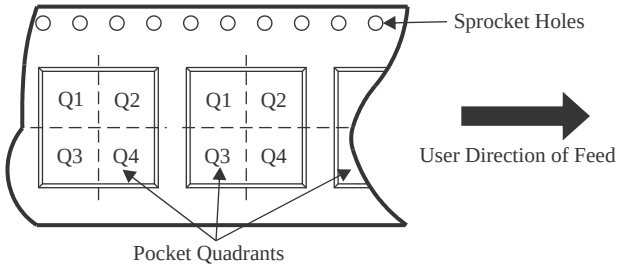


TAPE DIMENSIONS



A0	Dimension designed to accommodate the component width
B0	Dimension designed to accommodate the component length
K0	Dimension designed to accommodate the component thickness
W	Overall width of the carrier tape
P1	Pitch between successive cavity centers

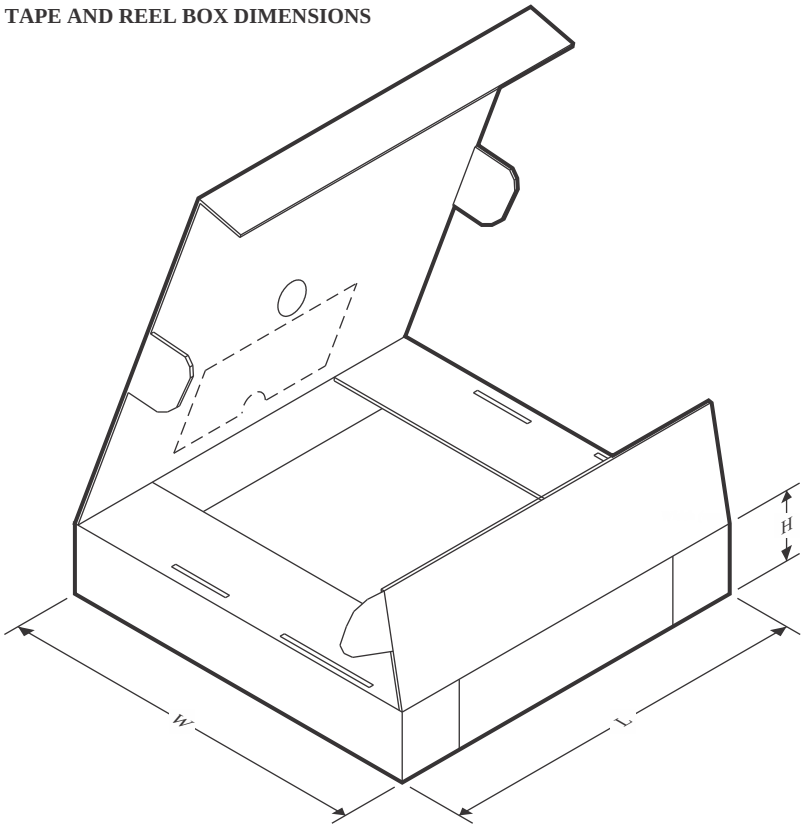
QUADRANT ASSIGNMENTS FOR PIN 1 ORIENTATION IN TAPE



*All dimensions are nominal

Device	Package Type	Package Drawing	Pins	SPQ	Reel Diameter (mm)	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P1 (mm)	W (mm)	Pin1 Quadrant
TWL6032A2B0YFFT	DSBGA	YFF	155	250	180.0	12.4	5.35	5.49	0.86	8.0	12.0	Q1
TWL6032A2B4YFFR	DSBGA	YFF	155	3000	330.0	12.4	5.35	5.49	0.86	8.0	12.0	Q1
TWL6032A2B4YFFT	DSBGA	YFF	155	250	180.0	12.4	5.35	5.49	0.86	8.0	12.0	Q1
TWL6032A2B6YFFR	DSBGA	YFF	155	3000	330.0	12.4	5.35	5.49	0.86	8.0	12.0	Q1
TWL6032A2B7YFFR	DSBGA	YFF	155	3000	330.0	12.4	5.35	5.49	0.86	8.0	12.0	Q1
TWL6032A2B7YFFT	DSBGA	YFF	155	250	180.0	12.4	5.35	5.49	0.86	8.0	12.0	Q1
TWL6032A2BEYFFT	DSBGA	YFF	155	250	180.0	12.4	5.35	5.49	0.86	8.0	12.0	Q1

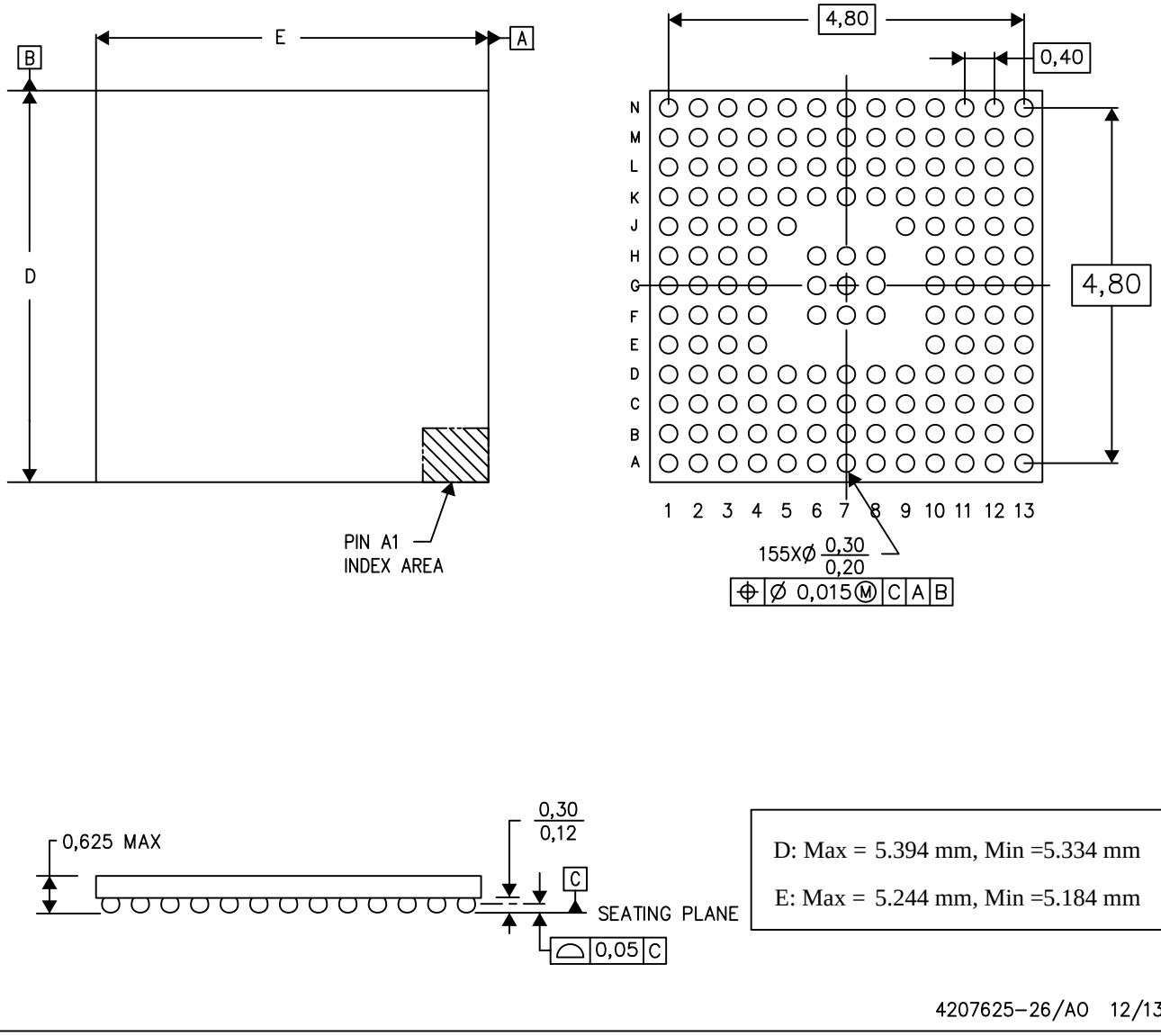
TAPE AND REEL BOX DIMENSIONS



*All dimensions are nominal

Device	Package Type	Package Drawing	Pins	SPQ	Length (mm)	Width (mm)	Height (mm)
TWL6032A2B0YFFT	DSBGA	YFF	155	250	182.0	182.0	20.0
TWL6032A2B4YFFR	DSBGA	YFF	155	3000	335.0	335.0	25.0
TWL6032A2B4YFFT	DSBGA	YFF	155	250	182.0	182.0	20.0
TWL6032A2B6YFFR	DSBGA	YFF	155	3000	335.0	335.0	25.0
TWL6032A2B7YFFR	DSBGA	YFF	155	3000	335.0	335.0	25.0
TWL6032A2B7YFFT	DSBGA	YFF	155	250	182.0	182.0	20.0
TWL6032A2BEYFFT	DSBGA	YFF	155	250	182.0	182.0	20.0

YFF (R–XBGA–N155) DIE–SIZE BALL GRID ARRAY



D: Max = 5.394 mm, Min =5.334 mm

E: Max = 5.244 mm, Min =5.184 mm

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NOTES: A. All linear dimensions are in millimeters. Dimensioning and tolerancing per ASME Y14.5M–1994.
B. This drawing is subject to change without notice.
C. NanoFree™ package configuration.

NanoFree is a trademark of Texas Instruments.

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Last updated 10/2025