

## **TAS5414/24 I<sup>2</sup>C Read/Write Errata**

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### **1 Errata Description**

A race condition can occur for the I<sup>2</sup>C state machine if both of the following conditions happen:

1. The rising edge of SCL and the rising edge of oscillator clocks align.
2. The I<sup>2</sup>C state machine is in the process of changing states.

The probability of occurrence is low and has not been reproduced yet by Texas Instruments in bench testing but is theoretically possible.

### **2 System Impact**

The TAS5414/24 might not acknowledge (NACK) an I<sup>2</sup>C command or incorrectly write/read register data.

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**Note:** Fault handling is not impacted although fault reporting through I<sup>2</sup>C might be incorrect.

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### **3 System Workaround**

None.

### **4 Comments**

This has been corrected in both the TAS5414A and TAS5424A.

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