

# ERRATA

## IEEE 1394b Three-Port Cable Transceiver/Arbiter TSB81BA3D

SILICON ERRATA ID	PROBLEM DESCRIPTION	RESOLUTION / WORK-AROUNDS
1. S800 $\beta$ jitter	<i>Symptom</i> The TSB81BA3D device does not meet all the jitter requirements specified for S800 operation in IEEE Std1394b-2002 tables 9-12 and 9-13.	The TSB81BA3D meets or exceeds the recommended IEEE Std 1394b-2002 corrections by the 1394 Trade Association. Refer to <i>1394TA Document TB2002001 Technical Bulletin 1394b Clarifications and Errata</i> , Draft 1.6:107, July 11, 2003, 9.7.1, Tables 9-12 and 9-13.
2. PowerPAD™ recommendation	Figure 9, <i>Example of a Thermal Land for the TSB81BA3D PHY</i> , should show thermal vias.	It is required that the TSB81BA3D uses a thermal land under its PowerPAD™ unless an external heatsink is used. It is strongly recommended that the thermal land be soldered to the PowerPAD™.  The recommended thermal land size is 10 mm × 10 mm with 36 thermal vias (6 × 6 array, 13-mil drill plated with no thermal web reliefs).  Refer to TI application report, <i>PowerPAD™ Thermally Enhanced Package</i> , <a href="#">SLMA002</a> for more information.

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