

XIO2213B Errata

This document is provided to summarize the errata seen on XIO2213B during validation.

1 Errata #1: UR Bit Incorrectly Set in the Uncorrectable Error Status Register When the ANFES Bit Is Set

1.1 Detailed Description

After a configuration transaction to an invalid function or a memory transaction to in an invalid memory window of the XIO2213B, the Unsupported Request bit in the Uncorrectable Error Status Register and the Advisory Non-Fatal Error Status bit in the Correctable Error Status Register are both set. In this scenario, only the Advisory Non-Fatal Error Status bit should be set as this does not result in an uncorrectable error.

1.2 Overall Impact

This causes a failure in the PCI SIG PTC Gold suite testing to Spec. revision 1.1. Revision 1.0a will continue to pass.

1.3 Workaround Proposal

None at this time as software does not implement AER.

1.4 Corrective Action

None for current device.

2 Errata #2: File Transfer Fails When L1 is Enabled

2.1 Detailed Description

The system will hang or the device will drop when L1 is enabled. The link will successfully enter L1, but when the root complex requests to exit L1 the XIO2213B is unresponsive. This is seen during prolonged file transfers. The following is observed:

1. Endpoint requests to go into L1
2. Root complex acknowledges the L1 request
3. Link goes into L1
4. Some unknown packet types are sent from the endpoint
5. Root complex wants to exit L1 and sends TS1 (not due to the unknown packet types sent from endpoint)
6. There is no response from endpoint
7. After some time, root complex tries to retrain the link but still no response from endpoint
8. Root complex goes into Link State Poll_Compliance

2.2 Overall Impact

This will cause the system to hang or the device to drop.

2.3 Workaround Proposal

Do not enable L1.

2.4 Corrective Action

None for current device.

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