



**ASSEMBLY:**

- 1) L1, L2 OPEN - default is 2013 series
- 2) D2 in SMB will not meet SMA pattern
- 3) D1 OPEN

**NOTES:**

- 1) R20 for test purposes only
- 2) built on PCB 7113 RevA

Orderable:	Designed for:	Mod. Date: 2/29/2016
TID #:	Project Title: TPS40170 @ Fsw 400kHz	
Number: PMP8629	Rev: A	Sheet Title: *
SVN Rev: Version control disabled	Assembly Variant: [No Variations]	Sheet: 1 of 1
Drawn By:	File: PMP8629RevA_SchDoc	Size: C
Engineer: B.Geck	Contact: TechSupport	http://www.ti.com

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