

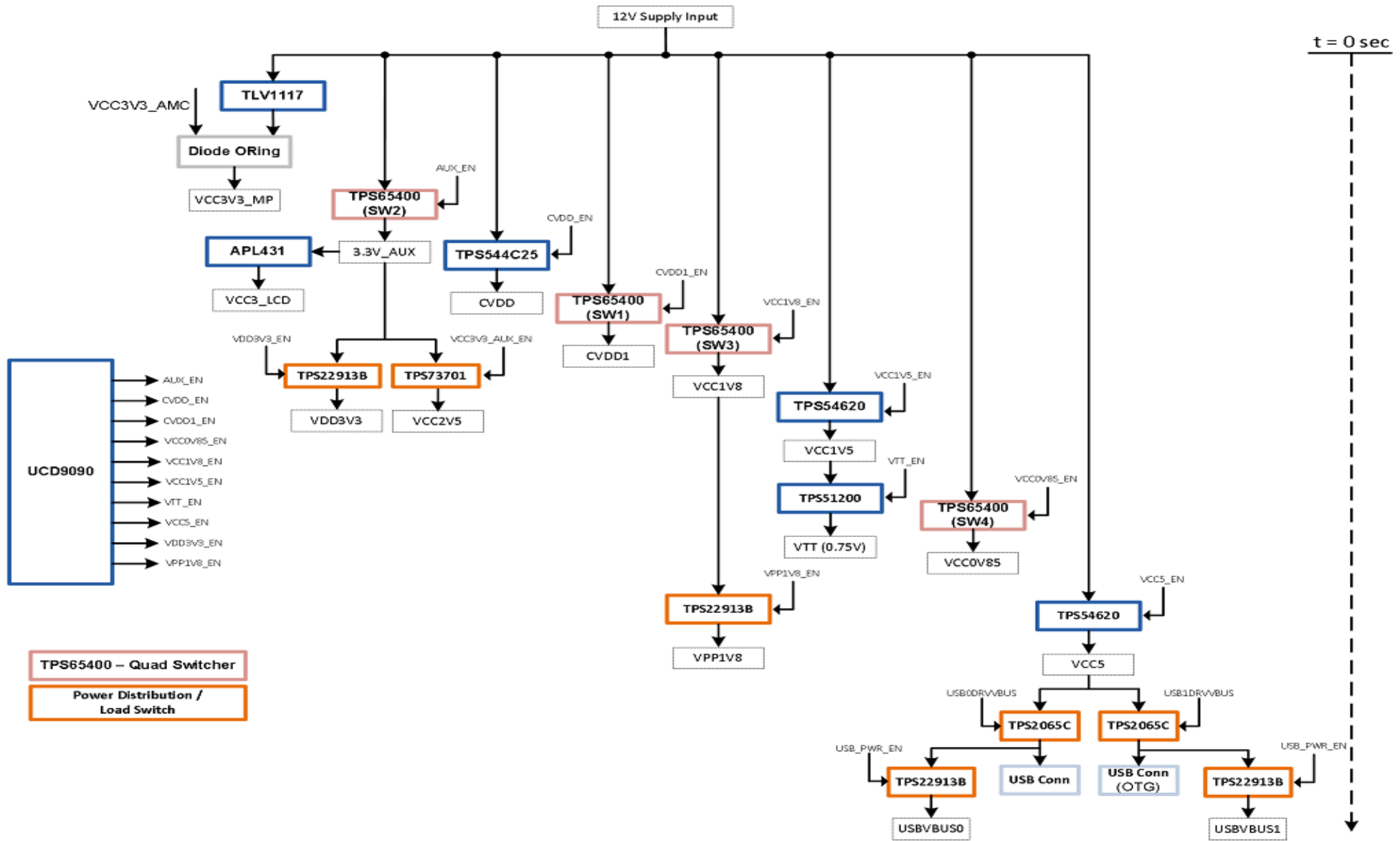
TIDEP0041

TI Design - Generating the AVS SmartReflex Core voltage for the K2E using the TPS544C25 and the LM10011

Based on the XEVMK2EX design.
This schematic is a subset of the XEVMK2EX schematic.

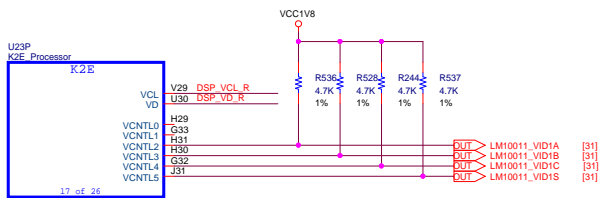
Title		
TI Design Generating AVS with TPS544C25 and LM10011		
Size A	Document Number TIDEP0041	Rev A
Date:	Thursday, July 02, 2015	Sheet 1 of 1

EDISON – Power Tree



Project		K2E EVM		Designed for TI by elfnchips	
Title		POWER DISTRIBUTION			
Size	Document Number			Rev	
C	16_00175_03			3.02	
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SMART REFLEX



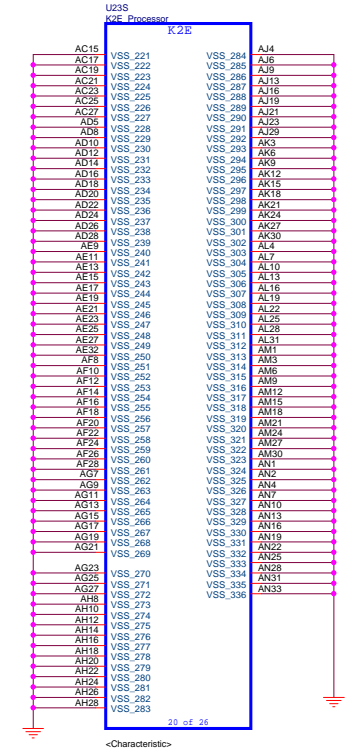
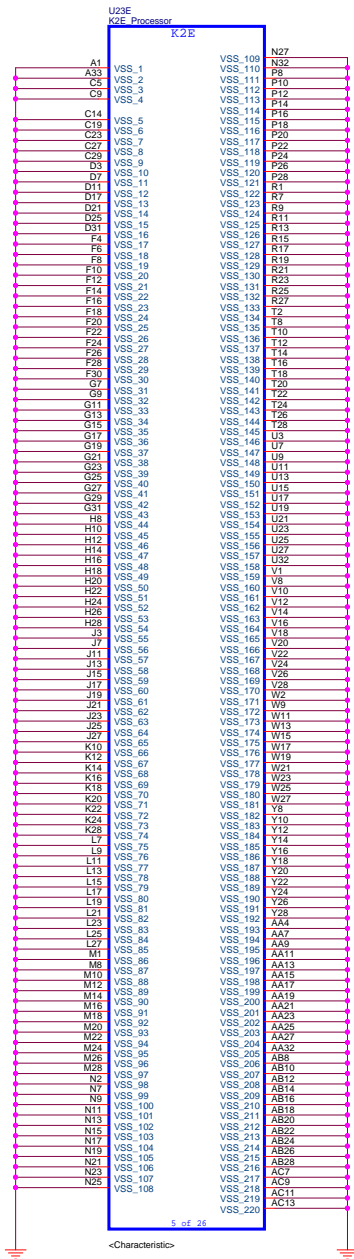
<-Characteristic>



These signals
are not used
but require
external
pull-ups

Project K2E EVM		Designed for TI by elfochips	
Title CLOCK and SMART REFLEX			
Size C	Document Number 16_00175_03	Rev 3.02	
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SOC GROUND



Project		K2E EVM		Designed for TI by elfnchips	
Title		SoC Ground			
Size	Document Number	Rev			
C	16_00175_03	3.02			
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