



Assembly Notes:
 Built on PMP7141 Rev_A PWB
 DNP = "Do Not Populate"
 Add ref. des. > 99 on the board

T1 details:
 Platform: EPCOS E13/7/4; core = N87
 Primary: 60 turns pins 1-2 + 60 turns pins 2-3, 0.2mm diam., 2 layers each winding
 Aux: 60 turns pins 4-5, 0.1mm diam., 1 layer, placed in "sandwich" between two half-primaries
 Insert 0.05mm mylar between primaries and aux.
 Lp=880uH, Llk=50uH, Al=60, Gap=0.3mm



Title 180Vac...264Vac to 40Vmax@120mA LED Driver		
Size B	Number PMP7141	Rev C
Date 10/10/2012	Drawn by R. Scibilia	
Filename PMP7141 Rev_C_pads.sch	Sheet 1 of 1	

Engineer R. Scibilia

10/10/2012

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