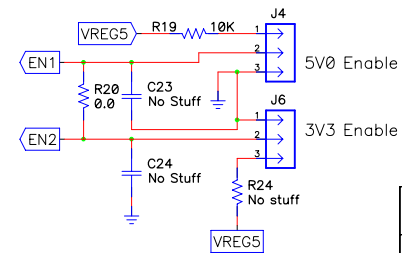
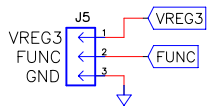


SKIPSEL Logic:  
 VREF2 = Auto-Skip Mode  
 VREG3 = OOA Auto-Skip Mode for fsw < 400KHz  
 VREG5 = OOA Auto-Skip Mode for fsw > 400KHz

FUNC Logic:  
 GND = Current Mode, OVP Enable  
 VREG3 = D-Cap Mode, OVP Enable



Enable Logic:  
 VREG5 = On  
 GND = Off

Texas Instruments

Title		
TPS51220RHB Reference Power		
Size	Number	Rev
C	PMP3843	A
Date	5/9/08	Drawn by R Taylor
Filename	PMP3843_REVA.SCH	Sheet 1 of 1

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Interface	<a href="http://interface.ti.com">interface.ti.com</a>	Energy	<a href="http://www.ti.com/energy">www.ti.com/energy</a>
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