
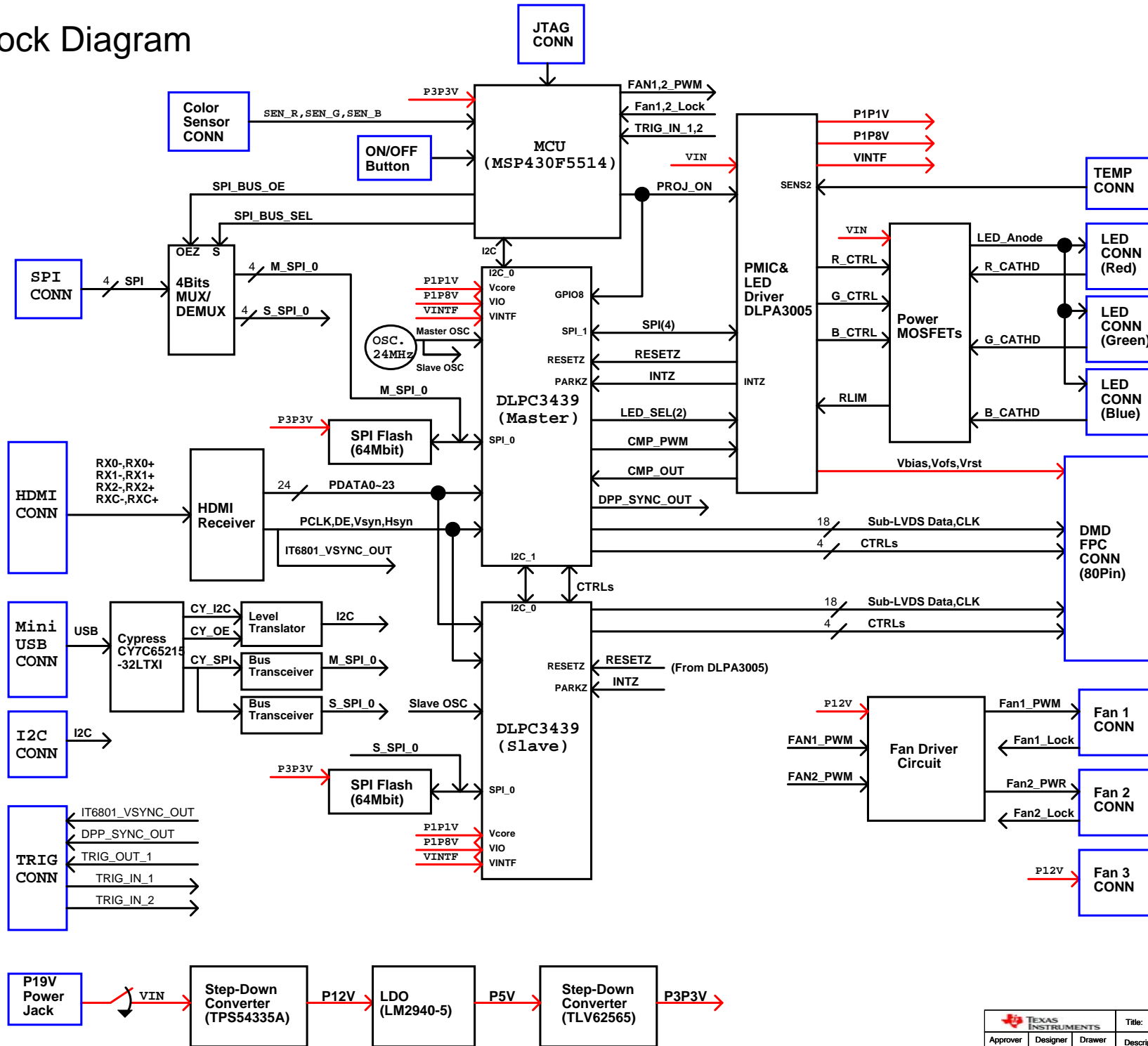


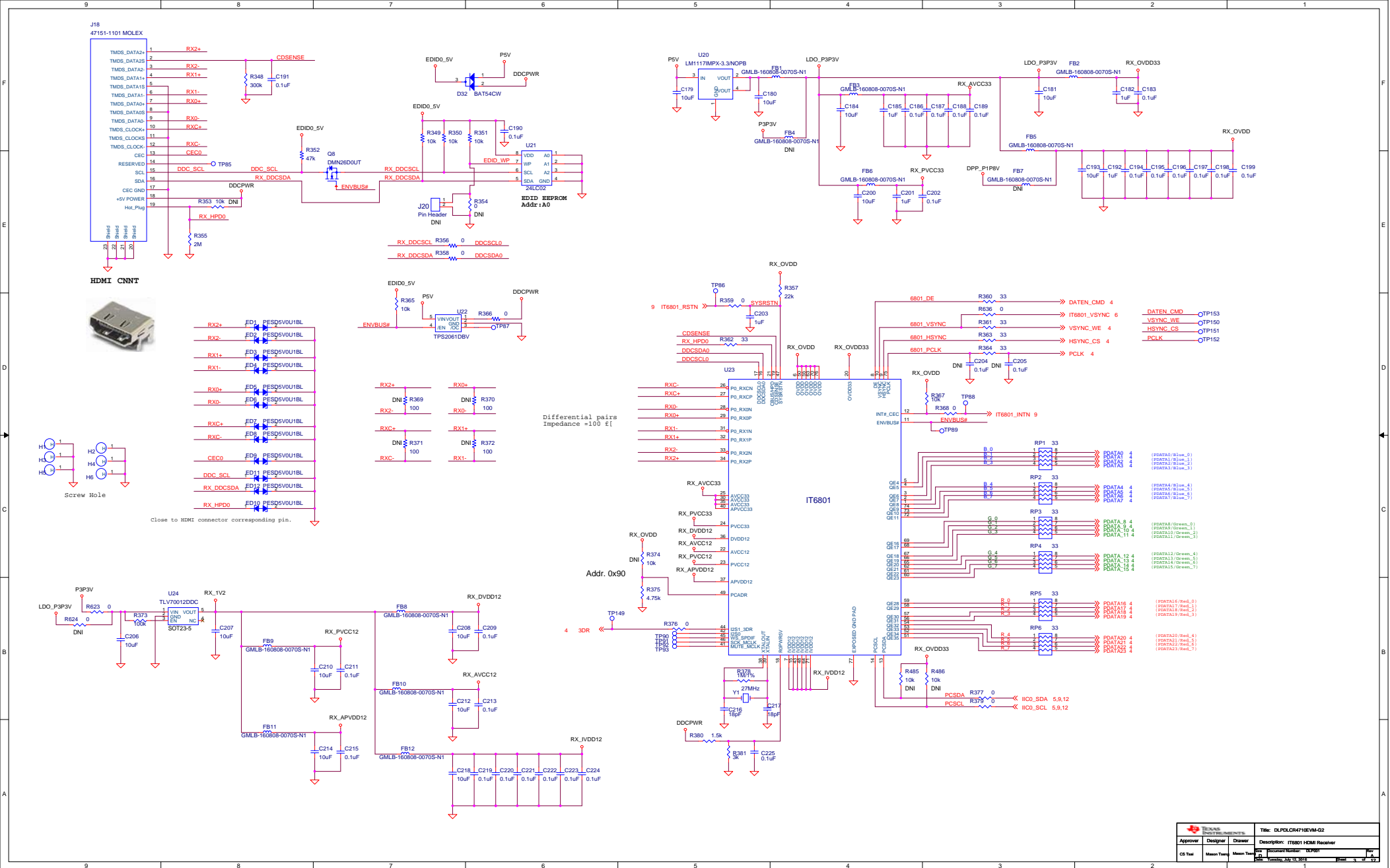
	1	2	3	4	5	6	7	8	9	
A										A
B										B
C										C
D										D
E										E
F										F
	1	2	3	4	5	6	7	8	9	

Version	Release Date	History
E1	2015/12/18	First release.
A	2016/03/18	1. Install R576 with 100K thermistor 2. Install R729 with 10K resistor 3. R471 change from 0R to 215K, R474 change from NC to 47K resistor 4. Uninstall C314, C315 5. Uninstall U64, R796
	2016/03/28	6. Update the silkscreen from Rev E1 to Rev A Change the soldermask from Green to RED
	2016/04/11	7. R622 change from 0R to 33R

 <b>TEXAS INSTRUMENTS</b>			Title: TIDA-01226 DLPDLCR4710EVM-G2		
Approver	Designer	Drawer	Description: History		
CS Tsai	Mason Tseng	Mason Tseng	Size A	Document Number: DLP001	Rev A
Date: Tuesday, July 12, 2016			Sheet 1 of 12		

## Block Diagram



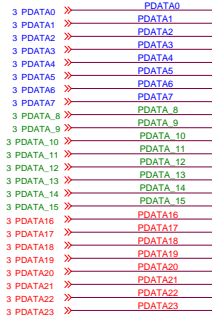


(PDATA0/Blue\_0)  
(PDATA1/Blue\_1)  
(PDATA2/Blue\_2)  
(PDATA3/Blue\_3)  
(PDATA4/Blue\_4)  
(PDATA5/Blue\_5)  
(PDATA6/Blue\_6)  
(PDATA7/Blue\_7)

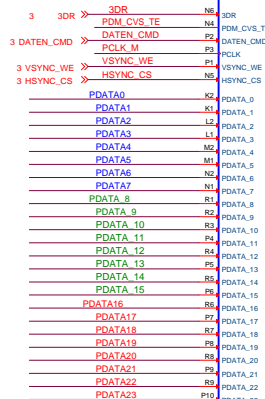
(PDATA8/Green\_0)  
(PDATA9/Green\_1)  
(PDATA10/Green\_2)  
(PDATA11/Green\_3)  
(PDATA12/Green\_4)  
(PDATA13/Green\_5)  
(PDATA14/Green\_6)  
(PDATA15/Green\_7)

(PDATA16/Red\_0)  
(PDATA17/Red\_1)  
(PDATA18/Red\_2)  
(PDATA19/Red\_3)  
(PDATA20/Red\_4)  
(PDATA21/Red\_5)  
(PDATA22/Red\_6)  
(PDATA23/Red\_7)

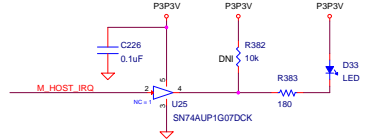
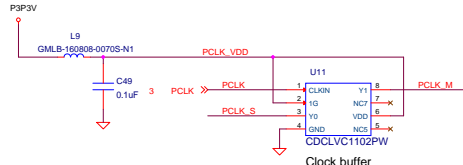
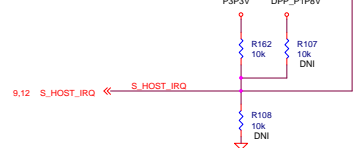
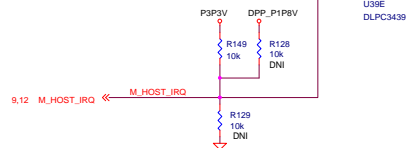
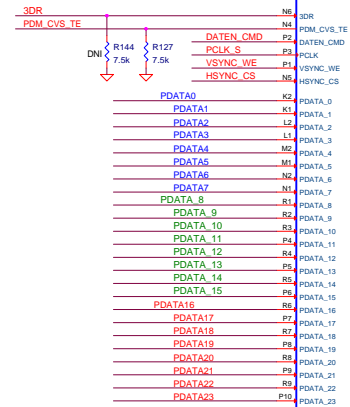
From IT6801  
HDMI Receiver



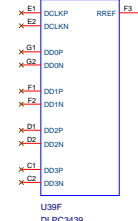
### MASTER PIXEL DATA



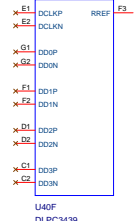
### SLAVE PIXEL DATA



### MASTER DSI

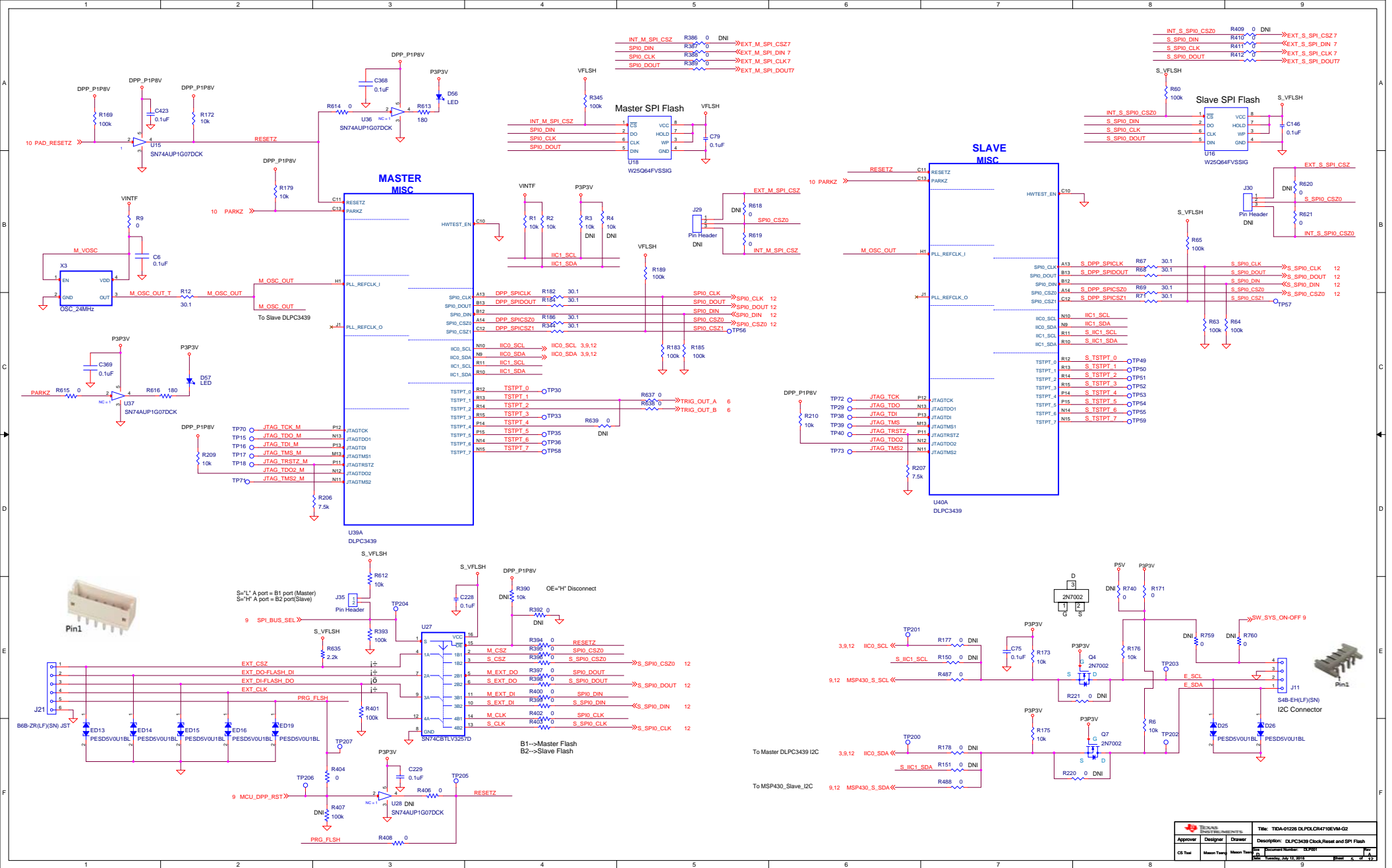


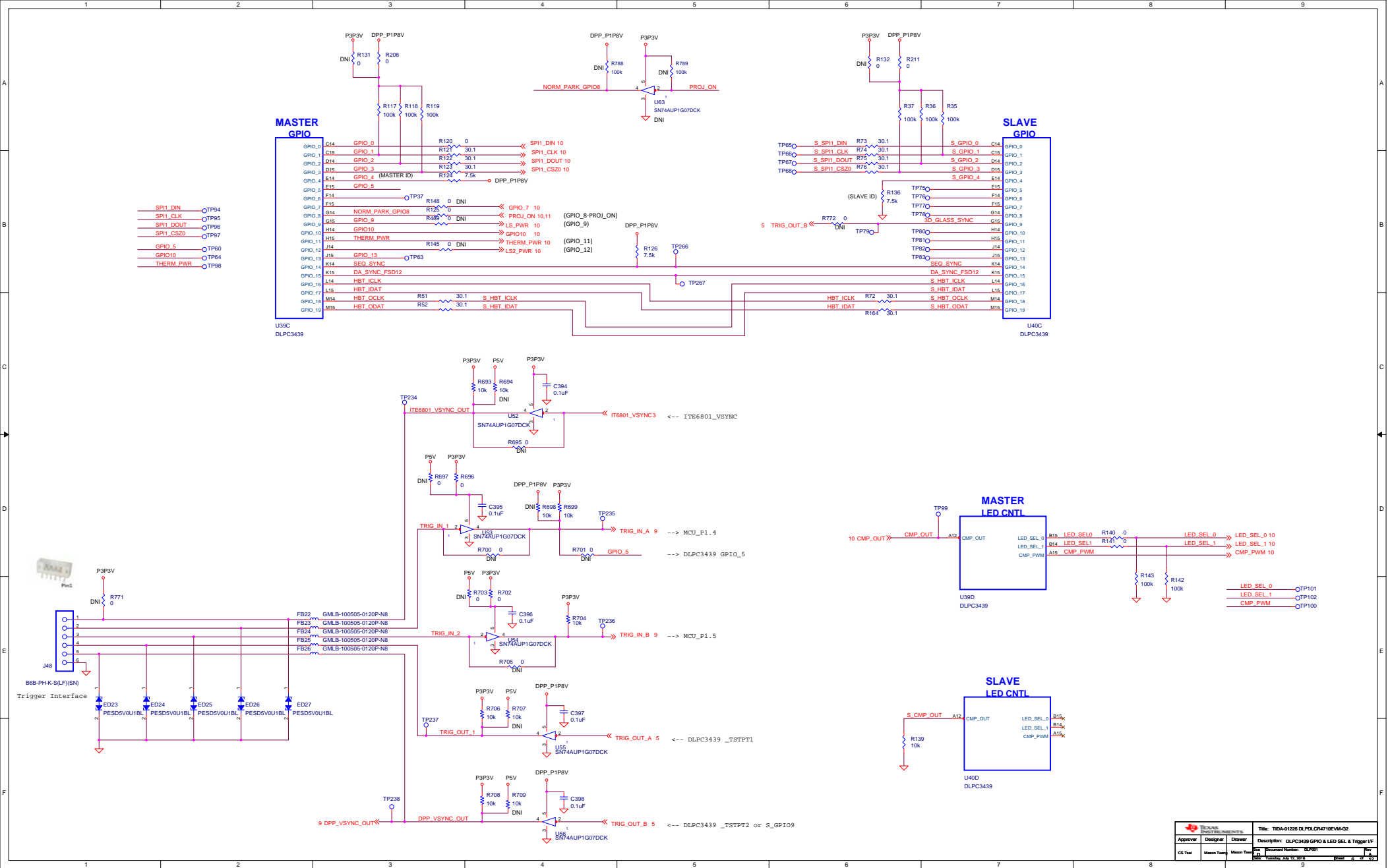
### SLAVE DSI

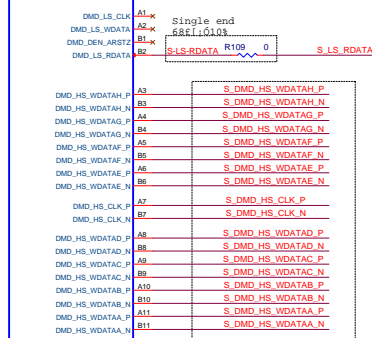


The DSI Port is not used.  
Inputs are left unconnected per ASIC spec recommendation.

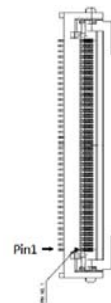
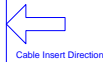
TDA-01226 DLPC3439 Video Input & DSI IF			
Approver	Designer	Drawer	Description
CS Test	Mean Test	Mean Test	Document Number: 01226
CS Test	Mean Test	Mean Test	Document Number: 01226



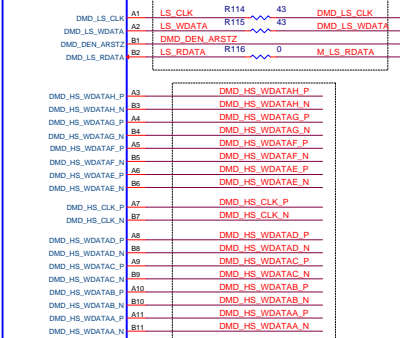




Differential  
100£[;Ö10%

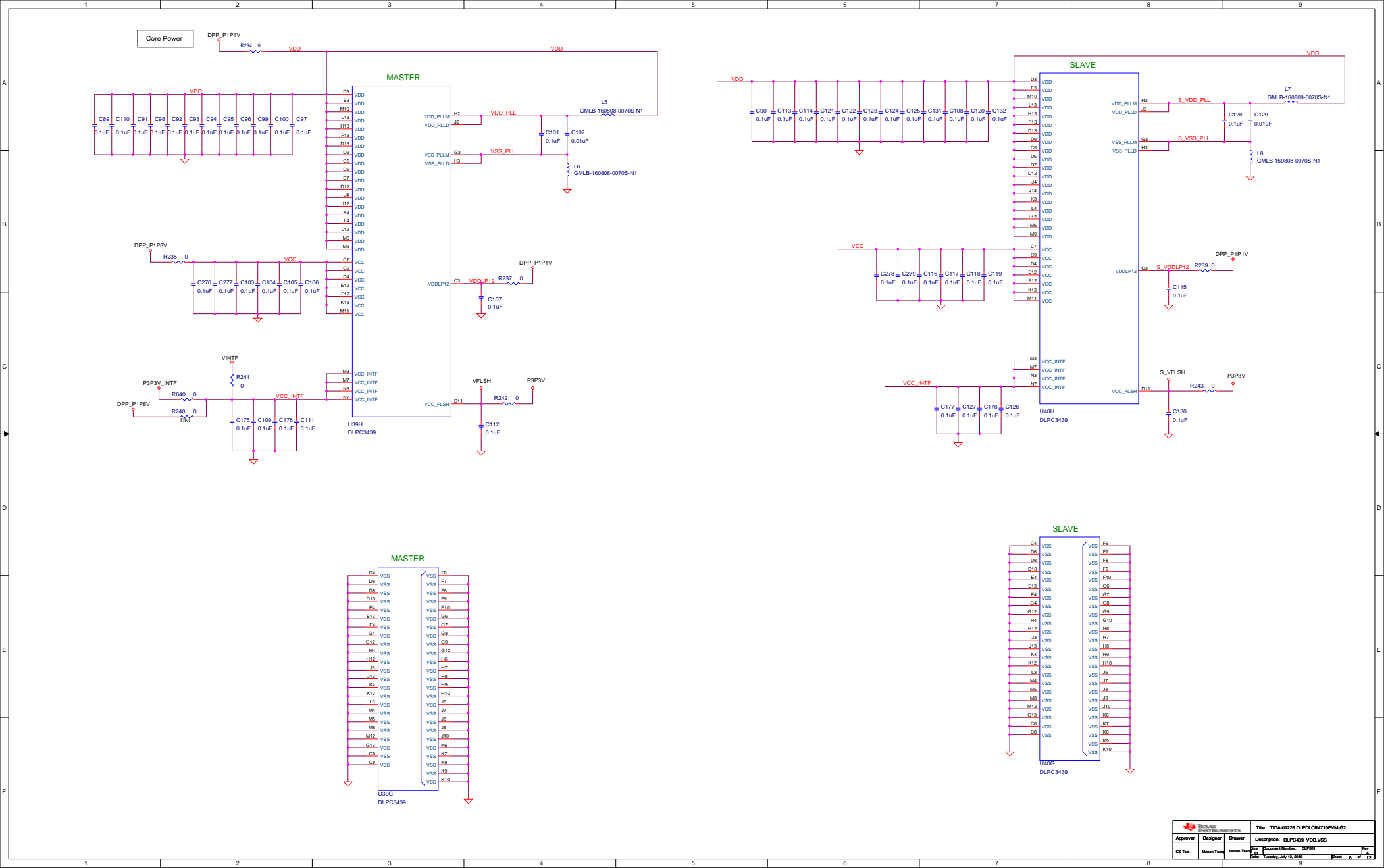


 <b>TEXAS INSTRUMENTS</b>			Title: TIDA-01228 DLPOLCR4710EVM-G2		
Approver	Designer	Drawer	Description: DLPCL3439_DMD Connector		
CS Tail	Mason Tsang	Mason Tsang	<div> <div> <div>Doc Number: DLPOL01</div> <div>Rev A</div> </div> <div> <div> <div>Date: Tuesday, July 19, 2016</div> <div>Sheet 7 of 12</div> </div> </div> </div>		



Differential  
100£[;610%



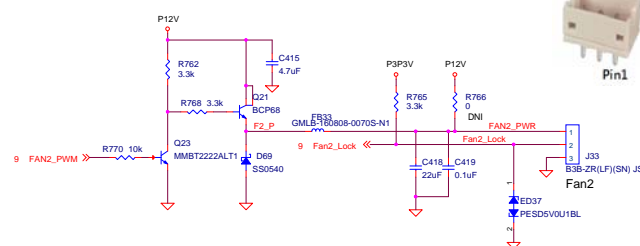
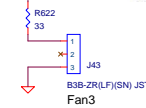
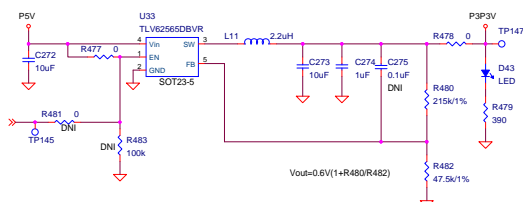
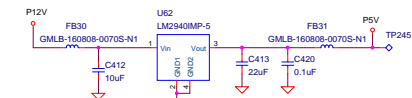
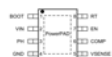
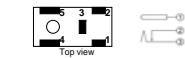



TID: TIDA-01228 DLPC3439 VDD/VSS			
Approver	Designer	Drawer	Description
CS Test	Mean Test	Mean Test	Document Number: 000001
CS Test	Mean Test	Mean Test	Document Number: 000001









 <b>TEXAS INSTRUMENTS</b>			<b>Title: TIDA-01226 DLPOLC84710EVM-G2</b>		
<b>Approver</b>	<b>Designer</b>	<b>Drawer</b>	<b>Description: Power supply &amp; Fan Driver</b>		
CS Tsai	Mason Tseng	Mason Tseng	<b>Date</b>	<b>Document Number</b>	<b>Rev</b>
			Tuesday, July 13, 2016	DLP001	1.0
			<b>Printed</b>	<b>11 of 11</b>	

