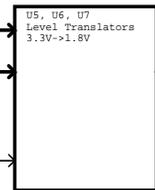
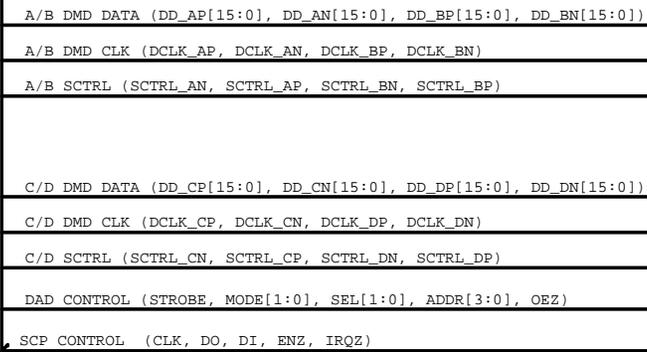
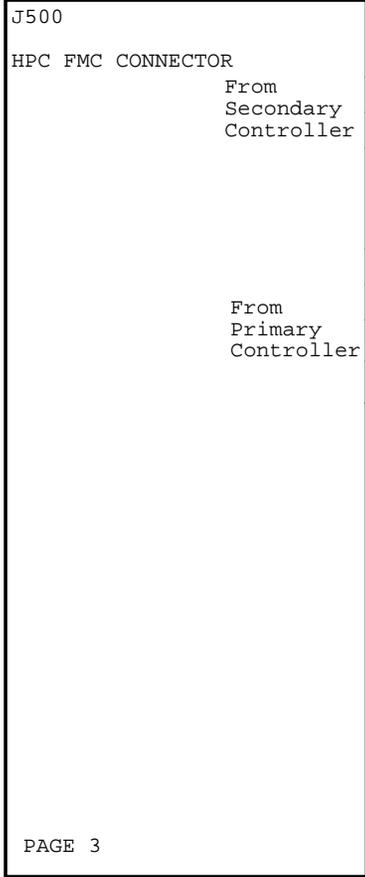


NOTES, UNLESS OTHERWISE SPECIFIED:

1. The netname "DMD_P3P3V" represents connection to the +3.3V digital power plane.
2. The symbol ∇ represents connection to the digital ground plane.
3. A "Z" suffix on a signal name indicates an active low signal.
4. All components with designators "U", "D", "Y" and "Q" are electrostatic discharge sensitive.
5. All resistor values are in ohms, 1/16W and 5% unless otherwise specified.



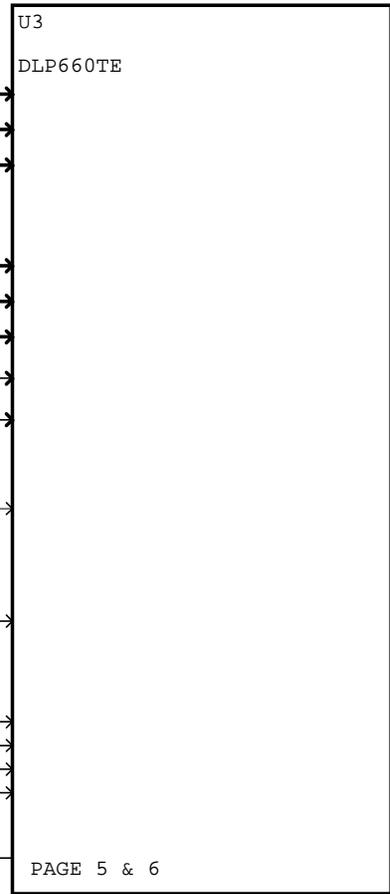
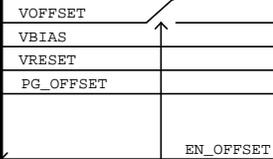
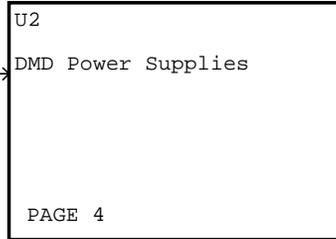
		DWG	Oscar Guiverra	DATE	6/17/2023	TEXAS INSTRUMENTS <small>(C) COPYRIGHT 2023 TEXAS INSTRUMENTS ALL RIGHTS RESERVED</small>	
		ENGR					
		SVST				TITLE	
		PRJ				DLP660TE DMD Board	
		QA				DRAWING NO	
NEXT ASSY	USED ON					D	DLP080
		SW				SCALE	SHEET 1 of 8
							REV B

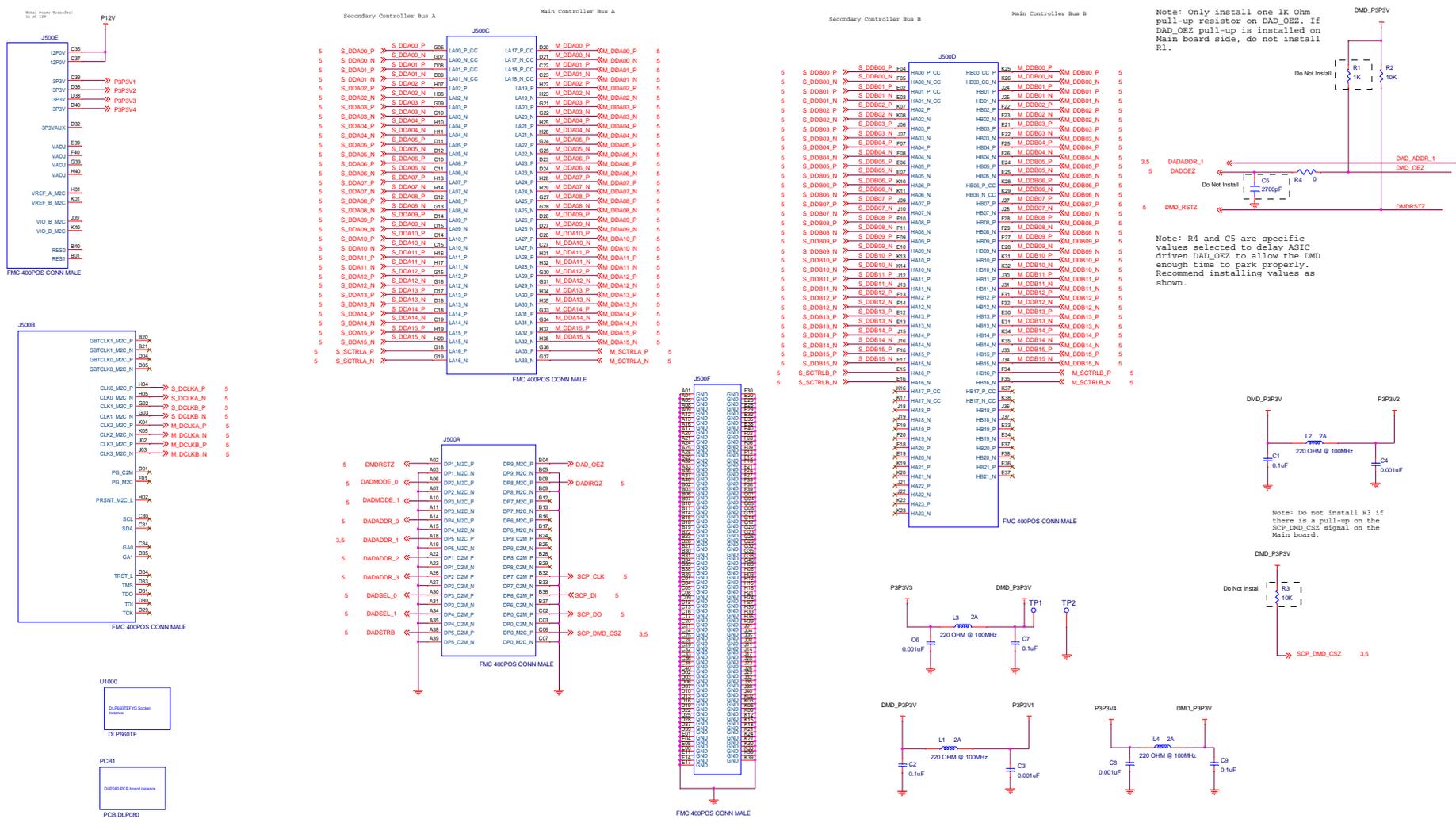


DMD_RSTZ

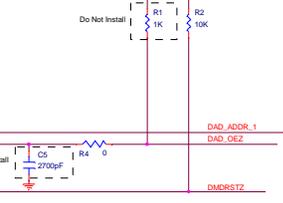


3.3V

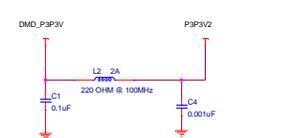




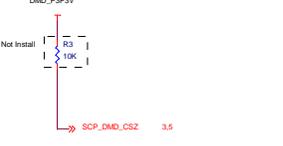
Note: Only install one 1K Ohm pull-up resistor on DAD_OE2. If DAD_OE2 pull-up is installed on Main board side, do not install R1.



Note: R4 and C5 are specific values selected to delay ASIC driven DAD_OE2 to allow the DMD enough time to park properly. Recommend installing values as shown.

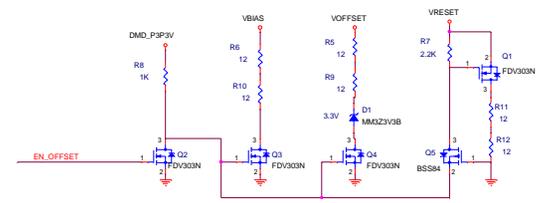
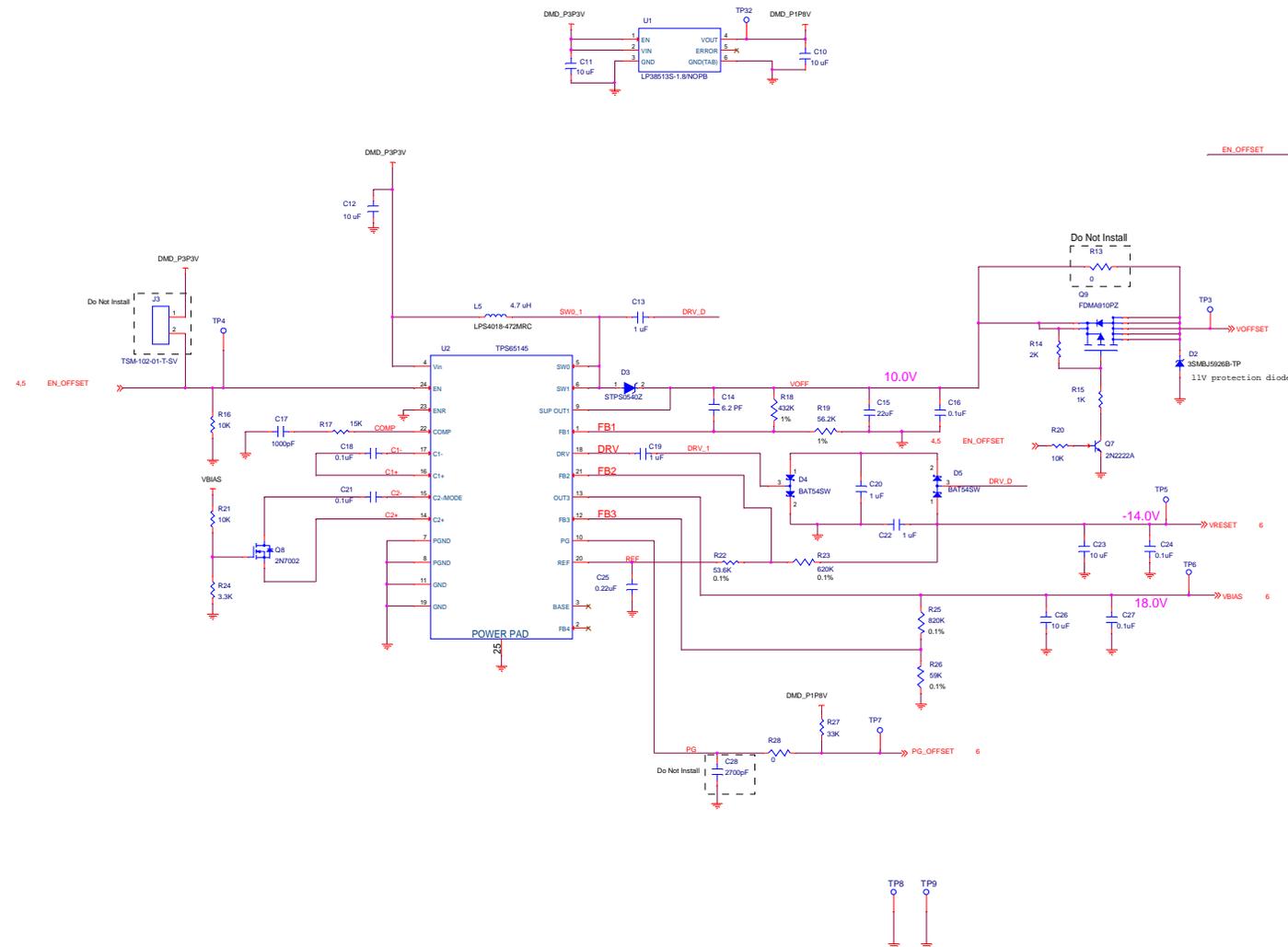


Note: Do not install R3 if there is a pull-up on the SCP_DMD_CS2 signal on the Main board.



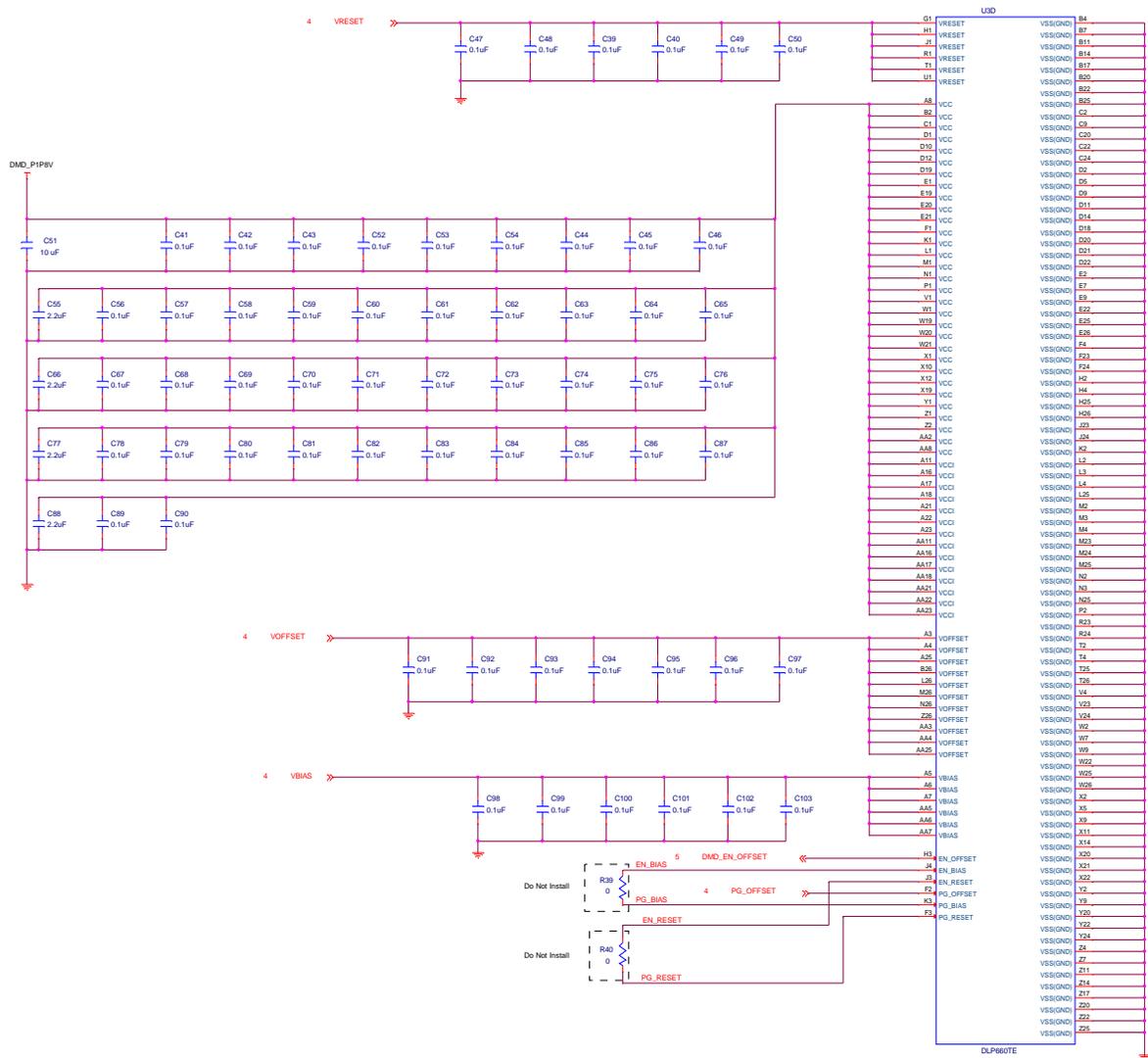
INPUT CONNECTOR

Power Down Circuitry



DMD Power Supplies

TEXAS INSTRUMENTS	DESIGNED BY	DATE	DRAWING NO.	REV. B
	ISSUE DATE	6/17/2022	DLP080	
			SHEET 4 OF 8	



DMD Power/Gnd

Rev A:
Initial Release

Rev B:
Removed J1 & J2 Mustang Flex Interface
Added J500 FMC connector

TEXAS INSTRUMENTS	DWN Oscar Guevara	DATE 6/17/2022	B	DRAWING NO DLP080	REV B
	ISSUE DATE				

IMPORTANT NOTICE AND DISCLAIMER

TI PROVIDES TECHNICAL AND RELIABILITY DATA (INCLUDING DATASHEETS), DESIGN RESOURCES (INCLUDING REFERENCE DESIGNS), APPLICATION OR OTHER DESIGN ADVICE, WEB TOOLS, SAFETY INFORMATION, AND OTHER RESOURCES "AS IS" AND WITH ALL FAULTS, AND DISCLAIMS ALL WARRANTIES, EXPRESS AND IMPLIED, INCLUDING WITHOUT LIMITATION ANY IMPLIED WARRANTIES OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE OR NON-INFRINGEMENT OF THIRD PARTY INTELLECTUAL PROPERTY RIGHTS.

These resources are intended for skilled developers designing with TI products. You are solely responsible for (1) selecting the appropriate TI products for your application, (2) designing, validating and testing your application, and (3) ensuring your application meets applicable standards, and any other safety, security, or other requirements. These resources are subject to change without notice. TI grants you permission to use these resources only for development of an application that uses the TI products described in the resource. Other reproduction and display of these resources is prohibited. No license is granted to any other TI intellectual property right or to any third party intellectual property right. TI disclaims responsibility for, and you will fully indemnify TI and its representatives against, any claims, damages, costs, losses, and liabilities arising out of your use of these resources.

TI's products are provided subject to TI's Terms of Sale (www.ti.com/legal/termsofsale.html) or other applicable terms available either on ti.com or provided in conjunction with such TI products. TI's provision of these resources does not expand or otherwise alter TI's applicable warranties or warranty disclaimers for TI products.

Mailing Address: Texas Instruments, Post Office Box 655303, Dallas, Texas 75265
Copyright © 2023, Texas Instruments Incorporated

TEXAS INSTRUMENTS	DW Oscar Guevara	DATE 6/17/2022	C	DRAWING NO DLP080	REV B
	ISSUE DATE	SCALE			