

Reducing Solution Size with PMBus in Multi-rail Systems



George Lakkas

You can find hardware systems with multiple low-voltage rails that need regulation, sequencing and monitoring in cloud infrastructure equipment like base stations, networking switches, servers and storage, as well as test and measurement applications such as integrated circuit (IC) testers, oscilloscopes and network analyzers.

The Power Management Bus (PMBus) digital interface is a popular interface, which I discussed in another blog post, “[A PMBus primer: common PMBus questions answered.](#)”

TI has a complete PMBus power solution for systems using 48V_{DC} on the front end and 12V_{DC} or another low-voltage DC rail as the intermediate bus; see [Figure 1](#).

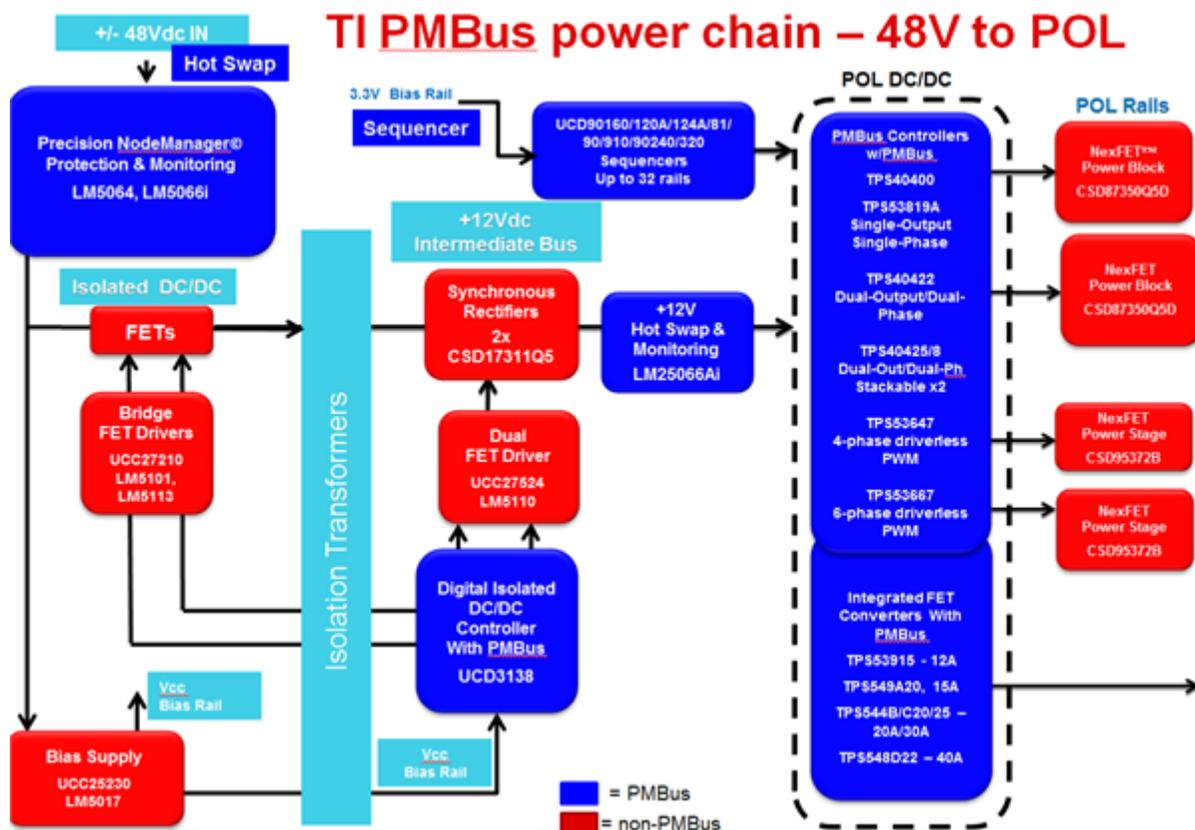


Figure 1. 48V to Point-of-load PMBus Power System

The benefits of a complete PMBus solution in multirail systems are many: ease of use; reduced design time (as a new design can be generated in seconds by reconfiguring the on-chip nonvolatile memory [NVM]); reduction in overall component count and total solution cost; a unified and seamless design and programming method through a single graphical user interface (GUI); much simpler board characterization through PMBus margining; and improved diagnostics through voltage, current, temperature, power and fault monitoring.

Multirail PMBus sequencers/managers such as the 24-rail [UCD90240](#) and the new 32-rail [UCD90320](#) can sequence, margin, monitor and report faults for up to 32 rails using TI’s [Fusion Digital Power™](#) GUI. They can also be stacked in fours for up to 128 rails, managed through the SYNC_CLK pin if needed.

Additionally, the UCD90240 and UCD90320 have true black-box logging that provides detailed information about all rails, General Purpose Input (GPI), and General Purpose Output (GPO) status on the first fault, as shown in [Figure 2](#).

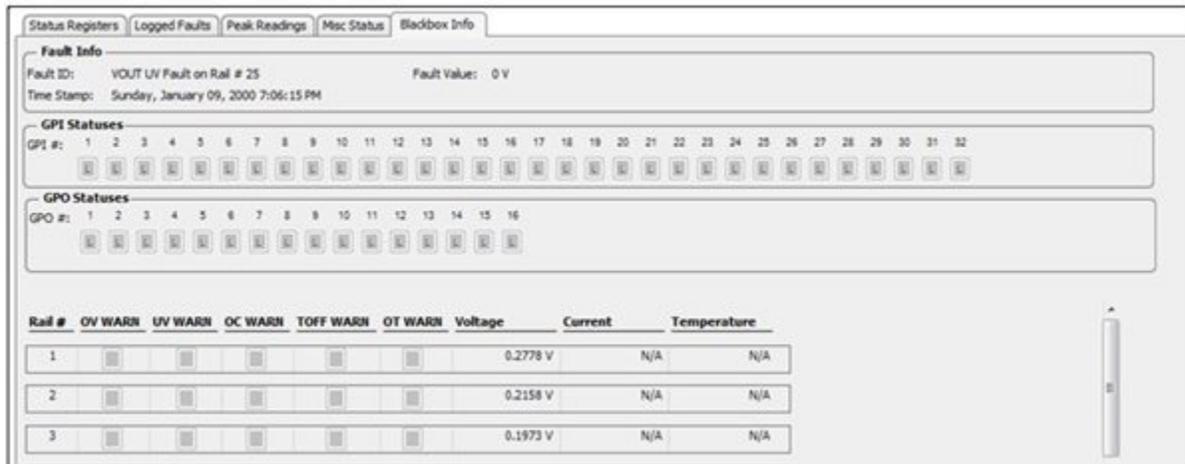


Figure 2. UCD90240/UCD90320 Black-box Logging

UCD90xxx PMBus sequencers/managers work with analog and PMBus point-of-load voltage regulators to provide a PMBus management and voltage-regulation solution in multirail systems, which almost always have one or more high-current application-specific integrated circuits (ASICs), processors and/or field-programmable gate arrays (FPGAs).

The loads require precise multiphase step-down conversion from the 12V intermediate bus to their respective rails. Typical load currents range from 50A to 200A, and the multiphase converter may require up to six phases to distribute power and thermals effectively and reduce the size and count of the inductor and output capacitors through phase interleaving.

One such six-phase converter is the [TPS53667](#).

How does a multirail UCD90xxx PMBus sequencer come together with multiple PMBus voltage regulators in a design? The [TI Designs PMBus Power System for Enterprise Ethernet Switches Reference Design](#) is a good example, as shown in [Figure 3](#).

The design employs a PMBus sequencer, a PMBus hot-swap IC (for the input current) and eight analog and PMBus voltage regulators, including a four-phase PMBus buck controller and a double-data-rate (DDR) termination (Vtt) switcher. The Fusion Digital Power GUI provides a graphical representation of the power tree and offers the capability to program the main parameters on the top half, while monitoring key parameters such as voltage, current, temperature and power on the bottom half.

If you are designing multirail systems with high-current processors, ASICs and/or FPGAs and want to simplify your design and characterization, reduce development time, and increase your system's diagnostic capability, consider TI's PMBus power solutions.

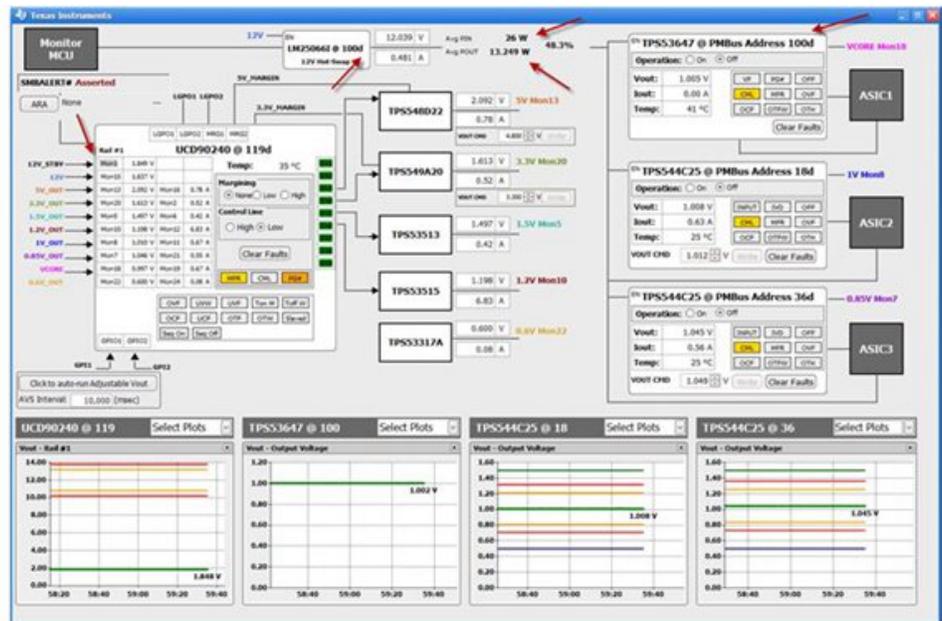


Figure 3. The Reference Design's PMBus Board and Fusion Digital Power GUI

Additional Resources

- Watch the video, [“Unboxing a 24-A, 6-Phase PMBus Buck Converter Design.”](#)
- See the [digital power supply portal](#) for more information about TI PMBus power solutions.

IMPORTANT NOTICE AND DISCLAIMER

TI PROVIDES TECHNICAL AND RELIABILITY DATA (INCLUDING DATA SHEETS), DESIGN RESOURCES (INCLUDING REFERENCE DESIGNS), APPLICATION OR OTHER DESIGN ADVICE, WEB TOOLS, SAFETY INFORMATION, AND OTHER RESOURCES "AS IS" AND WITH ALL FAULTS, AND DISCLAIMS ALL WARRANTIES, EXPRESS AND IMPLIED, INCLUDING WITHOUT LIMITATION ANY IMPLIED WARRANTIES OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE OR NON-INFRINGEMENT OF THIRD PARTY INTELLECTUAL PROPERTY RIGHTS.

These resources are intended for skilled developers designing with TI products. You are solely responsible for (1) selecting the appropriate TI products for your application, (2) designing, validating and testing your application, and (3) ensuring your application meets applicable standards, and any other safety, security, regulatory or other requirements.

These resources are subject to change without notice. TI grants you permission to use these resources only for development of an application that uses the TI products described in the resource. Other reproduction and display of these resources is prohibited. No license is granted to any other TI intellectual property right or to any third party intellectual property right. TI disclaims responsibility for, and you will fully indemnify TI and its representatives against, any claims, damages, costs, losses, and liabilities arising out of your use of these resources.

TI's products are provided subject to [TI's Terms of Sale](#) or other applicable terms available either on [ti.com](https://www.ti.com) or provided in conjunction with such TI products. TI's provision of these resources does not expand or otherwise alter TI's applicable warranties or warranty disclaimers for TI products.

TI objects to and rejects any additional or different terms you may have proposed.

Mailing Address: Texas Instruments, Post Office Box 655303, Dallas, Texas 75265
Copyright © 2023, Texas Instruments Incorporated