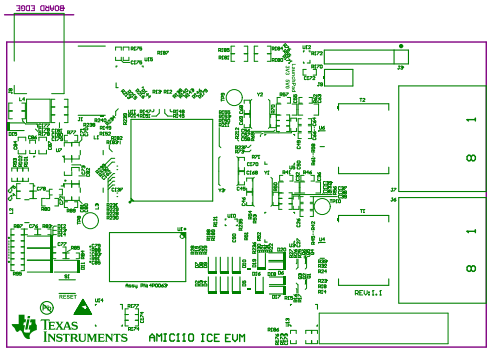
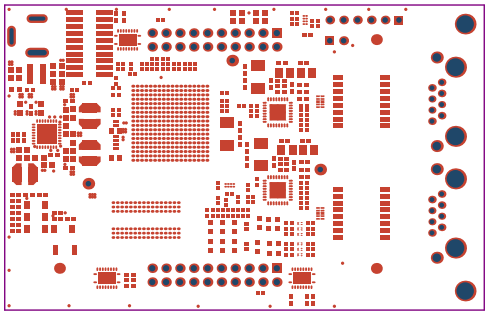


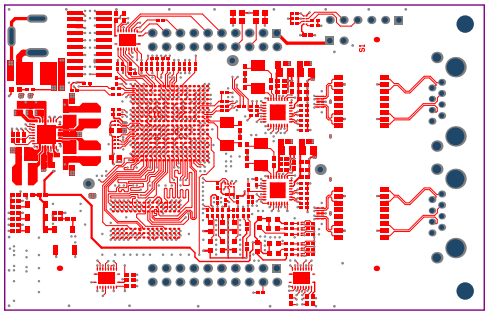
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: TMDXICE110	REV: 1.1	SVN REV: Not In VersionControl	
LAYER NAME = Top Overlay	TID #: NA			
PLOT NAME =TSLK	GENERATED : 12/26/2016 12:10:49 PM			TEXAS INSTRUMENTS

BOARD EDGE



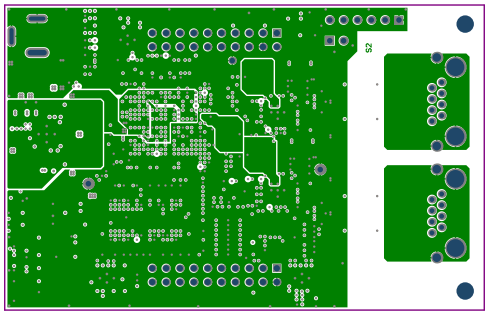
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: TMDXICE110	REV: 1.1	SVN REV: Not In VersionControl
LAYER NAME = Top Solder	TID #: NA		
PLOT NAME =TMSK	GENERATED : 12/26/2016 12:10:50 PM		TEXAS INSTRUMENTS

BOARD EDGE



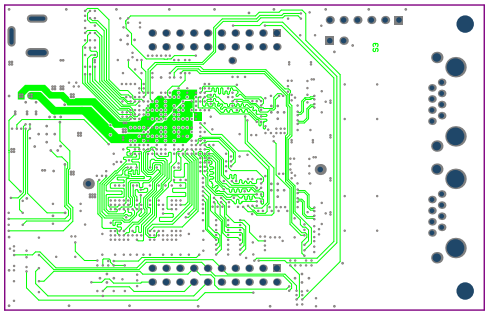
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: TMDXICE110	REV: 1.1	SVN REV: Not In VersionControl	
LAYER NAME = TopLayer - SI (.GTL)	TID #: NA			
PLOT NAME =TOP	GENERATED : 12/26/2016 12:10:50 PM			TEXAS INSTRUMENTS

BOARD EDGE



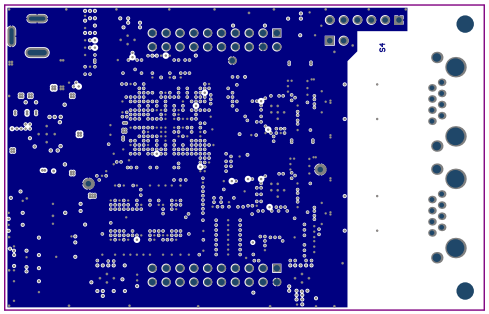
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: TMDXICE110	REV: 1.1	SVN REV: Not In VersionControl	
LAYER NAME = MidLayer1 - S2 (.G1)	TID #: NA			
PLOT NAME =GND1	GENERATED : 12/26/2016 12:10:51 PM			TEXAS INSTRUMENTS

BOARD EDGE



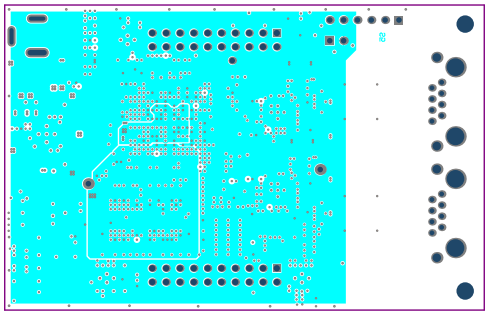
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: TMDXICE110	REV: 1.1	SVN REV: Not In VersionControl	
LAYER NAME = MidLayer2 - S3 (.G2)	TID #: NA			
PLOT NAME =INT1	GENERATED : 12/26/2016 12:10:51 PM			TEXAS INSTRUMENTS

BOARD EDGE



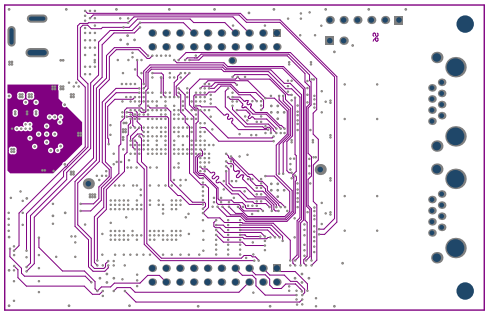
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: TMDXICE110	REV: 1.1	SVN REV: Not In VersionControl	
LAYER NAME = MidLayer3 - S4 (.G3)	TID #: NA			
PLOT NAME =GND2	GENERATED : 12/26/2016 12:10:51 PM			TEXAS INSTRUMENTS

BOARD EDGE



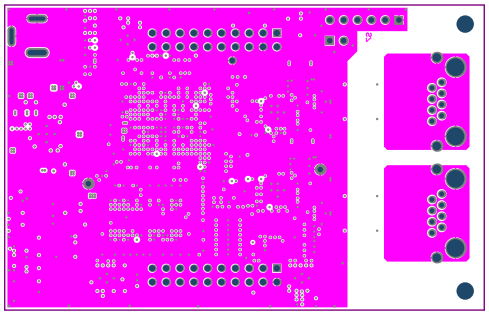
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: TMDXICE110	REV: 1.1	SVN REV: Not In VersionControl	
LAYER NAME = MidLayer4 - S5 (.G4)	TID #: NA			
PLOT NAME =PWR1	GENERATED : 12/26/2016 12:10:51 PM			TEXAS INSTRUMENTS

BOARD EDGE



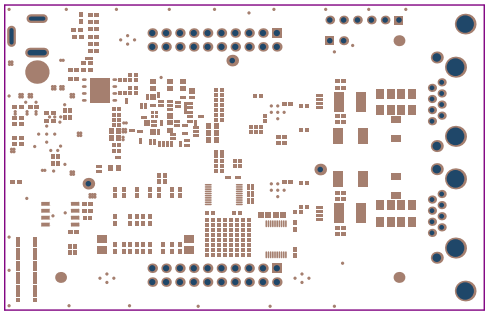
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: TMDXICE110	REV: 1.1	SVN REV: Not In VersionControl	
LAYER NAME = MidLayer5 - S6 (.G5)	TID #: NA			
PLOT NAME =INT2	GENERATED : 12/26/2016 12:10:51 PM			TEXAS INSTRUMENTS

BOARD EDGE



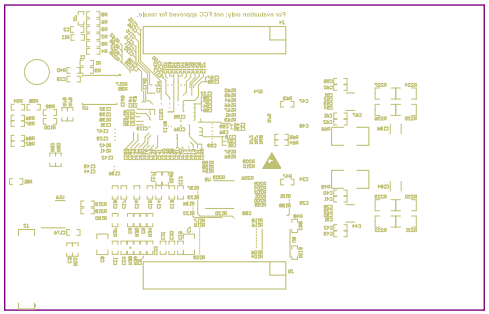
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: TMDXICE110	REV: 1.1	SVN REV: Not In VersionControl	
LAYER NAME = MidLayer6 - S7 (.G6)	TID #: NA			
PLOT NAME =GND3	GENERATED : 12/26/2016 12:10:51 PM			TEXAS INSTRUMENTS

BOARD EDGE



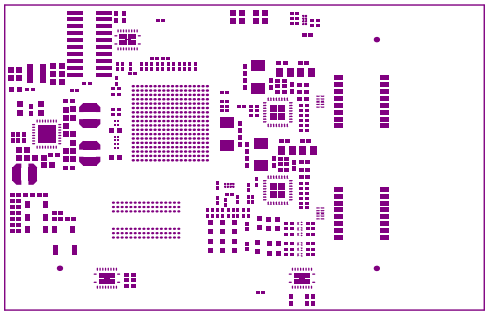
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: TMDXICE110	REV: 1.1	SVN REV: Not In VersionControl
LAYER NAME = Bottom Solder	TID #: NA		
PLOT NAME =BMSK	GENERATED : 12/26/2016 12:10:52 PM		TEXAS INSTRUMENTS

BOARD EDGE



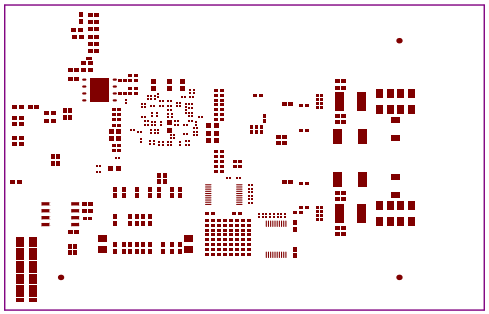
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: TMDXICE110	REV: 1.1	SVN REV: Not In VersionControl	
LAYER NAME = Bottom Overlay	TID #: NA			
PLOT NAME =BSLK	GENERATED : 12/26/2016 12:10:52 PM			TEXAS INSTRUMENTS

BOARD EDGE



ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: TMDXICE110	REV: 1.1	SVN REV: Not In VersionControl
LAYER NAME = Top Paste	TID #: NA		
PLOT NAME =TPST	GENERATED : 12/26/2016 12:10:53 PM		TEXAS INSTRUMENTS

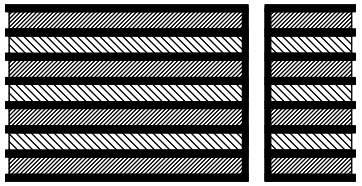
BOARD EDGE



ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: TMDXICE110	REV: 1.1	SVN REV: Not In VersionControl
LAYER NAME = Bottom Paste	TID #: NA		
PLOT NAME =BPST	GENERATED : 12/26/2016 12:10:53 PM		TEXAS INSTRUMENTS

LAYER STACKUP

	LAYER NAME	FINISHED Cu	DIELECTRIC THICKNESS [INCHES]
	PRIMARY SIDE SILKSCREEN		
	PRIMARY SIDE SOLDERMASK		
L01	PRIMARY SIDE	1oz.	0.0037
L02	GROUND–PLANE–1	1oz.	0.0050
L03	INNER–SIGNAL–1	1oz.	0.0120
L04	GROUND–PLANE–2	1oz.	0.0060
L05	POWER–PLANE	1oz.	0.0120
L06	INNER–SIGNAL–2	1oz.	0.0050
L07	GROUND–PLANE–3	1oz.	0.0037
L08	SECONDARY SIDE	1oz.	
	SECONDARY SIDE SOLDERMASK		
	SECONDARY SIDE SILKSCREEN		



****IMPEDANCE REQUIREMENTS SHALL BE FOLLOWED :
50 OHMS FOR 4.9210MIL, 100 OHMS DIFFERENTIAL FOR 3.976/8.268MIL
& 90 OHMS DIFFERENTIAL FOR 5.118/8.268MIL
PANEL FIDUCIALS SHALL BE ADDED DURING PANELIZATION

Symbol	Quantity	Finished Hole Size	Plated	Hole Type	Drill Layer Pair	Tolerance
	2	3,200mm (125,98mil)	NPTH	Round	TopLayer - S1 (.GTL) - BottomLayer - S8 (.GBL)	
	873	0,200mm (7,87mil)	PTH	Round	TopLayer - S1 (.GTL) - BottomLayer - S8 (.GBL)	
	16	0,890mm (35,04mil)	PTH	Round	TopLayer - S1 (.GTL) - BottomLayer - S8 (.GBL)	
	11	1,016mm (40,00mil)	PTH	Round	TopLayer - S1 (.GTL) - BottomLayer - S8 (.GBL)	
	40	1,150mm (45,28mil)	PTH	Round	TopLayer - S1 (.GTL) - BottomLayer - S8 (.GBL)	
	4	1,570mm (61,81mil)	PTH	Round	TopLayer - S1 (.GTL) - BottomLayer - S8 (.GBL)	
	4	3,251mm (128,00mil)	PTH	Round	TopLayer - S1 (.GTL) - BottomLayer - S8 (.GBL)	
	2	0,760mm (29,92mil)	PTH	Slot	TopLayer - S1 (.GTL) - BottomLayer - S8 (.GBL)	
	1	1,010mm (39,76mil)	PTH	Slot	TopLayer - S1 (.GTL) - BottomLayer - S8 (.GBL)	
	953 Total					

Slot definitions : Routed Path Length = Calculated from tool start centre position to tool end centre position, Hole Length = Routed Path Length + Tool Size = Slot length as defined in the PCB layout

DESIGN INFORMATION

MIN. TRACK WIDTH: 3.967 MIL
MIN. CLEARANCE: 3.937 MIL
MIN. VIA PAD SIZE: 17.716 MIL
MINIMUM ANNULAR RING 0.05mm (2MIL) EXTERNAL
PER IPC–D–275 CLASS 2 LEVEL C
REGISTRATION TOLERANCES: PTH +/– 2 MIL, HOLES +/– 3 MIL
HOLE SIZE TOLERANCE (UNLESS OTHERWISE SPECIFIED): +/– 3 MIL

MATERIAL:
[ ] FR–408 [X] FR–4 High Tg [ ] OTHER
THICKNESS: [X] 62 MIL (1.6mm) +/–10% [ ] OTHER
TOLERANCE: [X] ANSI IPC–6012 TYPE 3 CLASS 2
[ ] OTHER +/–
BOW & TWIST: [X] ANSI IPC–6012 TYPE 3 CLASS 2
[ ] OTHER +/–

DRILLING:
REFERENCE: [X] AS SHOWN [X] NC_DRILL FILES
PTH MIN COPPER THICKNESS: [ ] 1MIL [X] OTHER IPC–6012 CLASS 2

BOARD FINISH:
SILKSCREEN: [X] TOP [X] BOTTOM
SILKSCREEN COLOR: [X] WHITE [ ] OTHER
SOLDER RESIST COLOR: [ ] GREEN [X] OTHER BLUE
[X] MATTE [ ] SEMI–GLOSS
SURFACE FINISH: [X] IMMERSION GOLD (ENIG) [ ] ENEPIG
[ ] IMM. TIN/SILVER OR EQUIV [ ] OTHER
ARRAY/PANEL: [ ] CUT AND TRIM PER M1 BOARD OUTLINE
[ ] N.C. ROUTE [X] V. SCORE

TEAR DROPS SHALL BE ADDED FOR ALL THE VIAS IN BOTH THE INTERNAL AND EXTERNAL LAYERS.

CERTIFICATION: MATERIALS AND WORKMANSHIP FOR ALL PCBS TO MEET OR EXCEED THE REQUIREMENTS OF:
[X] ANSI IPC–A–600F CLASS –> [ ] 1 [X] 2 [ ] 3
[X] RoHS [ ] OTHER PER ORDER
ALL BOARDS MUST MEET OR EXCEED UL94–V0 REQUIREMENTS.
PCB MUST BEAR THE UL94V–0 UL REGISTERED MATERIAL ID NUMBER

ADDITIONAL REQUIREMENTS:
MICROSECTION: [ ] YES
BARE BOARD ELEC. TEST: [ ] NONE [X] REQUIRED [ ] PER ORDER

TEXAS INSTRUMENTS

PROJECT TITLE: AMIC110 ICE EVM
DESIGNED FOR: TEXAS INSTRUMENTS
FILE NAME: MS_TI_AMIC110_ICE_EVM_BRD_REV1.1.PcbDoc

ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: TMDXICE110	REV: 1.1	SVN REV: Not In VersionControl
LAYER NAME = [illegible]ensions	TID #: NA		
PLOT NAME =FAB	GENERATED : 12/26/2016 12:10:54 PM	TEXAS INSTRUMENTS	

ENGINEER:	LAYOUT BY:
.Ajit M.B.	.Zubair A.
SCALE: 1.00	ALTIUM DESIGNER VERSION: 16.1.7.188