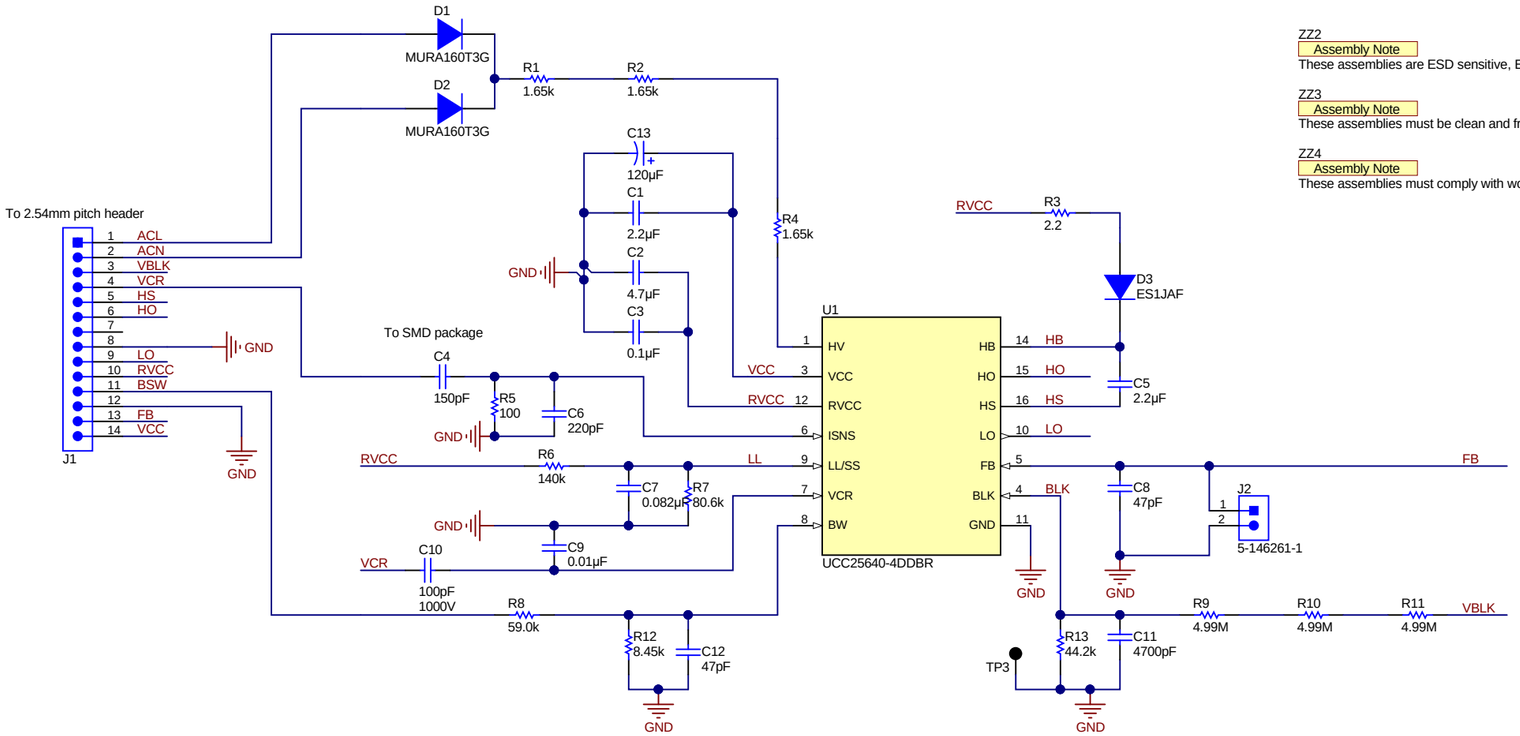
# Note: PMP22088 schematic is built on PMP40281 Rev1.0 PCB.




PCB
LOGO
Pb-Free Symbol

ZZ1
Label Assembly Note
This Assembly Note is for PCB labels only

ZZ2
Assembly Note
These assemblies are ESD sensitive, ESD precautions shall be observed.

ZZ3
Assembly Note
These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.

ZZ4
Assembly Note
These assemblies must comply with workmanship standards IPC-A-610 Class 2, unless otherwise specified.

ACL: AC Line input. Combined with ACN, they are used to detect the AC voltage and perform X cap discharging
ACN: AC Neutral input
VBLK: output voltage of PFC stage. 400VDC
HO: to Gate of high side FET
HS: to intermediate node of half bridge, return path of HO. High voltage switching node
VCR: tied to resonant capacitor node, sense the resonant capacitor voltage and resonant current (?)

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LO: to Gate of low side FET
BSW: to sense the bias winding voltage for output OVP, connect to the winding before rectifier diode. Switching node
VCC: bias power from bias winding, after the rectifier diode. 12VDC
GND: ground plane of power board (two pins for shielding BSW and LO)
RVCC: to VCC of PFC controller. It is used to supply a DC voltage to PFC controller

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FB: optocoupler output for output feedback. The current is injected into part directly

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|---|---|---|---|
| Orderable: | | Designed for: Public Release | Mod. Date: 6/11/2019 |
| TID #: N/A | | Project Title: UCC25640x Daughter Card | |
| Number: PMP22088 | Rev: 1.0 | Sheet Title: | |
| SVN Rev: Version control disabled | | Assembly Variant: 001 | Sheet: 1 of 1 |
| Drawn By: | | File: PMP22088_Main.SchDoc | Size: B |
| Engineer: Sheng-Yang Yu | | Contact: http://www.ti.com/support | |

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