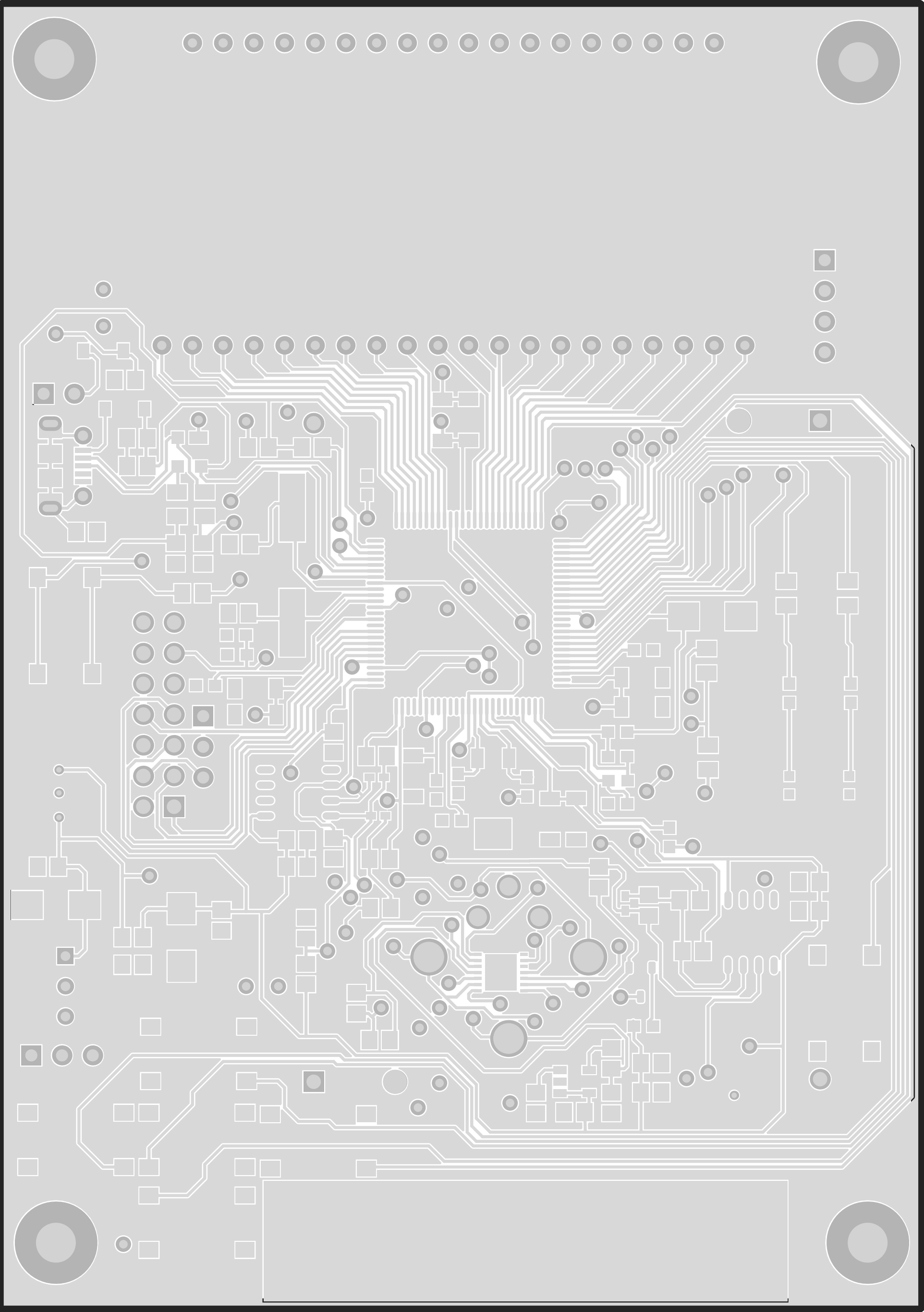


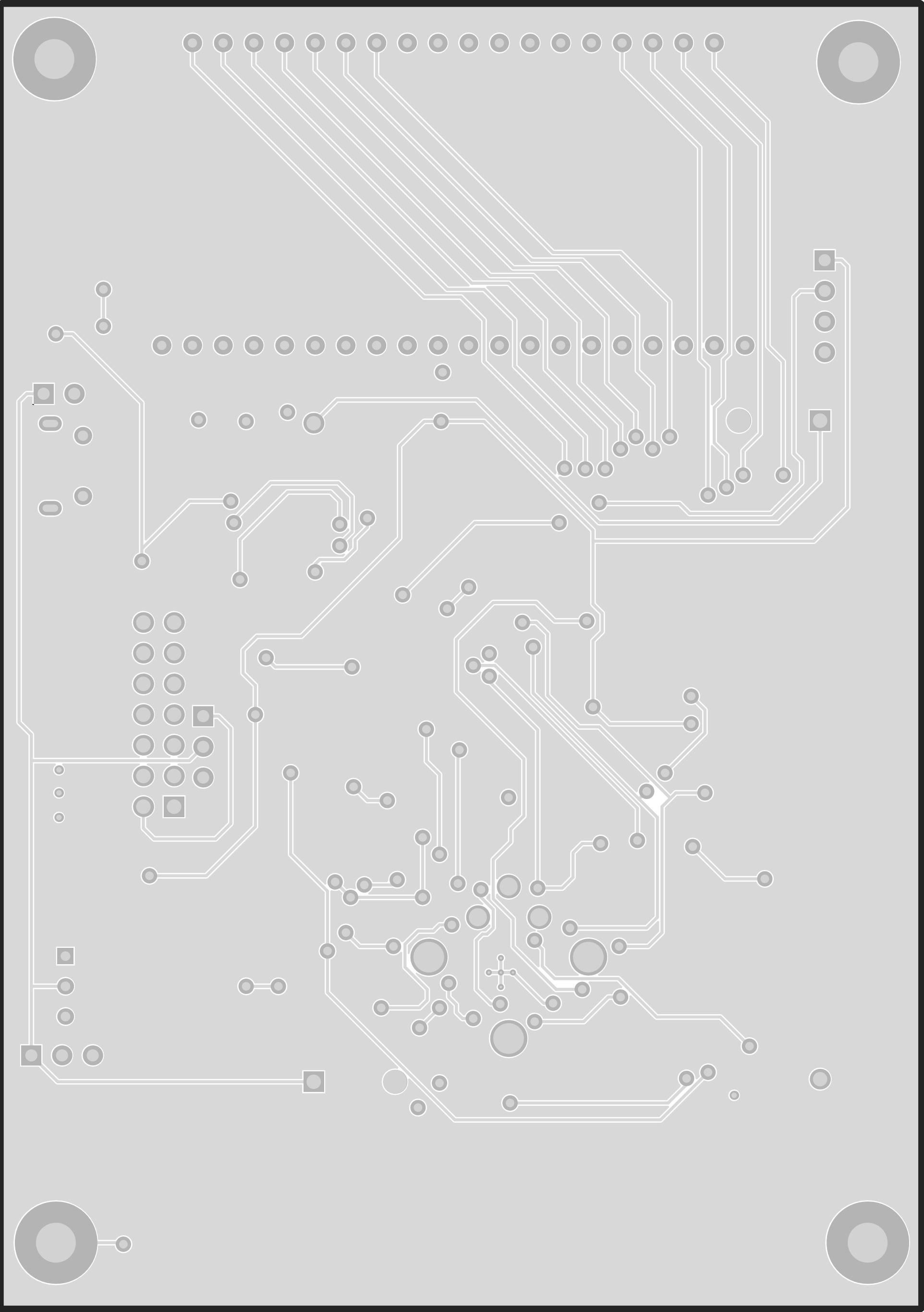
M156amp 0011-55 Top



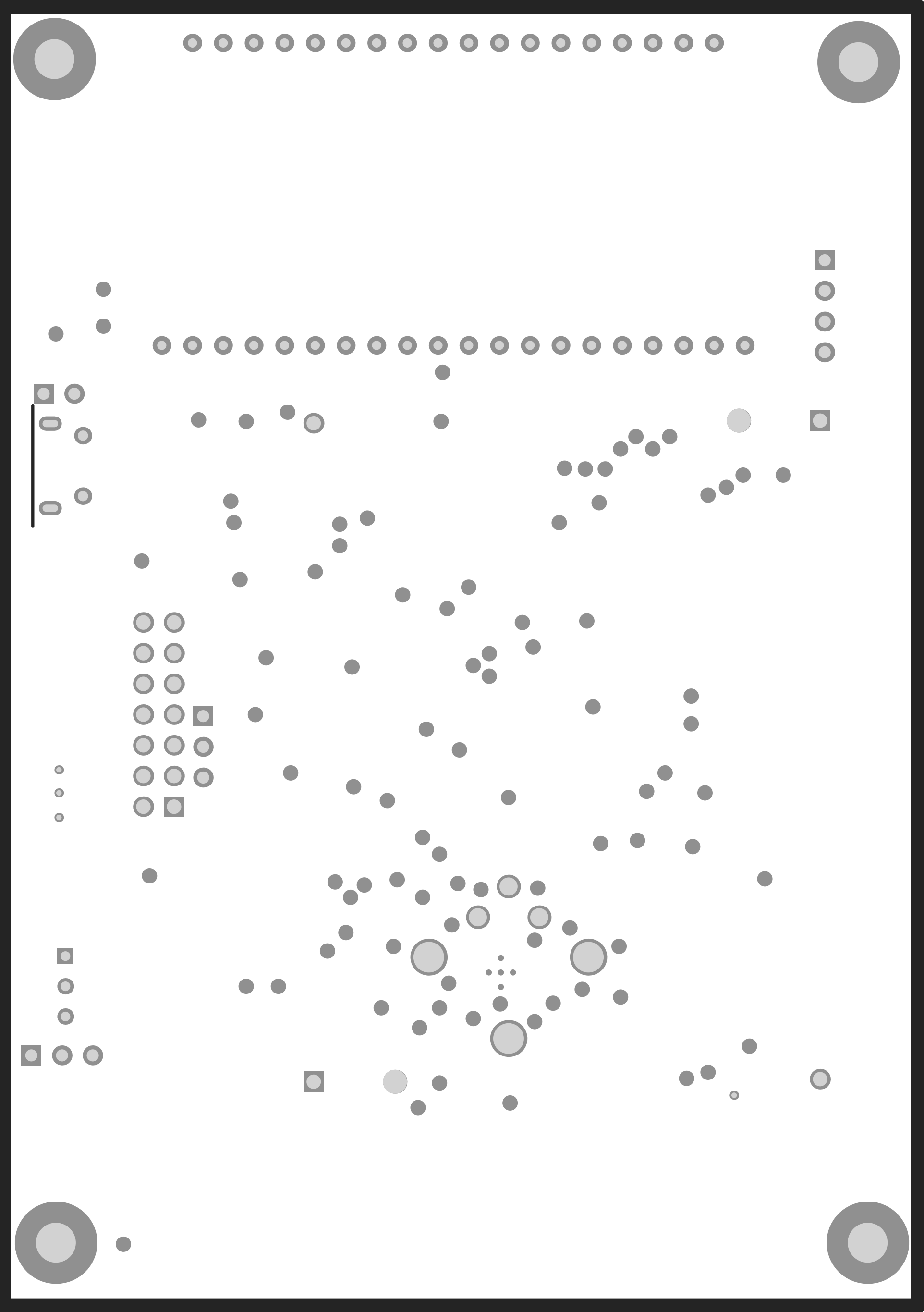
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #:	REV: 1.0	SVN REV: Not In VersionControl
LAYER NAME = Top Solder Outline	TID #: TIDM-1CHP-DTECT-CO		
PLOT NAME = Top Solder Mask	GENERATED : 5/6/2016 10:28:01 AM		TEXAS INSTRUMENTS



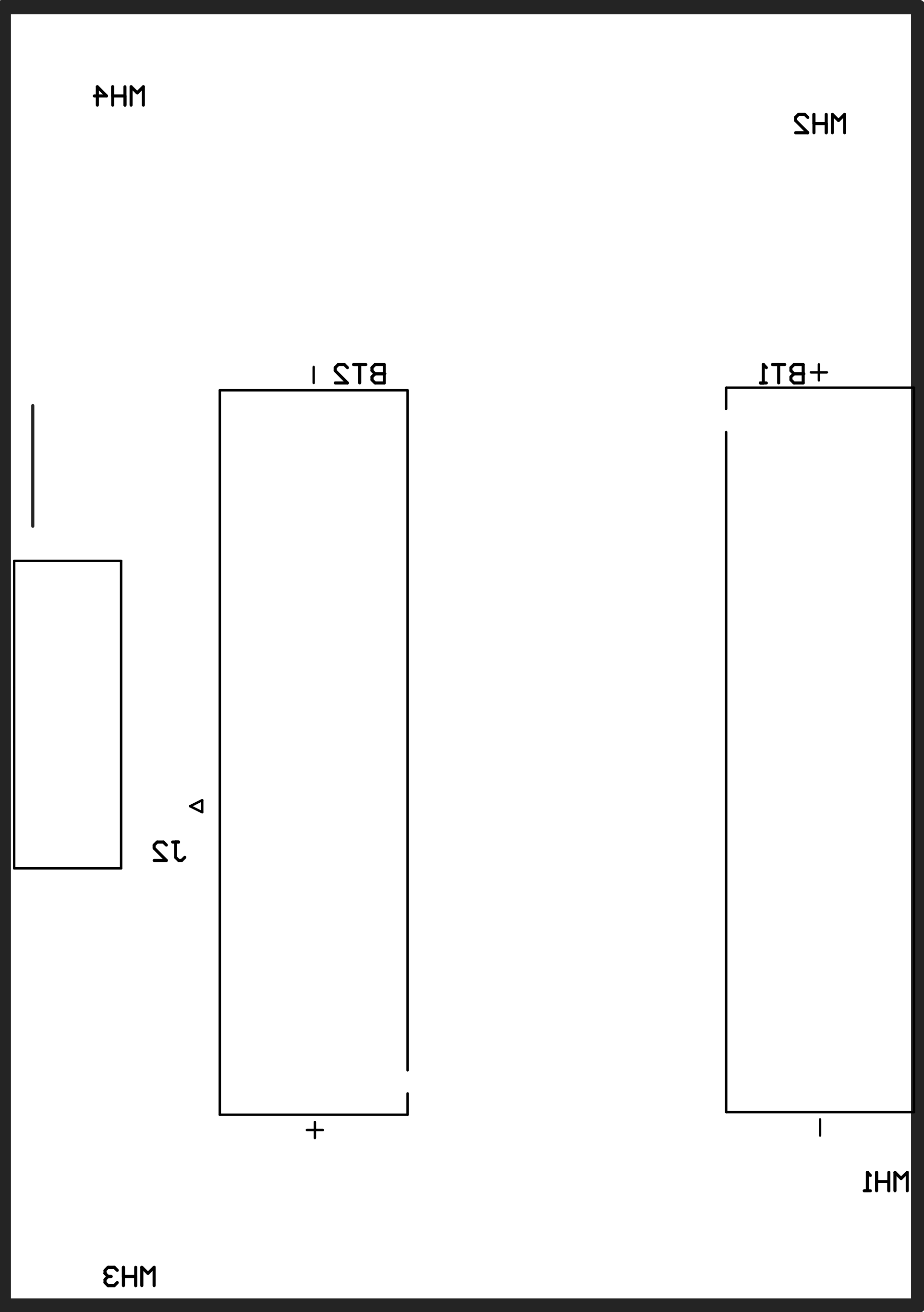
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #:	REV: 1.0	SVN REV: Not In VersionControl
LAYER NAME = M1Board Outline	TID #: TIDM-1CHP-DTECT-CO		
PLOT NAME = Top Layer	GENERATED : 5/6/2016 10:28:04 AM		TEXAS INSTRUMENTS



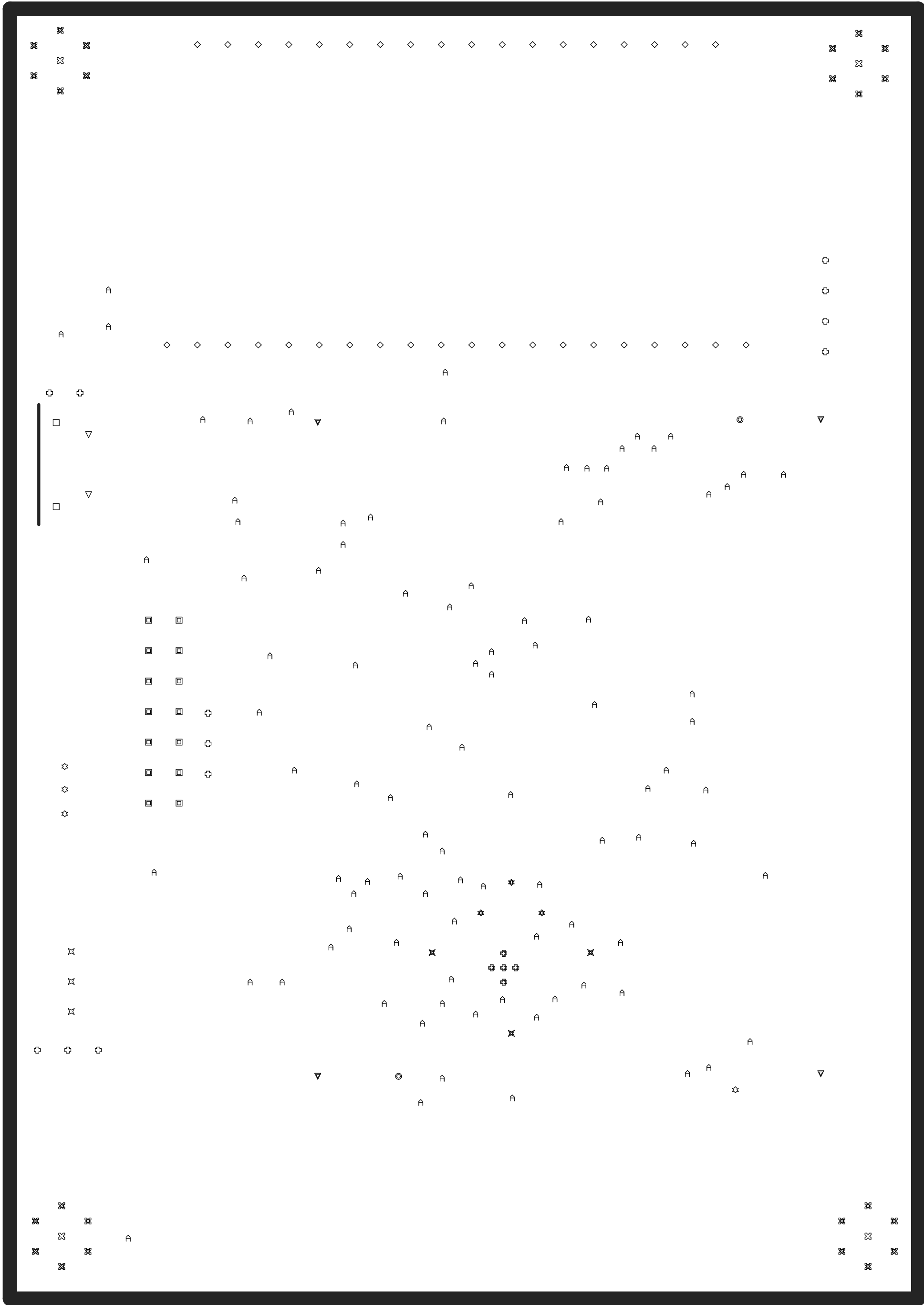
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #:	REV: 1.0	SVN REV: Not In VersionControl
LAYER NAME = 11 Board Outline	TID #: TIDM-1CHP-DTECT-CO		
PLOT NAME = Bottom Layer	GENERATED : 5/6/2016 10:28:08 AM	TEXAS INSTRUMENTS	



ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #:	REV: 1.0	SVN REV: Not In VersionControl
LAYER NAME = Bottom Solder Mask	TID #: TIDM-1CHP-DTECT-CO		
PLOT NAME = Bottom Solder Mask	GENERATED : 5/6/2016 10:28:11 AM		TEXAS INSTRUMENTS



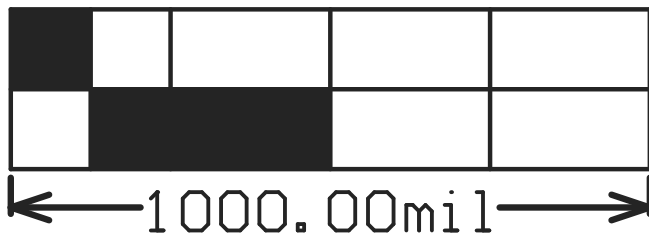
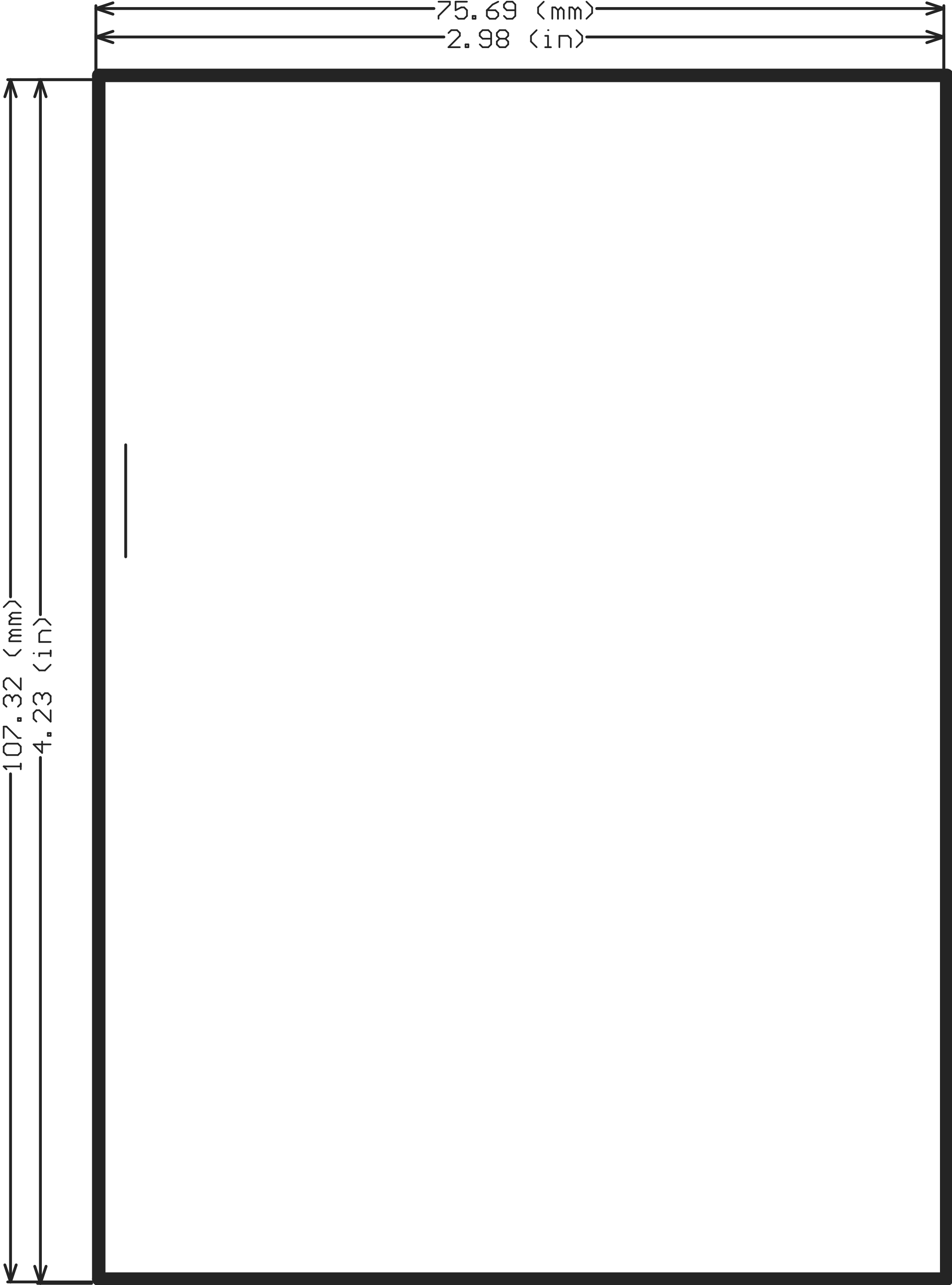
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #:	REV: 1.0	SVN REV: Not In VersionControl
LAYER NAME = Bot Board Outline	TID #: TIDM-1CHP-DTECT-CO		
PLOT NAME = Bottom Overlay	GENERATED : 5/6/2016 10:28:14 AM		TEXAS INSTRUMENTS



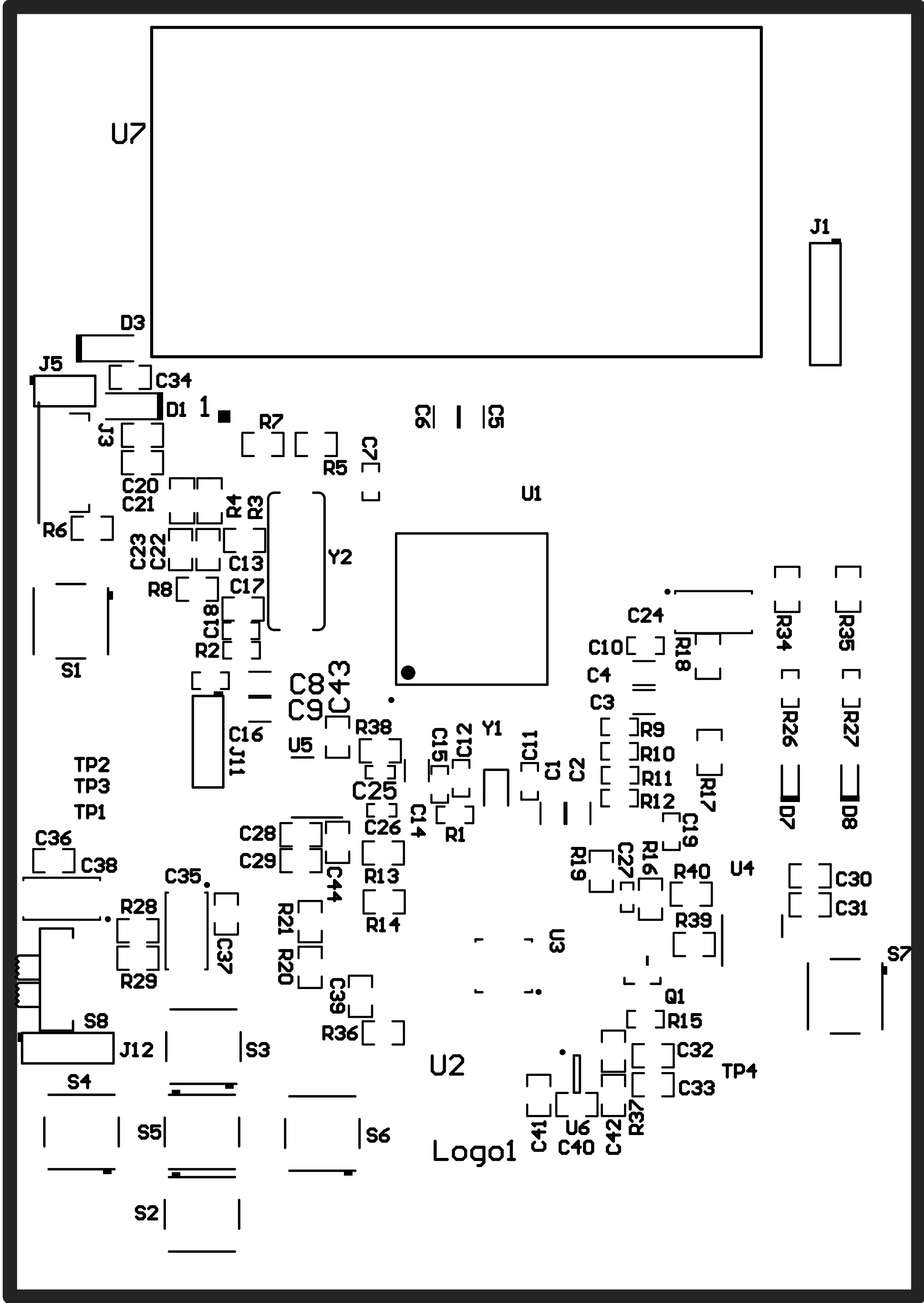
Symbol	Quantity	Finished Hole Size	Plated	Hole Type	Drill Layer Pair	Hole Tolerance
⊙	2	77.95mil (1.980mm)	NPTH	Round	Top Layer - Bottom Layer	
⊕	5	7.87mil (0.200mm)	PTH	Round	Top Layer - Bottom Layer	
☆	4	17.72mil (0.450mm)	PTH	Round	Top Layer - Bottom Layer	
⊗	24	19.69mil (0.500mm)	PTH	Round	Top Layer - Bottom Layer	
⌒	94	28.00mil (0.711mm)	PTH	Round	Top Layer - Bottom Layer	
◇	38	30.00mil (0.762mm)	PTH	Round	Top Layer - Bottom Layer	
✕	3	31.50mil (0.800mm)	PTH	Round	Top Layer - Bottom Layer	
▽	2	33.46mil (0.850mm)	PTH	Round	Top Layer - Bottom Layer	
⊕	12	39.37mil (1.000mm)	PTH	Round	Top Layer - Bottom Layer	
▽	4	46.85mil (1.190mm)	PTH	Round	Top Layer - Bottom Layer	
▣	14	47.24mil (1.200mm)	PTH	Round	Top Layer - Bottom Layer	
☆	3	59.06mil (1.500mm)	PTH	Round	Top Layer - Bottom Layer	
✕	3	100.00mil (2.540mm)	PTH	Round	Top Layer - Bottom Layer	
⊗	4	129.92mil (3.300mm)	PTH	Round	Top Layer - Bottom Layer	
□	2	23.62mil (0.600mm)	PTH	Slot	Top Layer - Bottom Layer	
	214 Total					

Slot definitions : Routed Path Length = Calculated from tool start centre position to tool end centre position.
Hole Length = Routed Path Length + Tool Size = Slot length as defined in the PCB layout

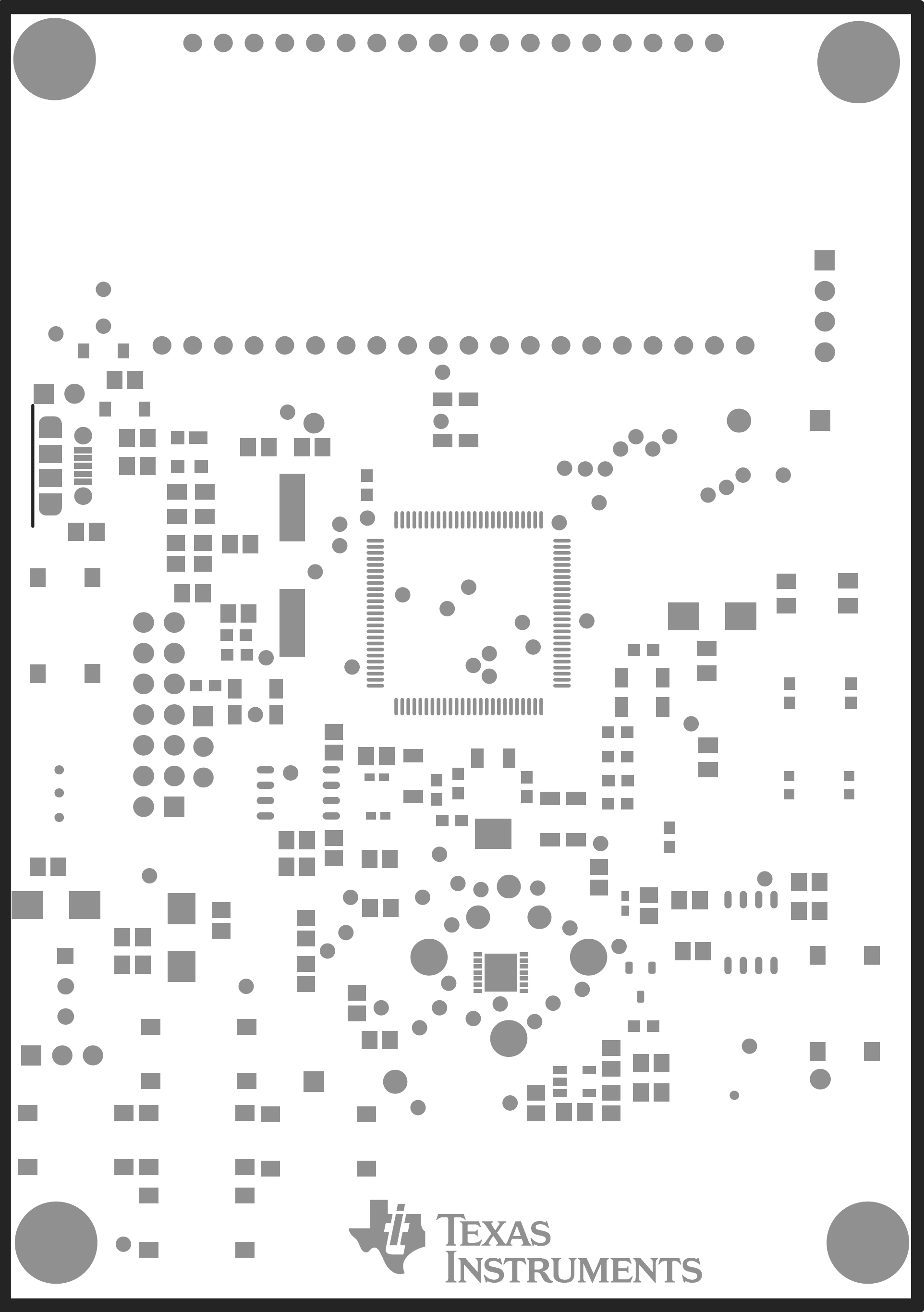
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #:	REV: 1.0	SUN REV: Not In VersionControl
LAYER NAME = Micro Drawing	TID #: TIDM-1CHP-DTECT-CO		
PLOT NAME = Drill Drawing	GENERATED : 5/6/2016	10:28:17 AM	TEXAS INSTRUMENTS



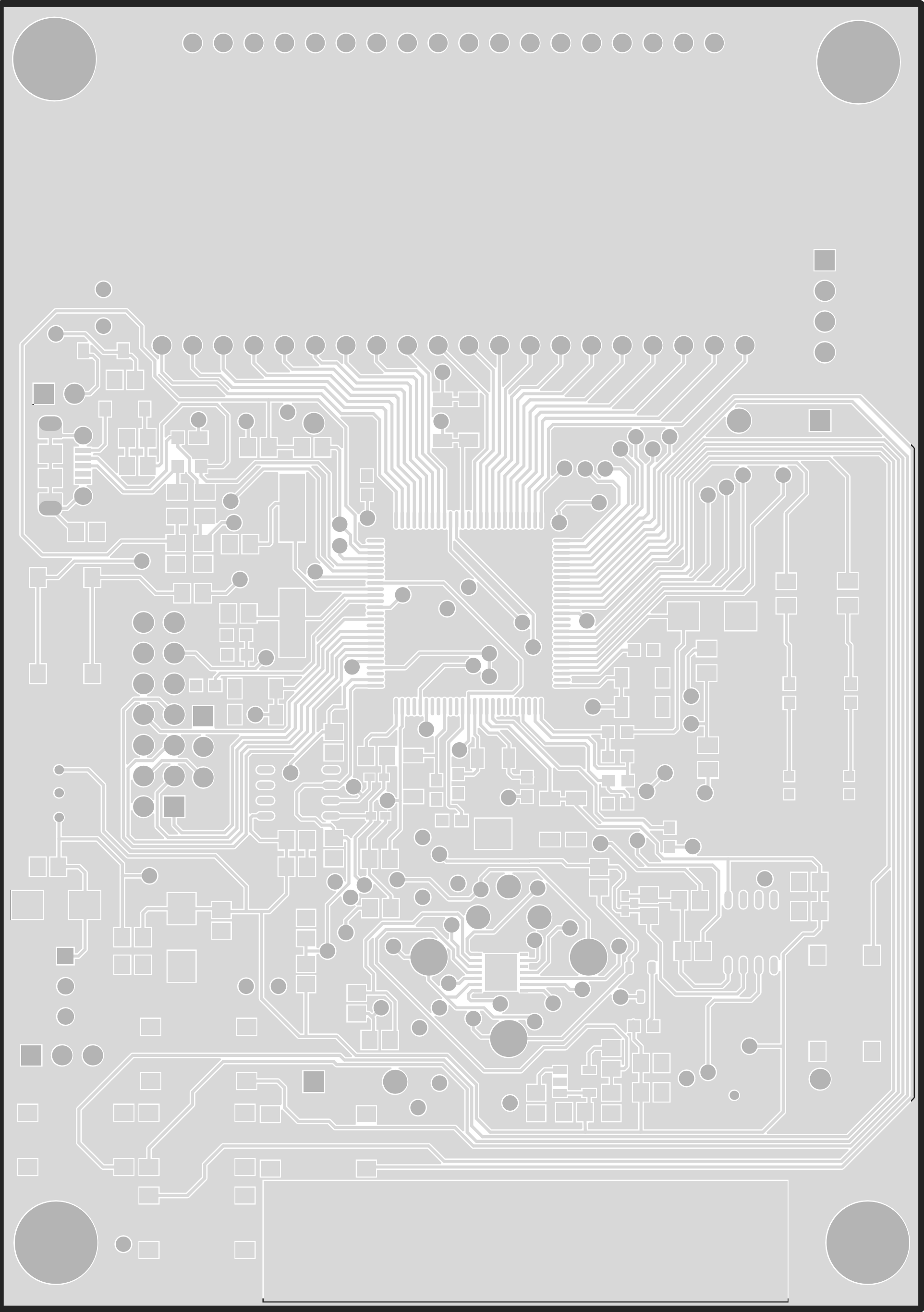
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #:	REV: 1.0	SVN REV: Not In VersionControl
LAYER NAME = M2 Board Dimensions	TID #: TIDM-1CHP-DTECT-CO		
PLOT NAME = Board Dimensions	GENERATED : 5/6/2016 10:28:20 AM		TEXAS INSTRUMENTS



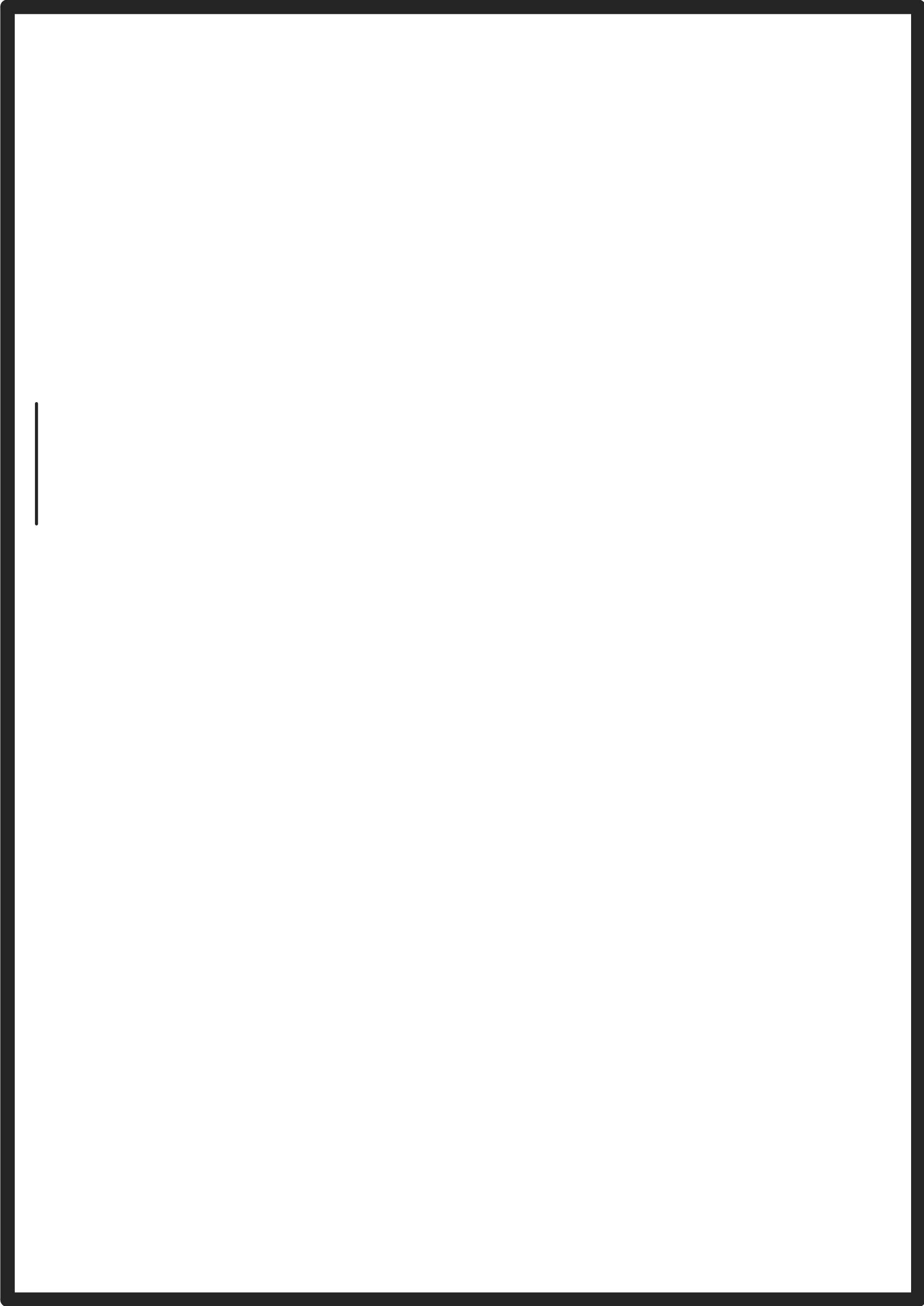
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #:	REV: 1.0	SVN REV: Not In VersionControl
LAYER NAME = Top Overlay	TID #: TIDM-1CHP-DTECT-CO		
PLOT NAME = Top Overlay	GENERATED : 5/6/2016 10:28:23 AM	TEXAS INSTRUMENTS	



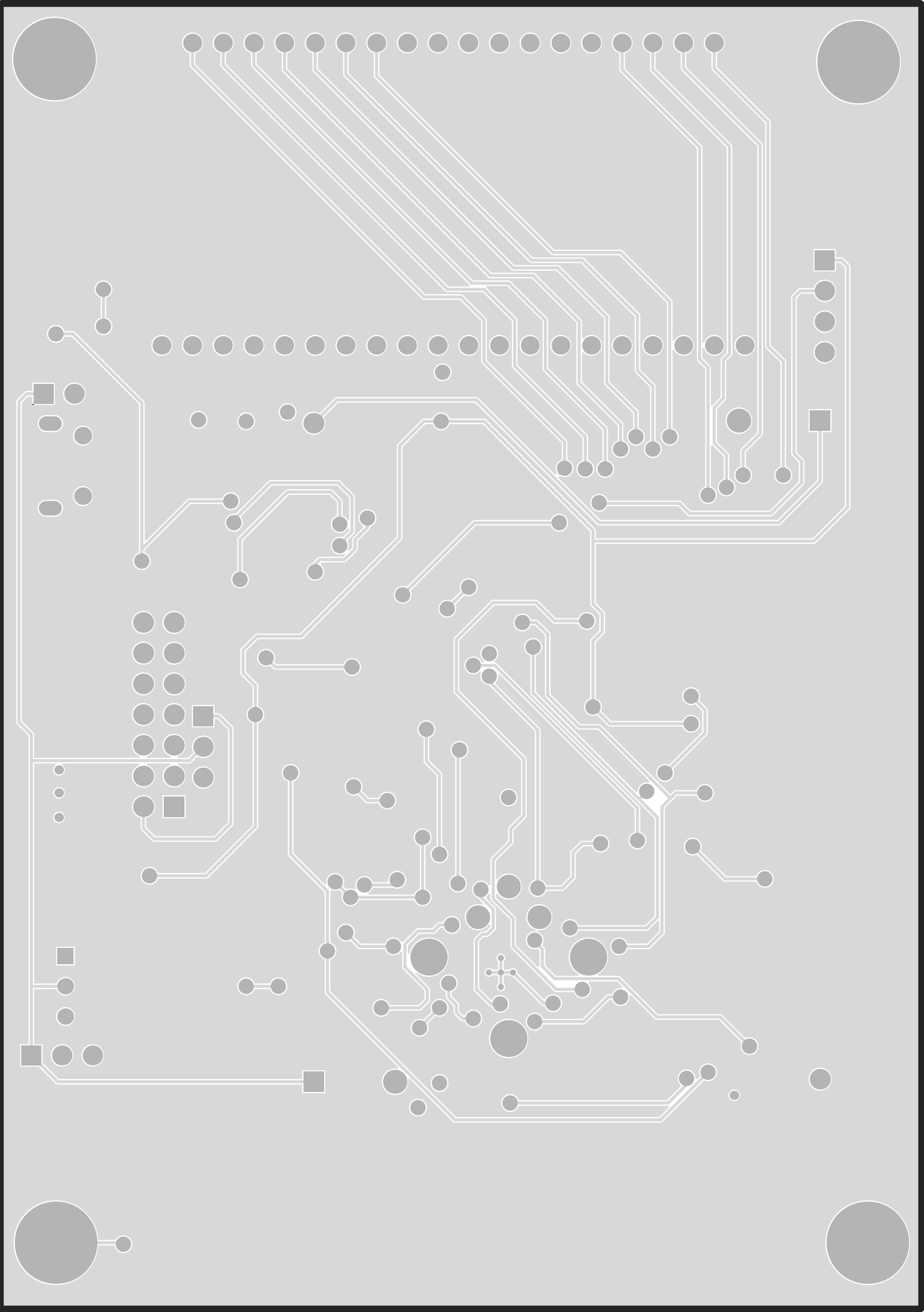
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #:	REV: 1.0	SVN REV: Not In VersionControl
LAYER NAME = Top Solder Mask	TID #: TIDM-1CHP-DTECT-CO		
PLOT NAME = Top Solder Mask	GENERATED : 5/6/2016 10:28:28 AM		TEXAS INSTRUMENTS



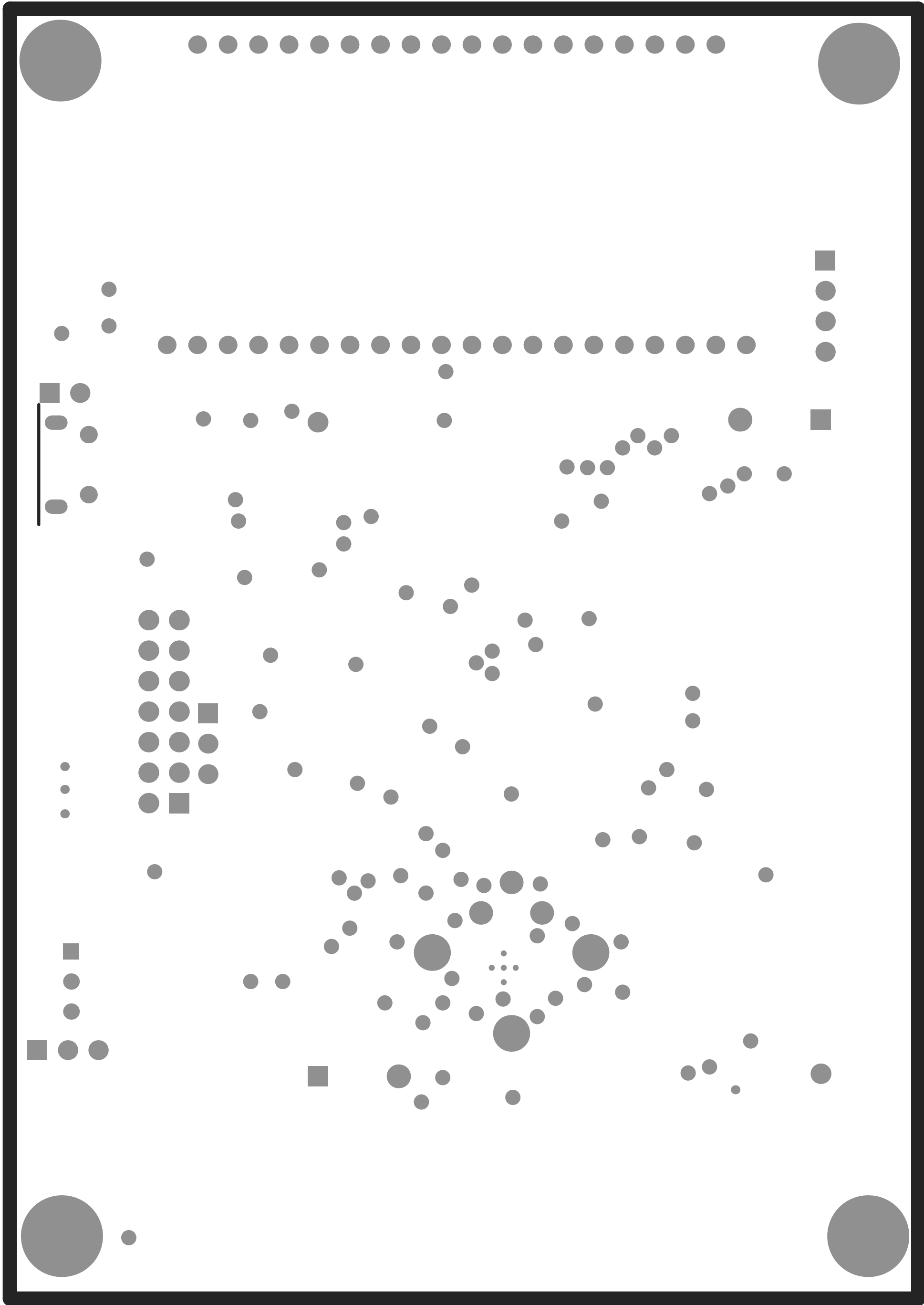
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #:	REV: 1.0	SVN REV: Not In VersionControl
LAYER NAME = M1Board Outline	TID #: TIDM-1CHP-DTECT-CO		
PLOT NAME = Top Layer	GENERATED : 5/6/2016 10:28:31 AM		TEXAS INSTRUMENTS



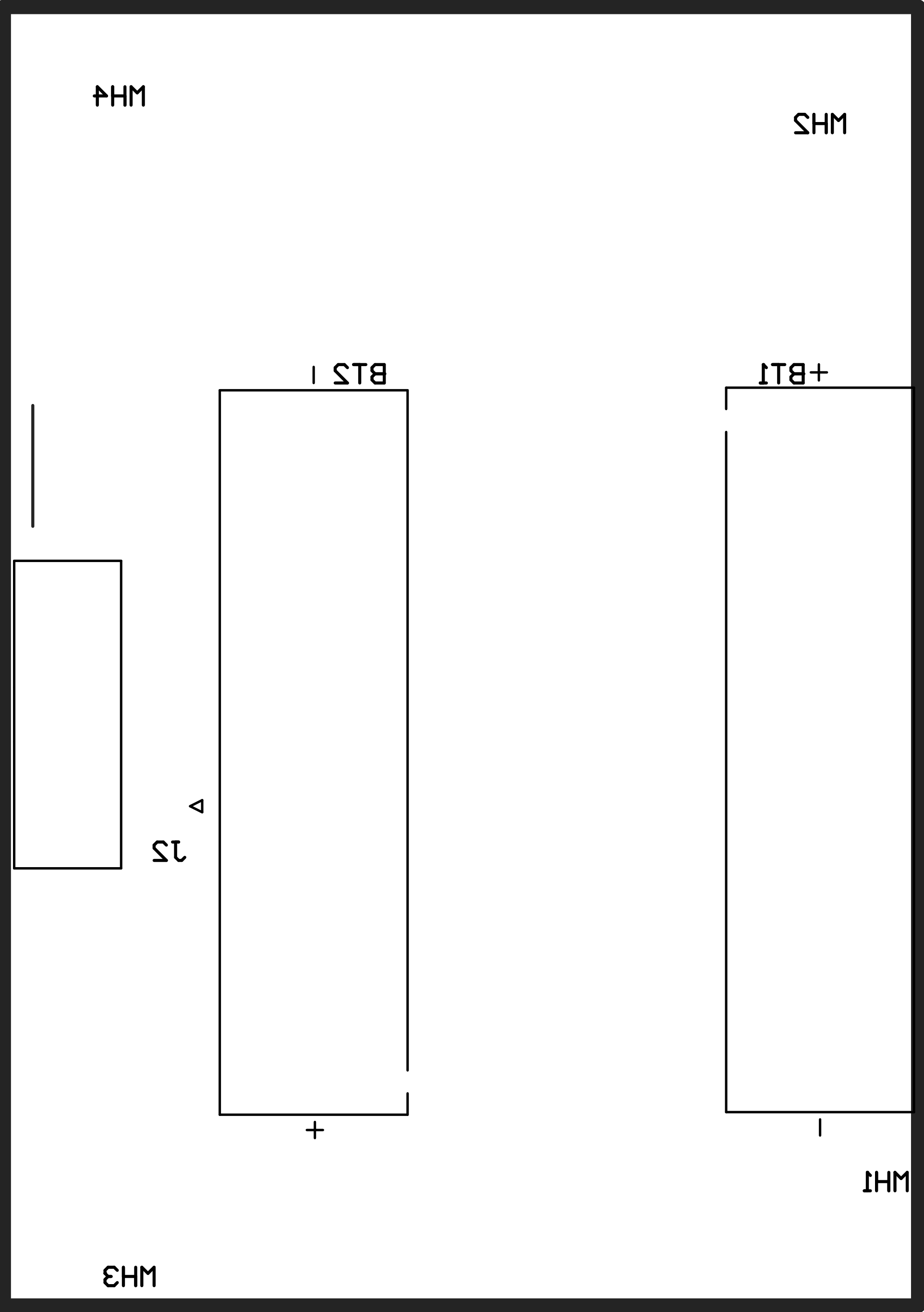
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #:	REV: 1.0	SVN REV: Not In VersionControl
LAYER NAME = M1 Board Outline	TID #: TIDM-1CHP-DTECT-CO		
PLOT NAME = Signal Layer 1	GENERATED : 5/6/2016 10:28:35 AM		TEXAS INSTRUMENTS



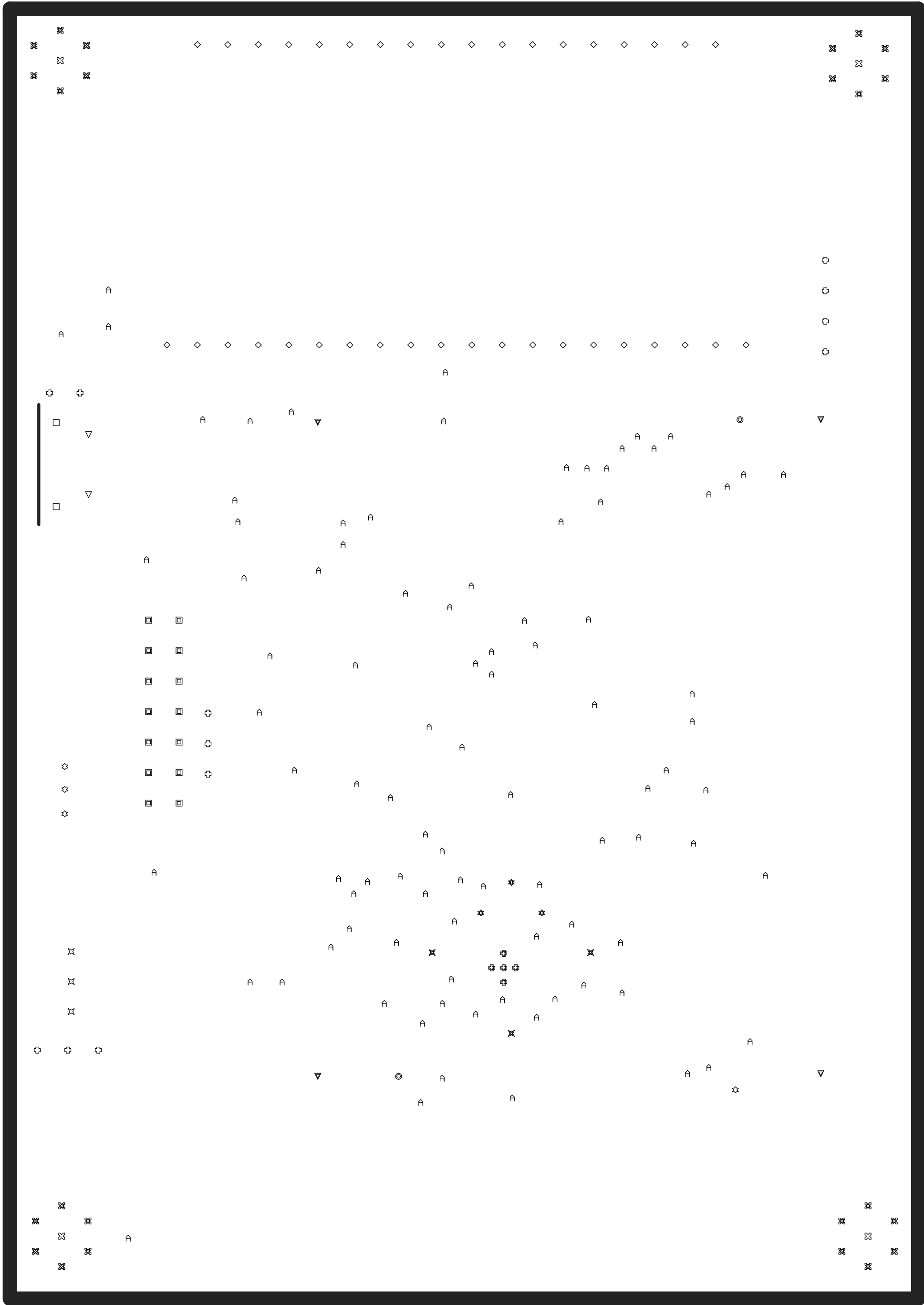
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #:	REV: 1.0	SVN REV: Not In VersionControl
LAYER NAME = 11 Board Outline	TID #: TIDM-1CHP-DTECT-CO		
PLOT NAME = Bottom Layer	GENERATED : 5/6/2016 10:28:37 AM		TEXAS INSTRUMENTS



ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #:	REV: 1.0	SVN REV: Not In VersionControl
LAYER NAME = Bottom Solder Mask	TID #: TIDM-1CHP-DTECT-CO		
PLOT NAME = Bottom Solder Mask	GENERATED : 5/6/2016 10:28:40 AM		TEXAS INSTRUMENTS



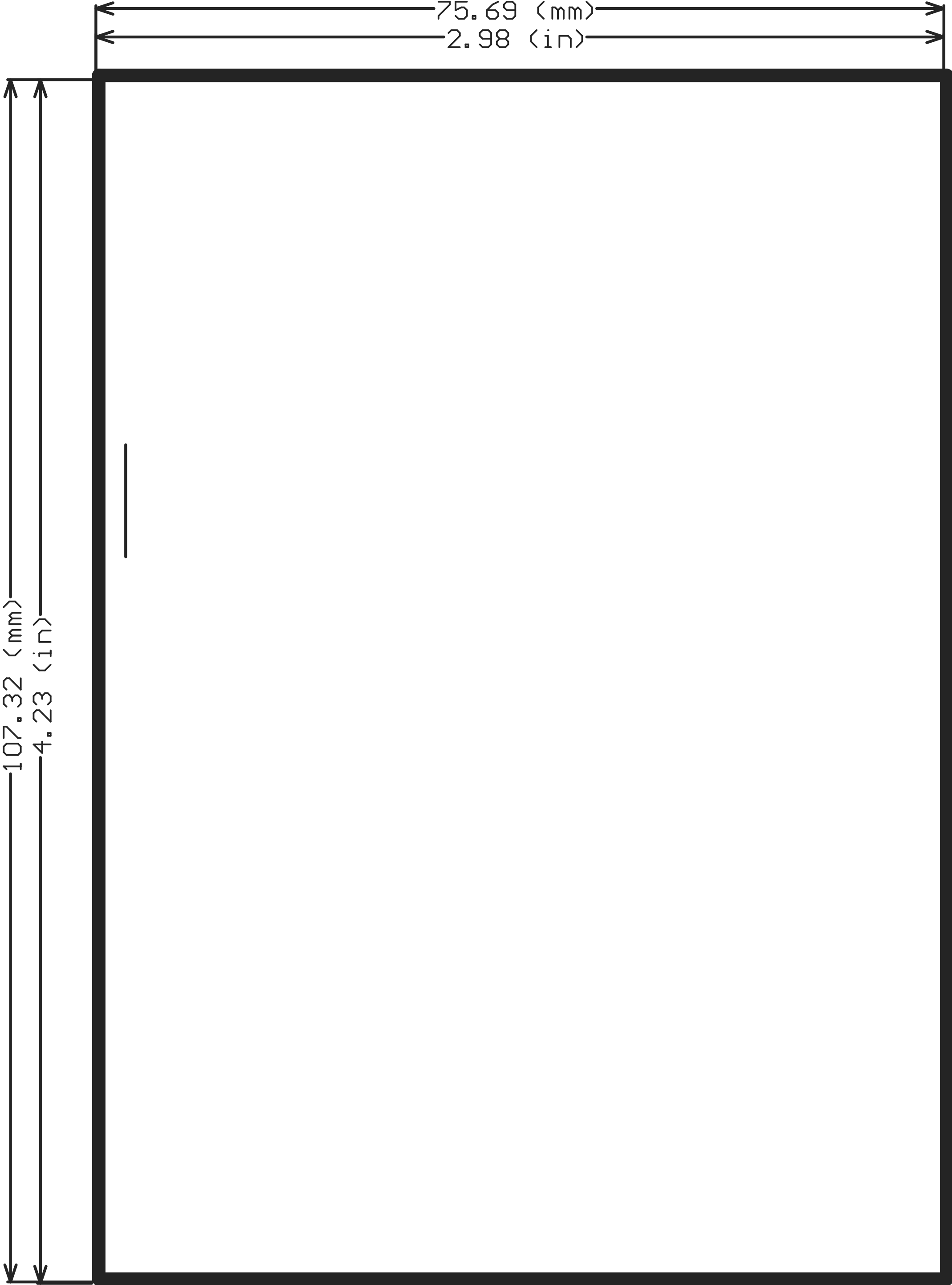
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #:	REV: 1.0	SVN REV: Not In VersionControl
LAYER NAME = Bot Board Outline	TID #: TIDM-1CHP-DTECT-CO		
PLOT NAME = Bottom Overlay	GENERATED : 5/6/2016 10:28:44 AM		TEXAS INSTRUMENTS



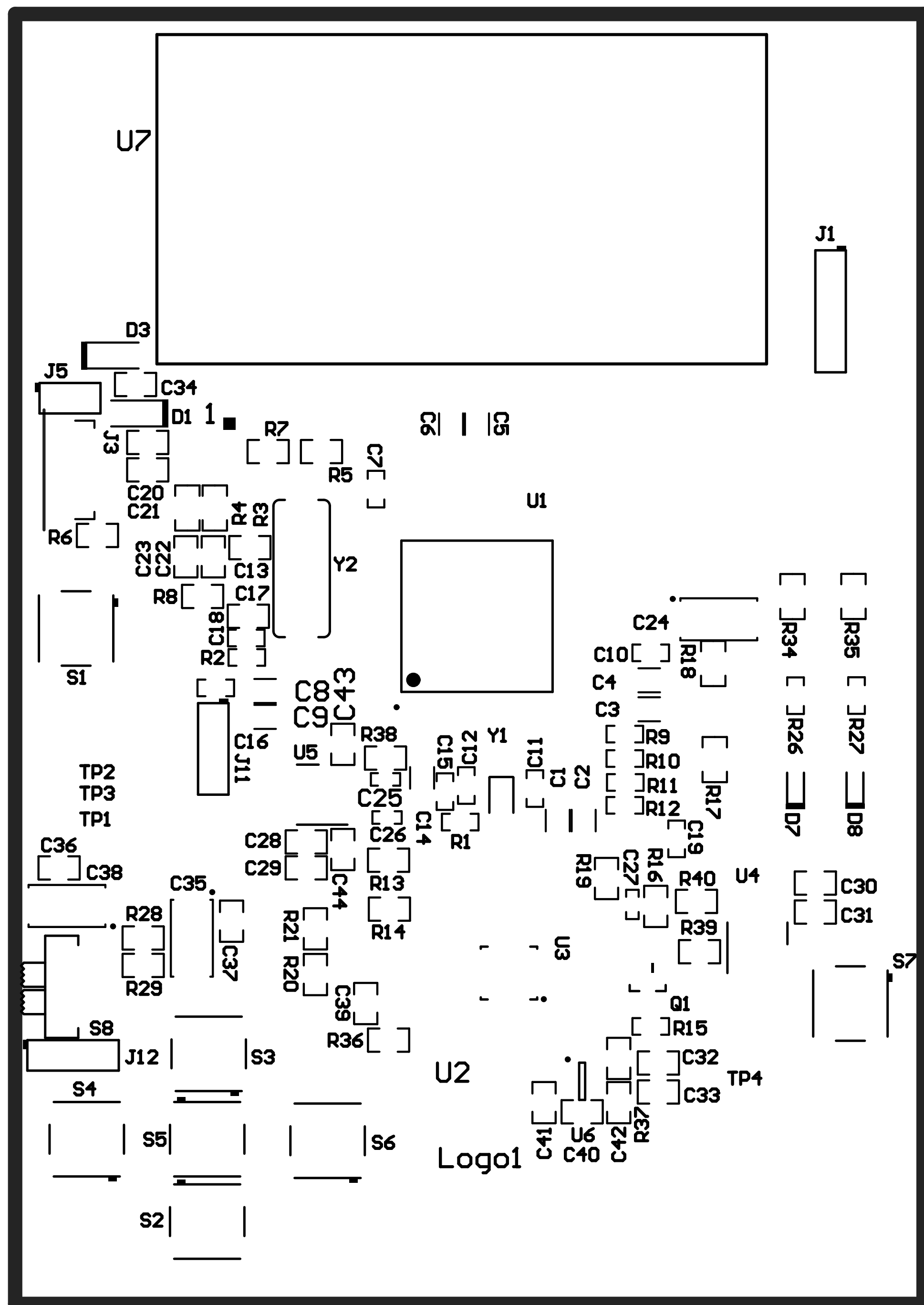
Symbol	Quantity	Finished Hole Size	Plated	Hole Type	Drill Layer Pair	Hole Tolerance
⦿	2	77.95mil (1.980mm)	NPTH	Round	Top Layer - Bottom Layer	
⊕	5	7.87mil (0.200mm)	PTH	Round	Top Layer - Bottom Layer	
☆	4	17.72mil (0.450mm)	PTH	Round	Top Layer - Bottom Layer	
⊗	24	19.69mil (0.500mm)	PTH	Round	Top Layer - Bottom Layer	
⌒	94	28.00mil (0.711mm)	PTH	Round	Top Layer - Bottom Layer	
◇	38	30.00mil (0.762mm)	PTH	Round	Top Layer - Bottom Layer	
✕	3	31.50mil (0.800mm)	PTH	Round	Top Layer - Bottom Layer	
▽	2	33.46mil (0.850mm)	PTH	Round	Top Layer - Bottom Layer	
⊕	12	39.37mil (1.000mm)	PTH	Round	Top Layer - Bottom Layer	
▽	4	46.85mil (1.190mm)	PTH	Round	Top Layer - Bottom Layer	
▣	14	47.24mil (1.200mm)	PTH	Round	Top Layer - Bottom Layer	
☆	3	59.06mil (1.500mm)	PTH	Round	Top Layer - Bottom Layer	
✕	3	100.00mil (2.540mm)	PTH	Round	Top Layer - Bottom Layer	
⊗	4	129.92mil (3.300mm)	PTH	Round	Top Layer - Bottom Layer	
□	2	23.62mil (0.600mm)	PTH	Slot	Top Layer - Bottom Layer	
	214 Total					

Slot definitions : Routed Path Length = Calculated from tool start centre position to tool end centre position.
Hole Length = Routed Path Length + Tool Size = Slot length as defined in the PCB layout

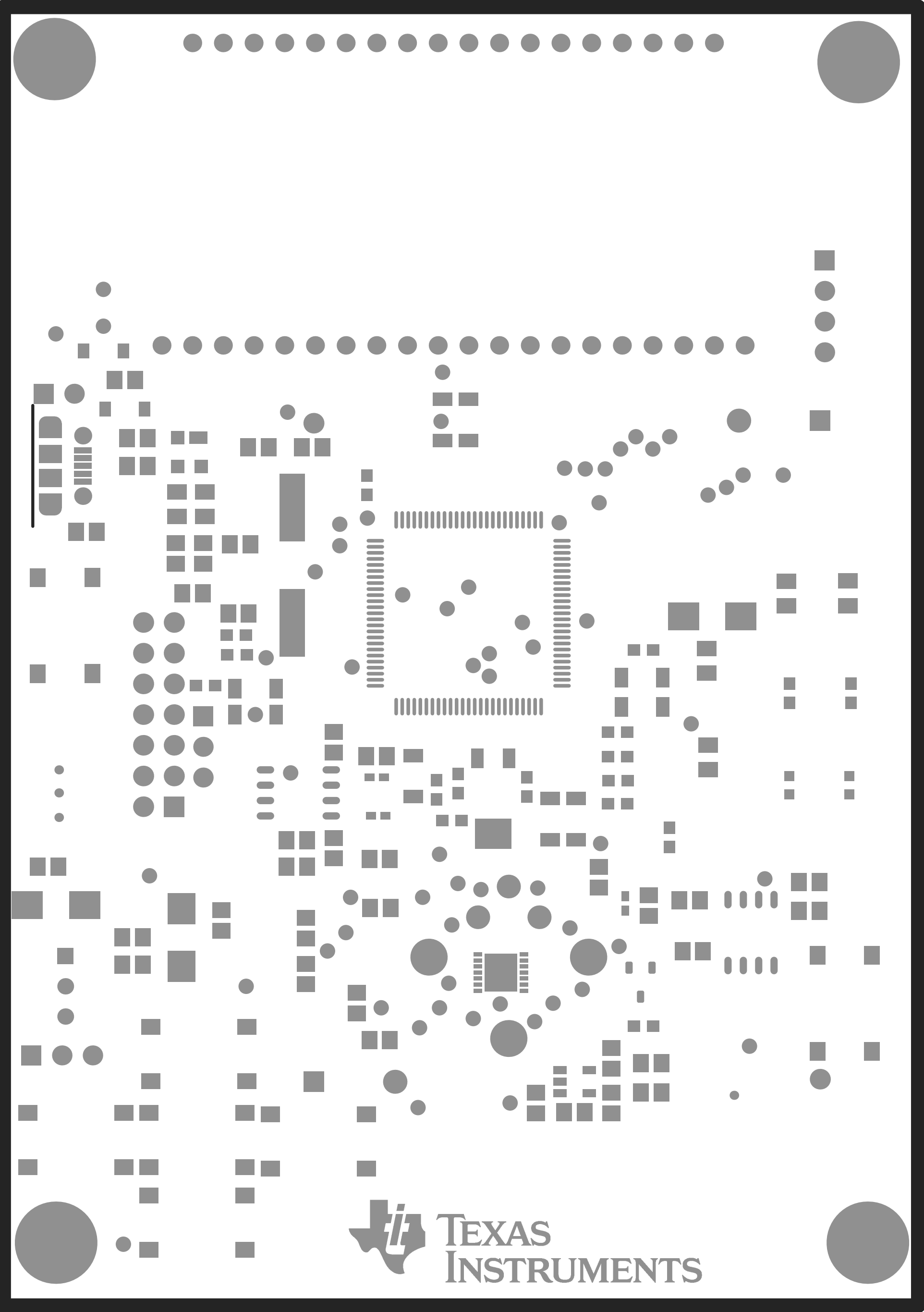
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #:	REV: 1.0	SUN REV: Not In VersionControl
LAYER NAME = Micro Drawing	TID #: TIDM-1CHP-DTECT-CO		
PLOT NAME = Drill Drawing	GENERATED : 5/6/2016	10:28:47 AM	TEXAS INSTRUMENTS



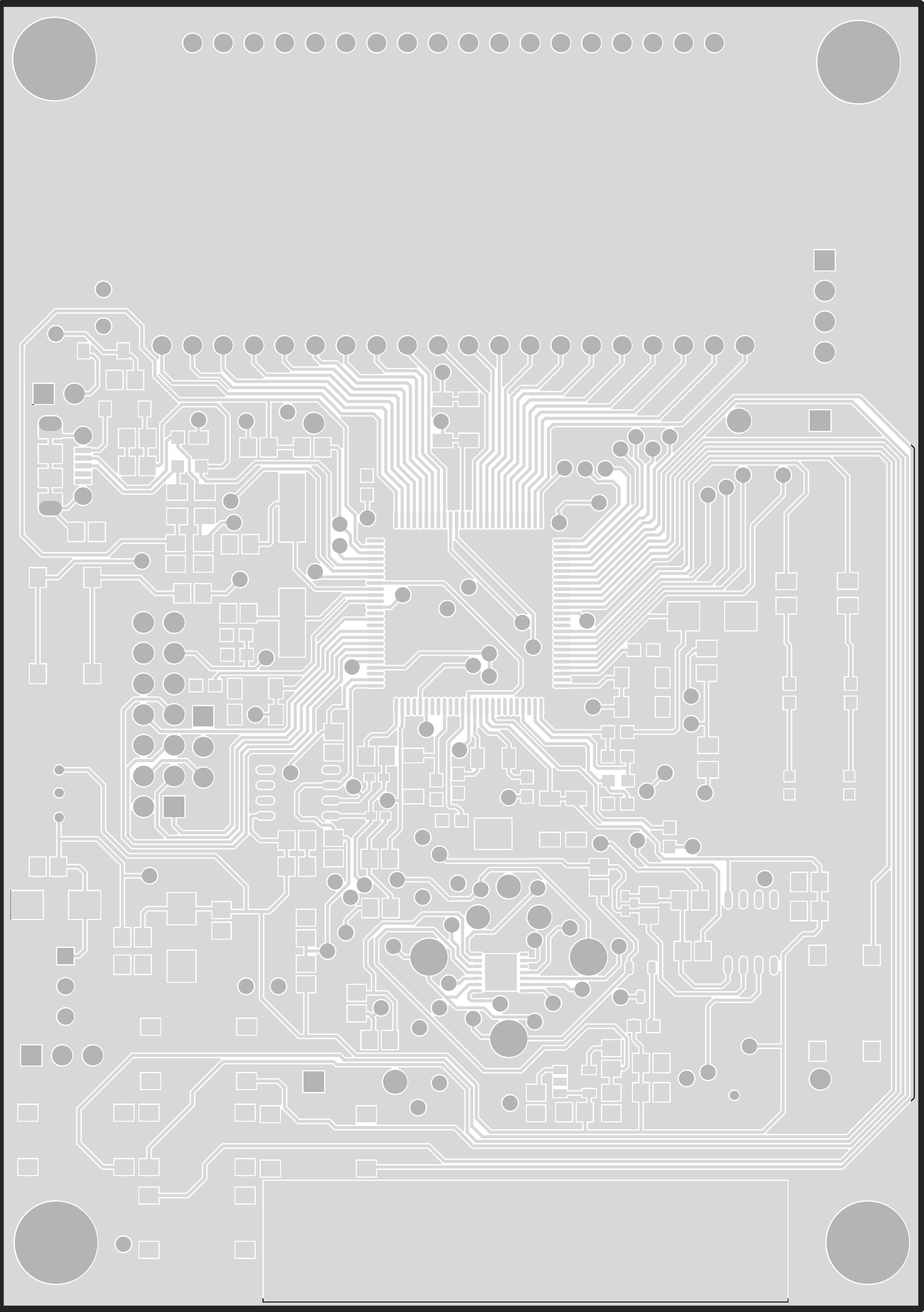
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #:	REV: 1.0	SVN REV: Not In VersionControl
LAYER NAME = M2 Board Dimensions	TID #: TIDM-1CHP-DTECT-CO		
PLOT NAME = Board Dimensions	GENERATED : 5/6/2016 10:28:50 AM		TEXAS INSTRUMENTS



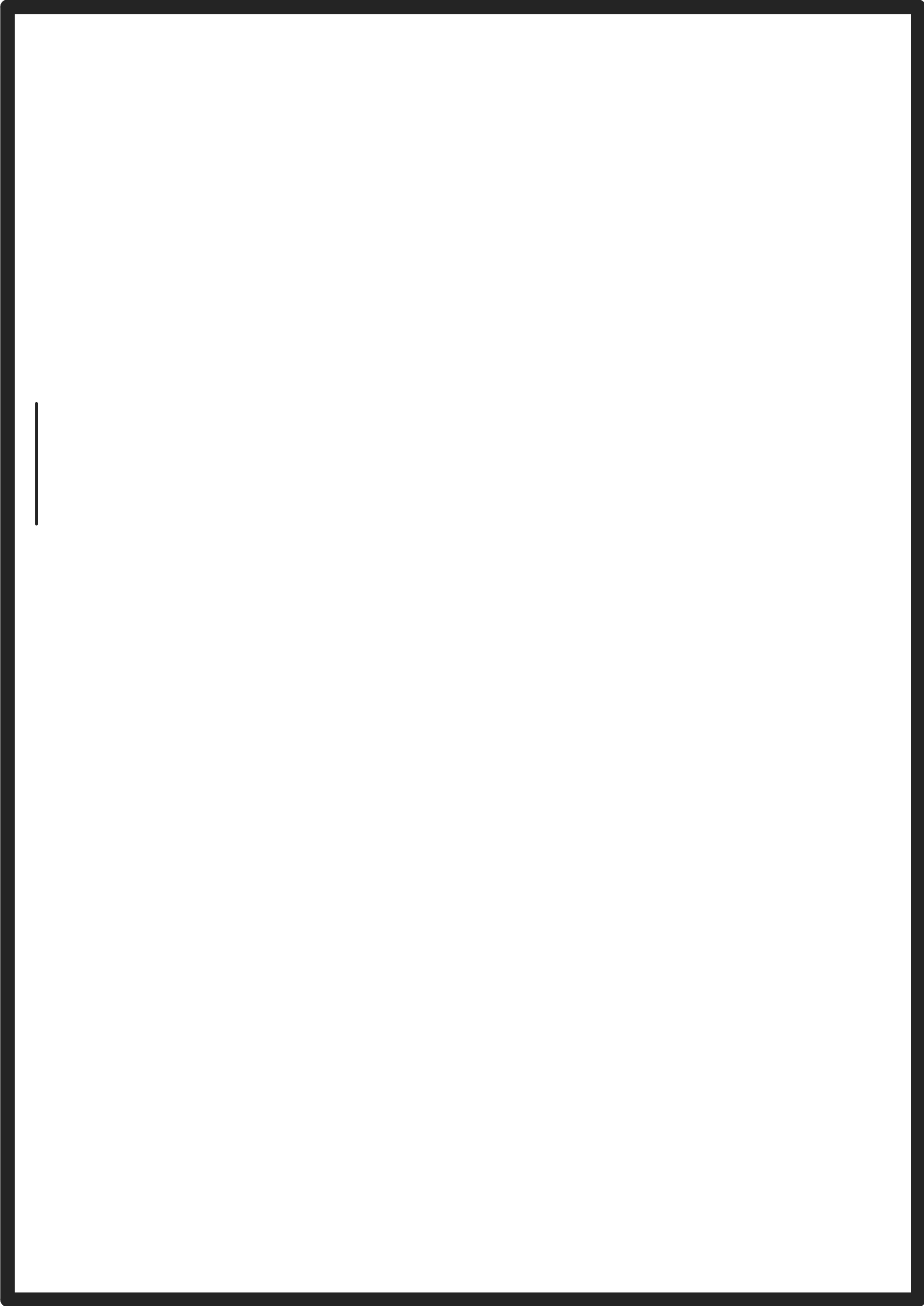
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #:	REV: 1.0	SUN REV: Not In VersionControl
LAYER NAME = Top Overlay	TID #: TIDM-1CHP-DTECT-CO		
PLOT NAME = Top Overlay	GENERATED : 5/6/2016 10:28:54 AM		TEXAS INSTRUMENTS



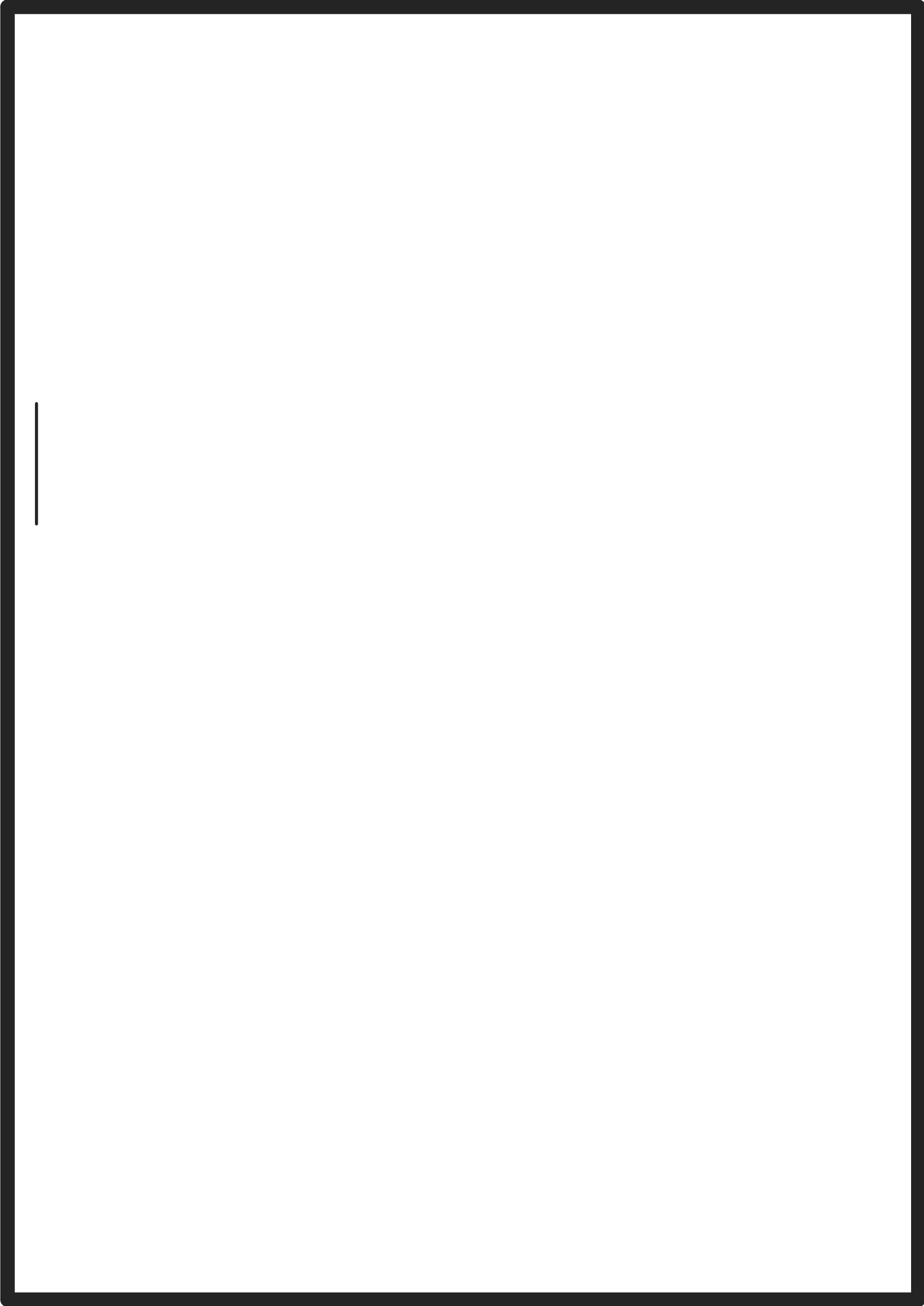
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #:	REV: 1.0	SVN REV: Not In VersionControl
LAYER NAME = Top Solder Mask	TID #: TIDM-1CHP-DTECT-CO		
PLOT NAME = Top Solder Mask	GENERATED : 5/6/2016 10:28:58 AM		TEXAS INSTRUMENTS



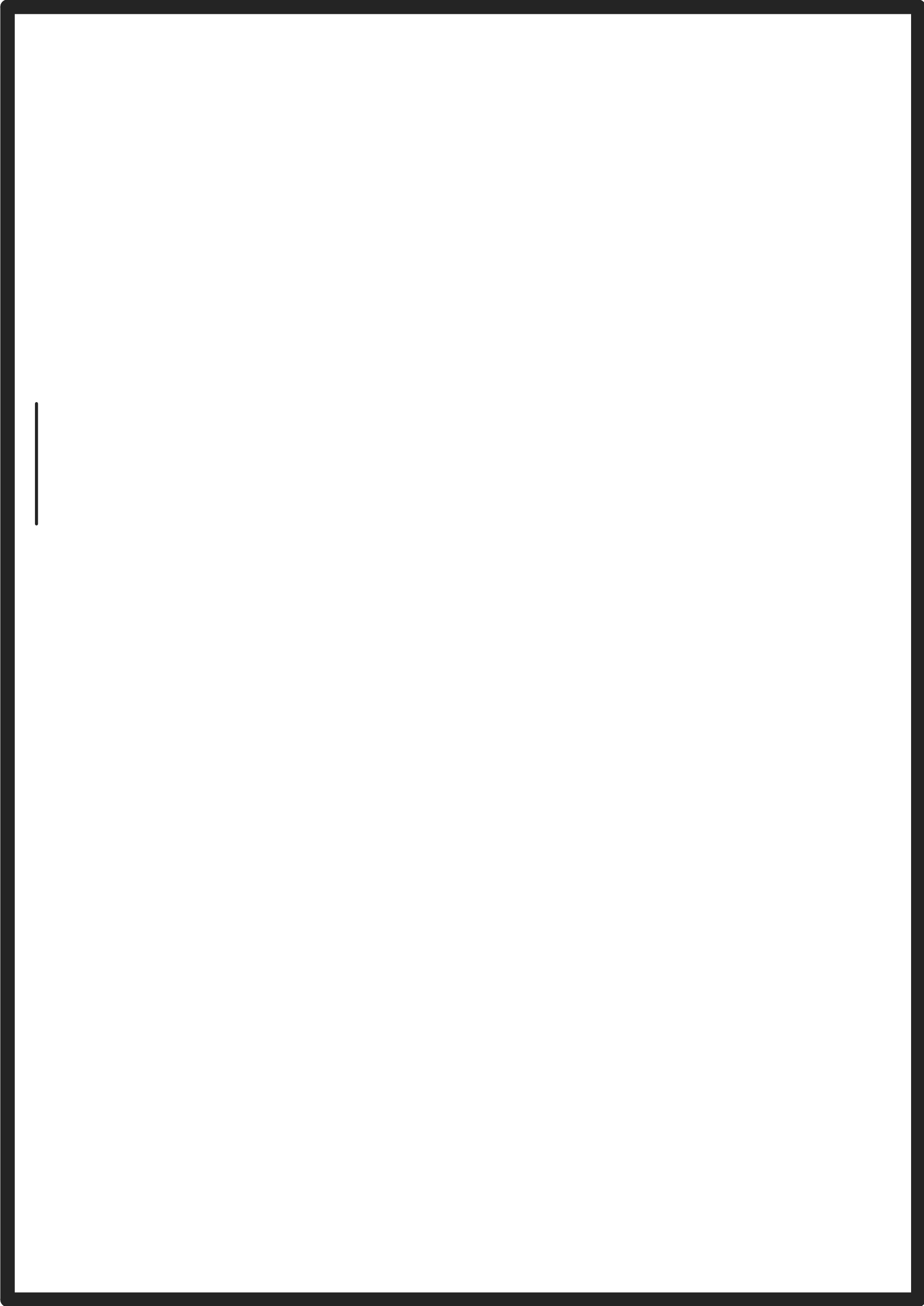
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #:	REV: 1.0	SVN REV: Not In VersionControl
LAYER NAME = M1Board Outline	TID #: TIDM-1CHP-DTECT-CO		
PLOT NAME = Top Layer	GENERATED : 5/6/2016 10:29:02 AM		TEXAS INSTRUMENTS



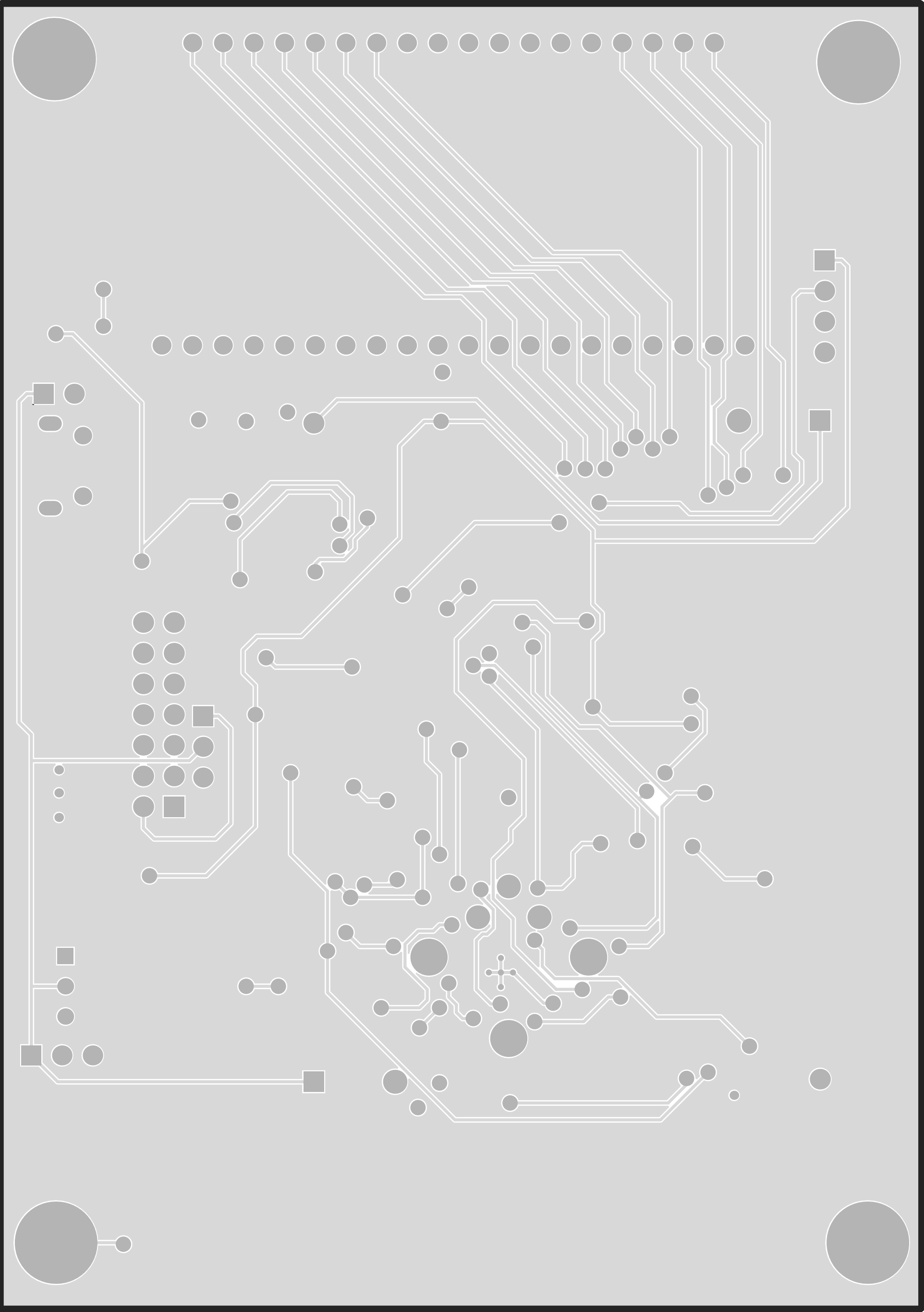
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #:	REV: 1.0	SVN REV: Not In VersionControl
LAYER NAME = M1 Board Outline	TID #: TIDM-1CHP-DTECT-CO		
PLOT NAME = Signal Layer 1	GENERATED : 5/6/2016 10:29:06 AM		TEXAS INSTRUMENTS



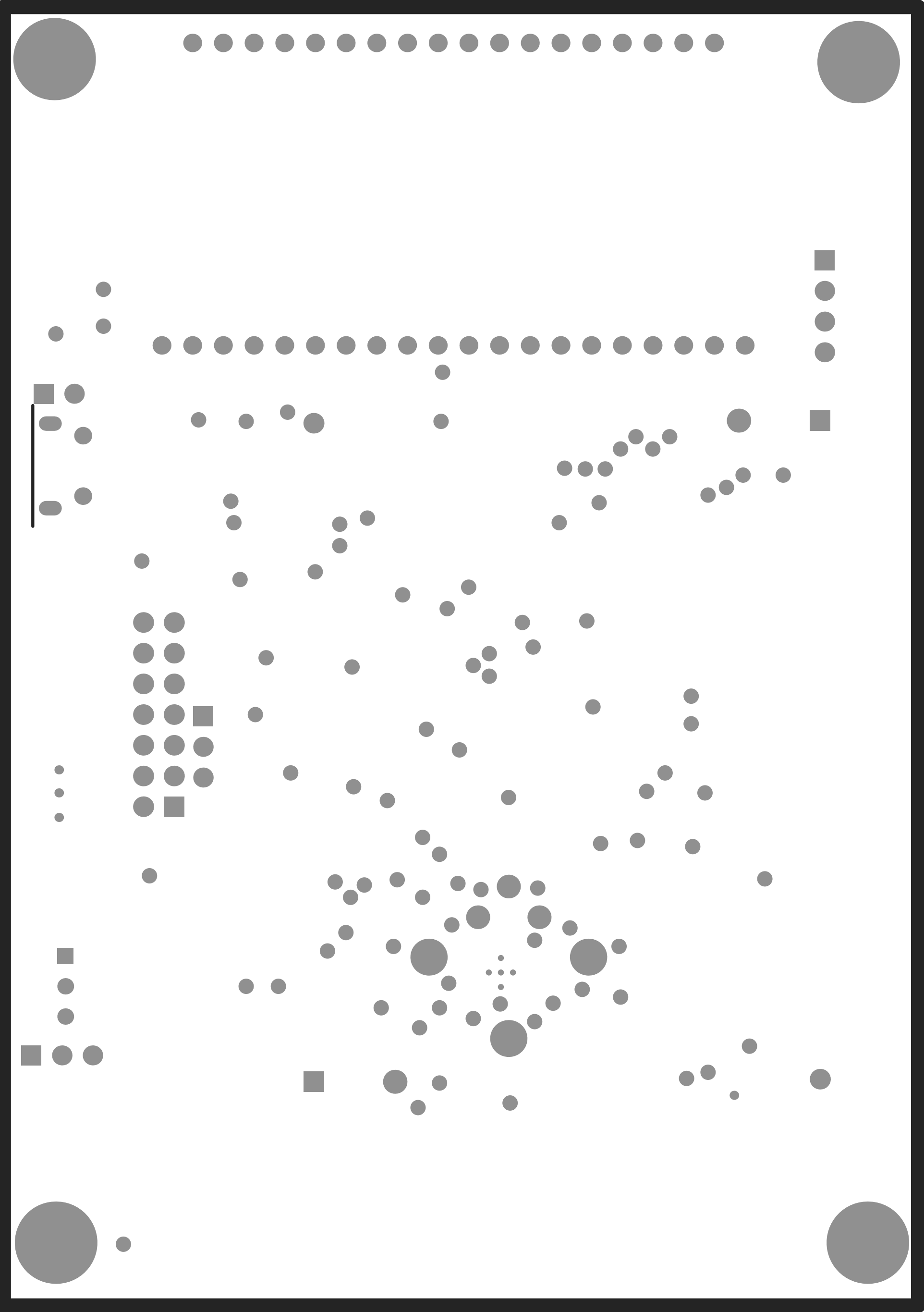
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #:	REV: 1.0	SVN REV: Not In VersionControl
LAYER NAME = M1 Board Outline	TID #: TIDM-1CHP-DTECT-CO		
PLOT NAME = Signal Layer 3	GENERATED : 5/6/2016 10:29:09 AM		TEXAS INSTRUMENTS



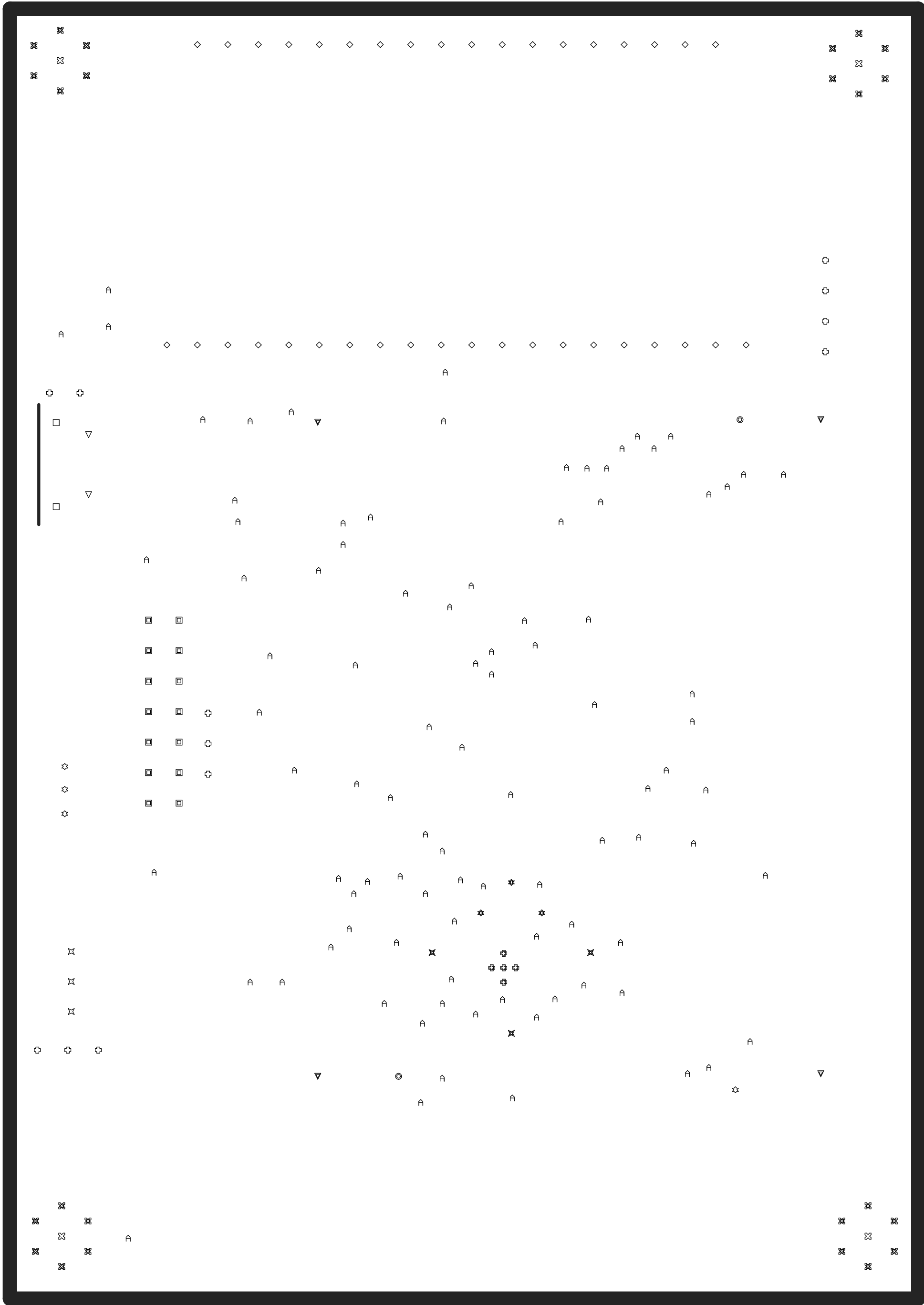
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #:	REV: 1.0	SVN REV: Not In VersionControl
LAYER NAME = M1 Board Outline	TID #: TIDM-1CHP-DTECT-CO		
PLOT NAME = Signal Layer 4	GENERATED : 5/6/2016 10:29:12 AM		TEXAS INSTRUMENTS



ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #:	REV: 1.0	SVN REV: Not In VersionControl
LAYER NAME = 11 Board Outline	TID #: TIDM-1CHP-DTECT-CO		
PLOT NAME = Bottom Layer	GENERATED : 5/6/2016	10:29:16 AM	TEXAS INSTRUMENTS



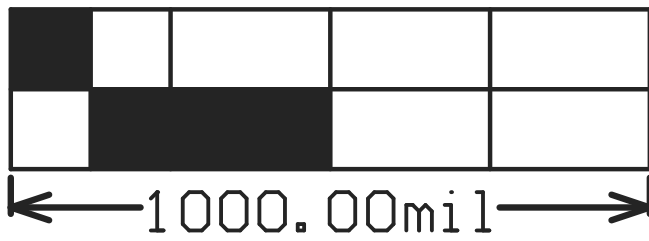
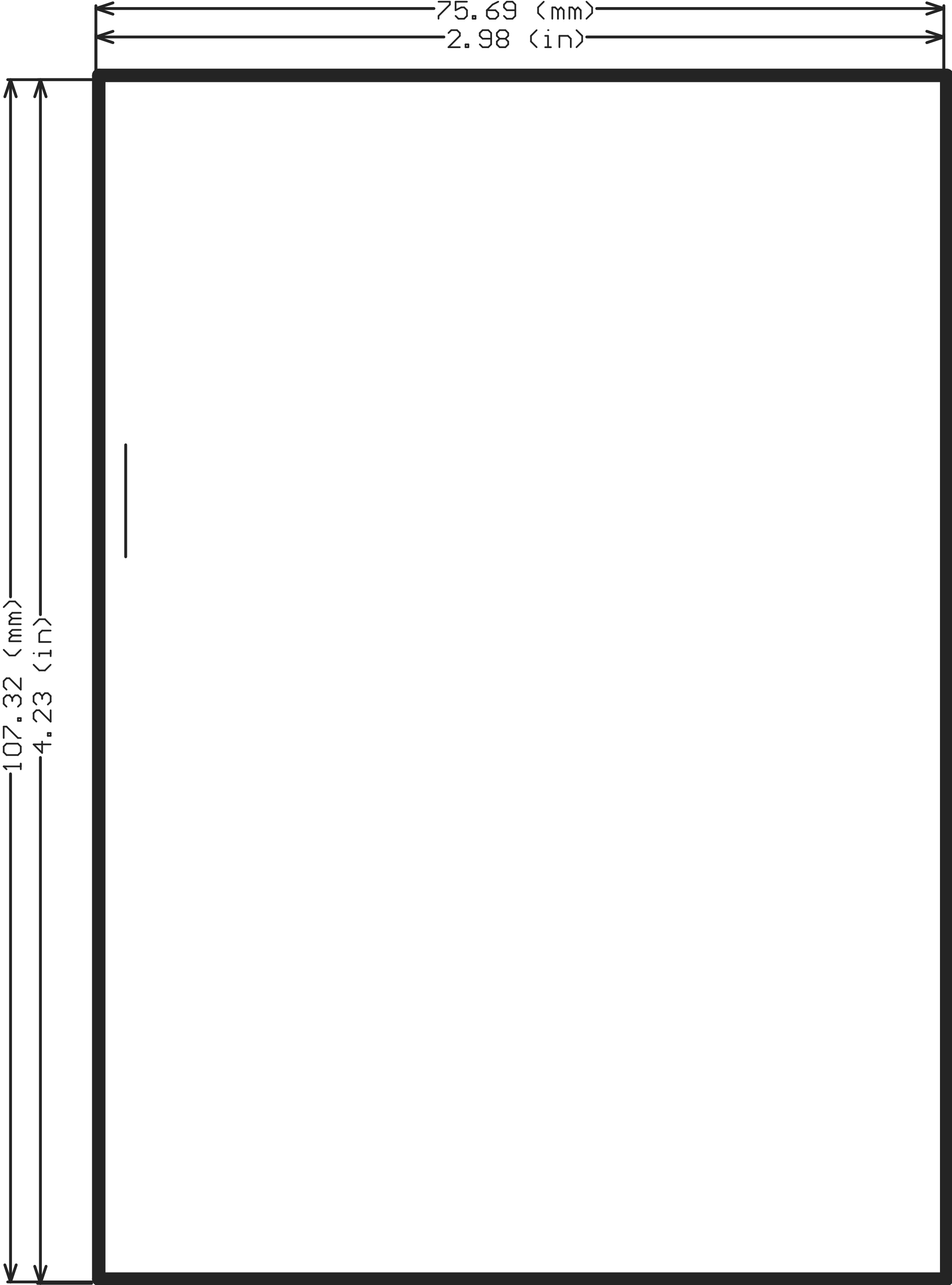
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #:	REV: 1.0	SVN REV: Not In VersionControl
LAYER NAME = Bottom Solder Mask	TID #: TIDM-1CHP-DTECT-CO		
PLOT NAME = Bottom Solder Mask	GENERATED : 5/6/2016 10:29:19 AM		TEXAS INSTRUMENTS



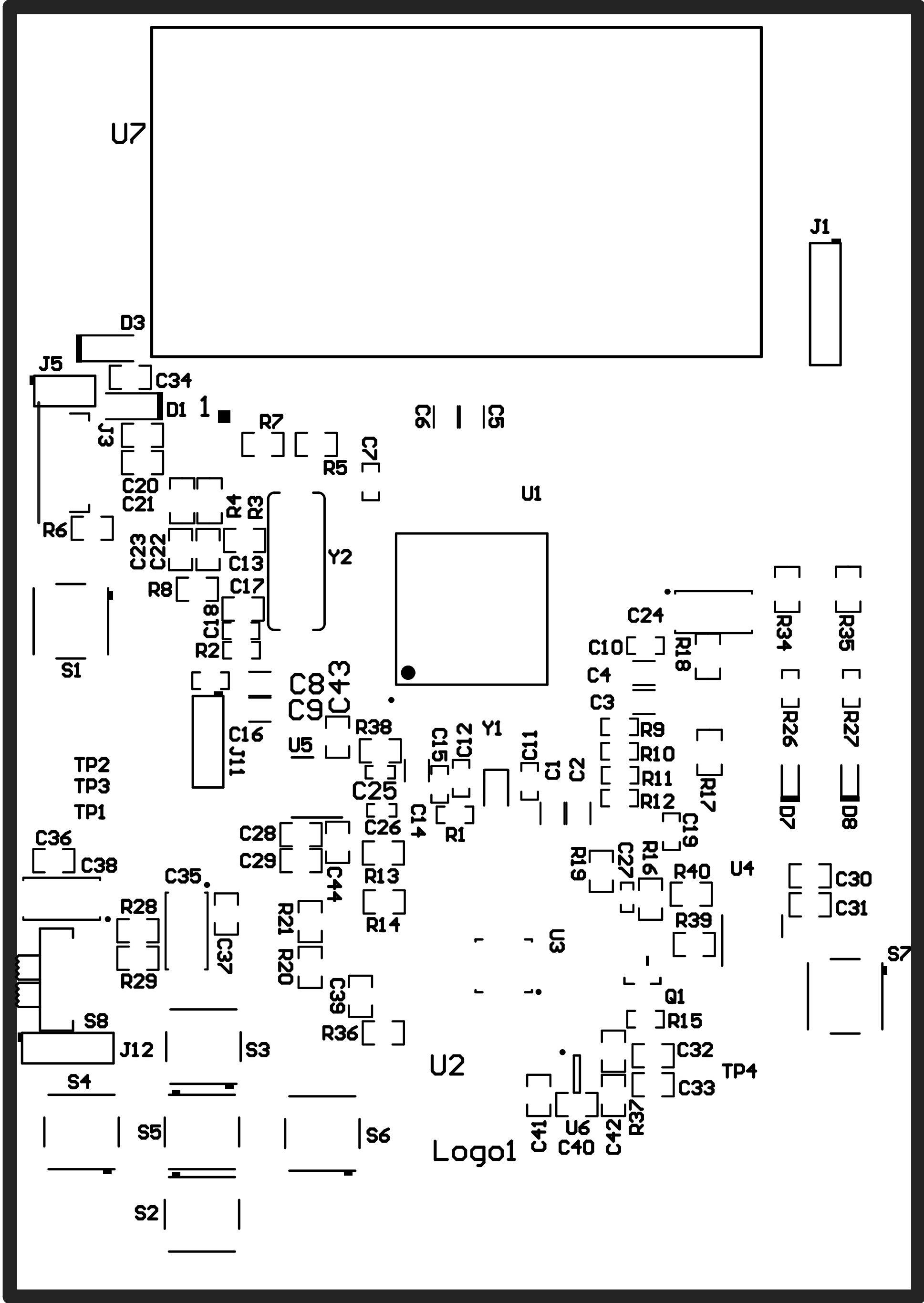
Symbol	Quantity	Finished Hole Size	Plated	Hole Type	Drill Layer Pair	Hole Tolerance
⦿	2	77.95mil (1.980mm)	NPTH	Round	Top Layer - Bottom Layer	
⊕	5	7.87mil (0.200mm)	PTH	Round	Top Layer - Bottom Layer	
☆	4	17.72mil (0.450mm)	PTH	Round	Top Layer - Bottom Layer	
⊗	24	19.69mil (0.500mm)	PTH	Round	Top Layer - Bottom Layer	
⌒	94	28.00mil (0.711mm)	PTH	Round	Top Layer - Bottom Layer	
◇	38	30.00mil (0.762mm)	PTH	Round	Top Layer - Bottom Layer	
✕	3	31.50mil (0.800mm)	PTH	Round	Top Layer - Bottom Layer	
▽	2	33.46mil (0.850mm)	PTH	Round	Top Layer - Bottom Layer	
⊕	12	39.37mil (1.000mm)	PTH	Round	Top Layer - Bottom Layer	
▽	4	46.85mil (1.190mm)	PTH	Round	Top Layer - Bottom Layer	
▣	14	47.24mil (1.200mm)	PTH	Round	Top Layer - Bottom Layer	
☆	3	59.06mil (1.500mm)	PTH	Round	Top Layer - Bottom Layer	
✕	3	100.00mil (2.540mm)	PTH	Round	Top Layer - Bottom Layer	
⊗	4	129.92mil (3.300mm)	PTH	Round	Top Layer - Bottom Layer	
□	2	23.62mil (0.600mm)	PTH	Slot	Top Layer - Bottom Layer	
	214 Total					

Slot definitions : Routed Path Length = Calculated from tool start centre position to tool end centre position.
Hole Length = Routed Path Length + Tool Size = Slot length as defined in the PCB layout

ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #:	REV: 1.0	SUN REV: Not In VersionControl
LAYER NAME = Micro Drawing	TID #: TIDM-1CHP-DTECT-CO		
PLOT NAME = Drill Drawing	GENERATED : 5/6/2016	10:29:27 AM	TEXAS INSTRUMENTS



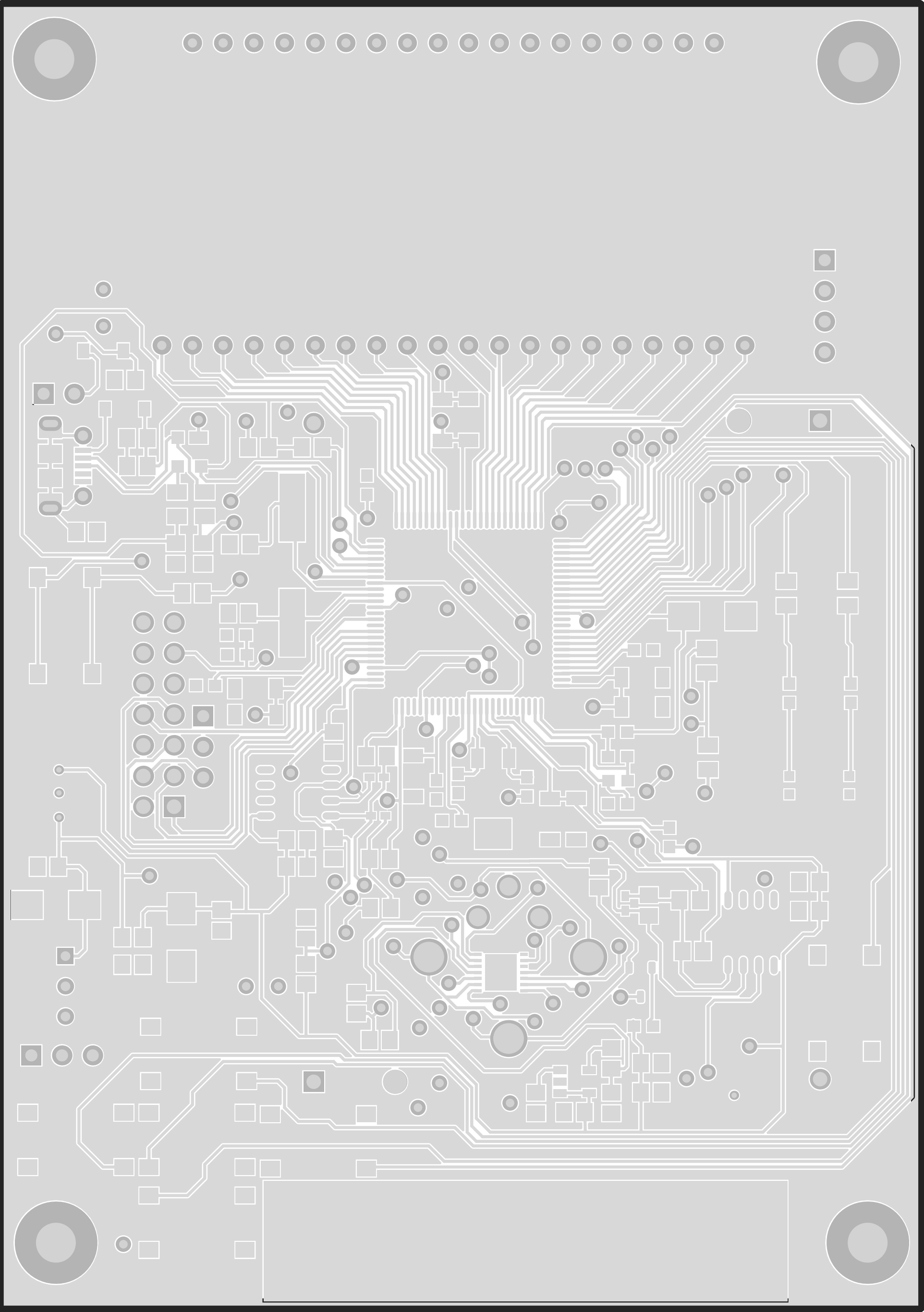
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #:	REV: 1.0	SVN REV: Not In VersionControl
LAYER NAME = M2 Board Dimensions	TID #: TIDM-1CHP-DTECT-CO		
PLOT NAME = Board Dimensions	GENERATED : 5/6/2016 10:29:31 AM		TEXAS INSTRUMENTS



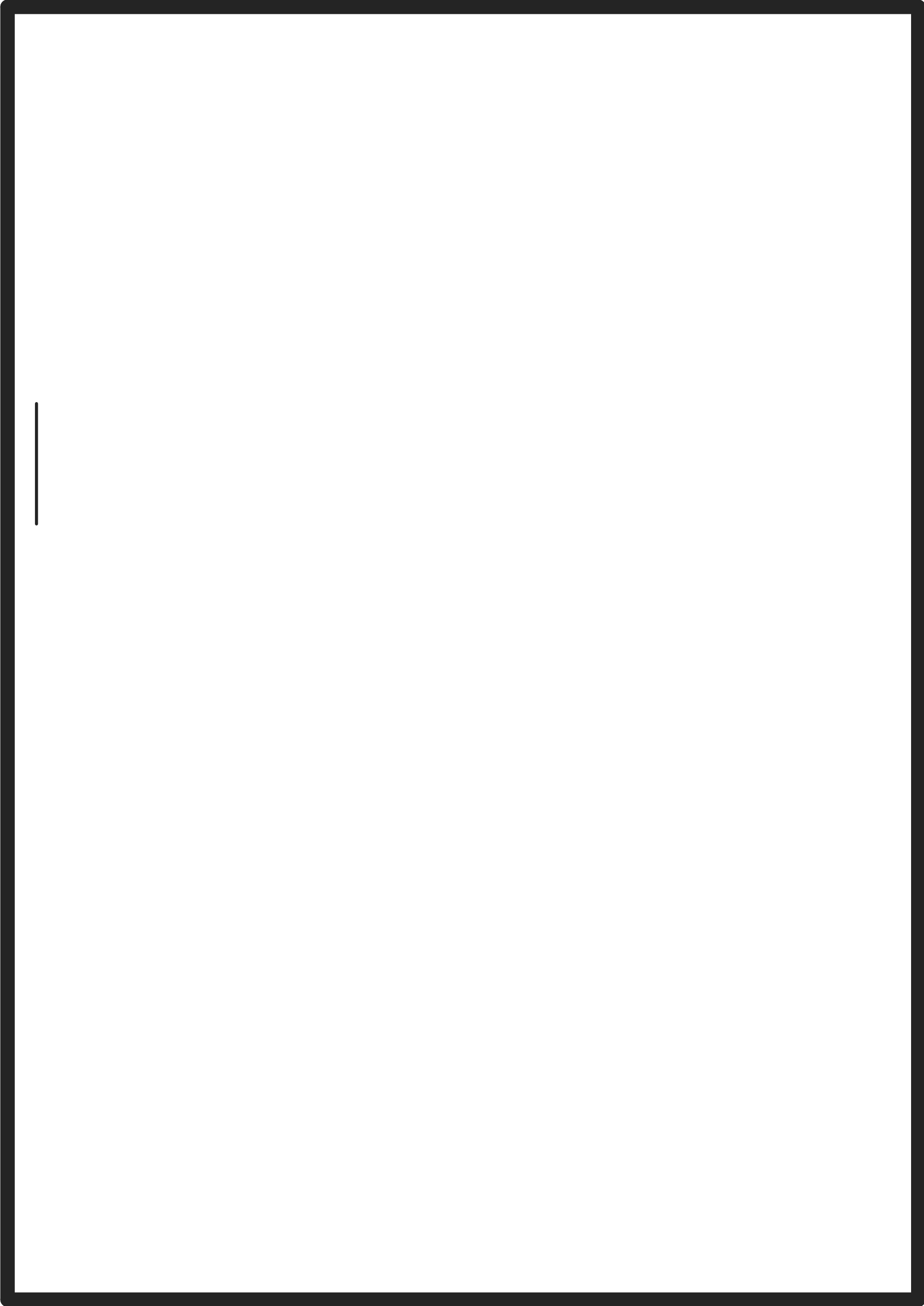
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #:	REV: 1.0	SVN REV: Not In VersionControl
LAYER NAME = Top Overlay	TID #: TIDM-1CHP-DTECT-CO		
PLOT NAME = Top Overlay	GENERATED : 5/6/2016 10:29:34 AM	TEXAS INSTRUMENTS	



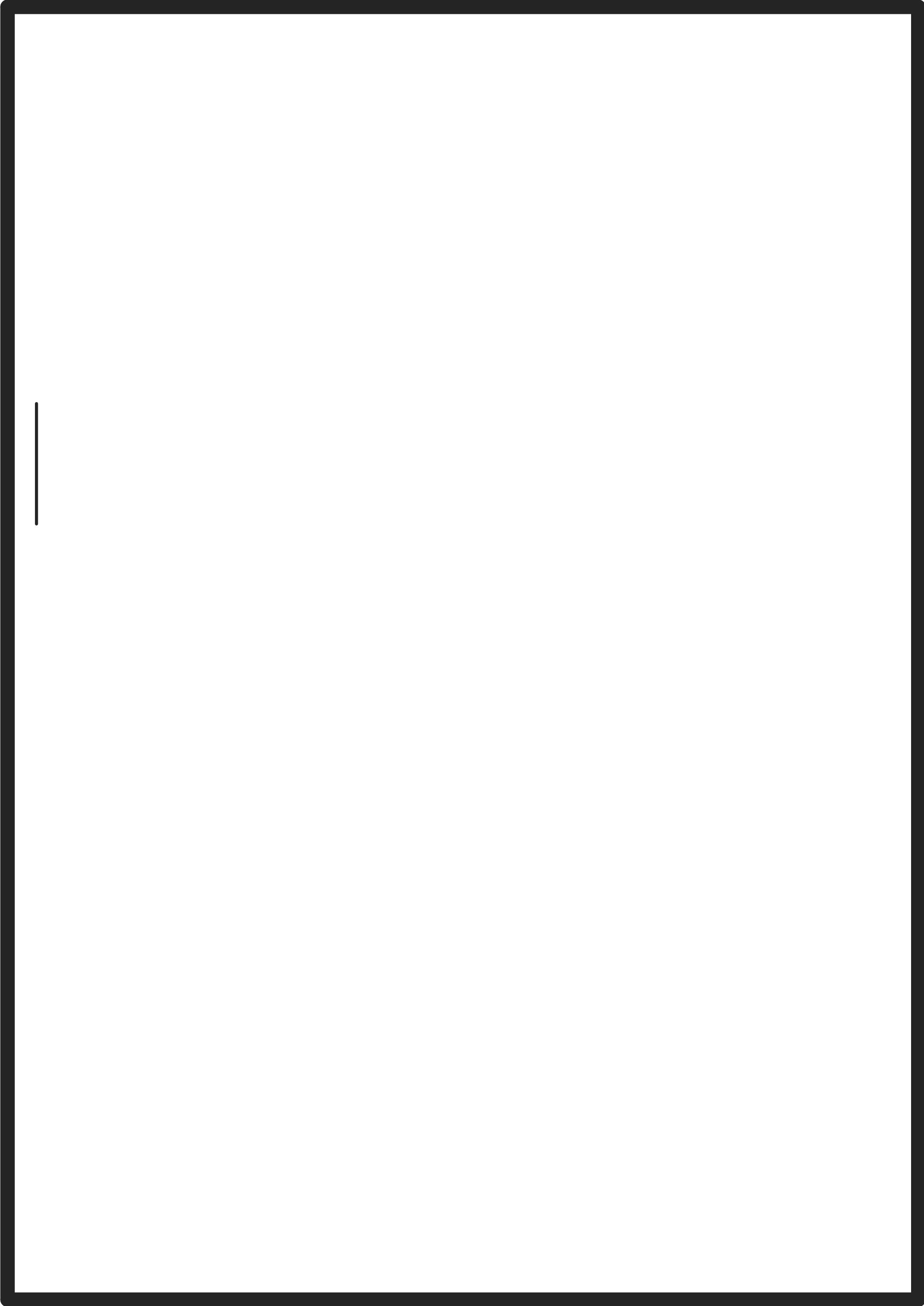
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #:	REV: 1.0	SVN REV: Not In VersionControl
LAYER NAME = Top Solder Outline	TID #: TIDM-1CHP-DTECT-CO		
PLOT NAME = Top Solder Mask	GENERATED : 5/6/2016 10:29:39 AM		TEXAS INSTRUMENTS



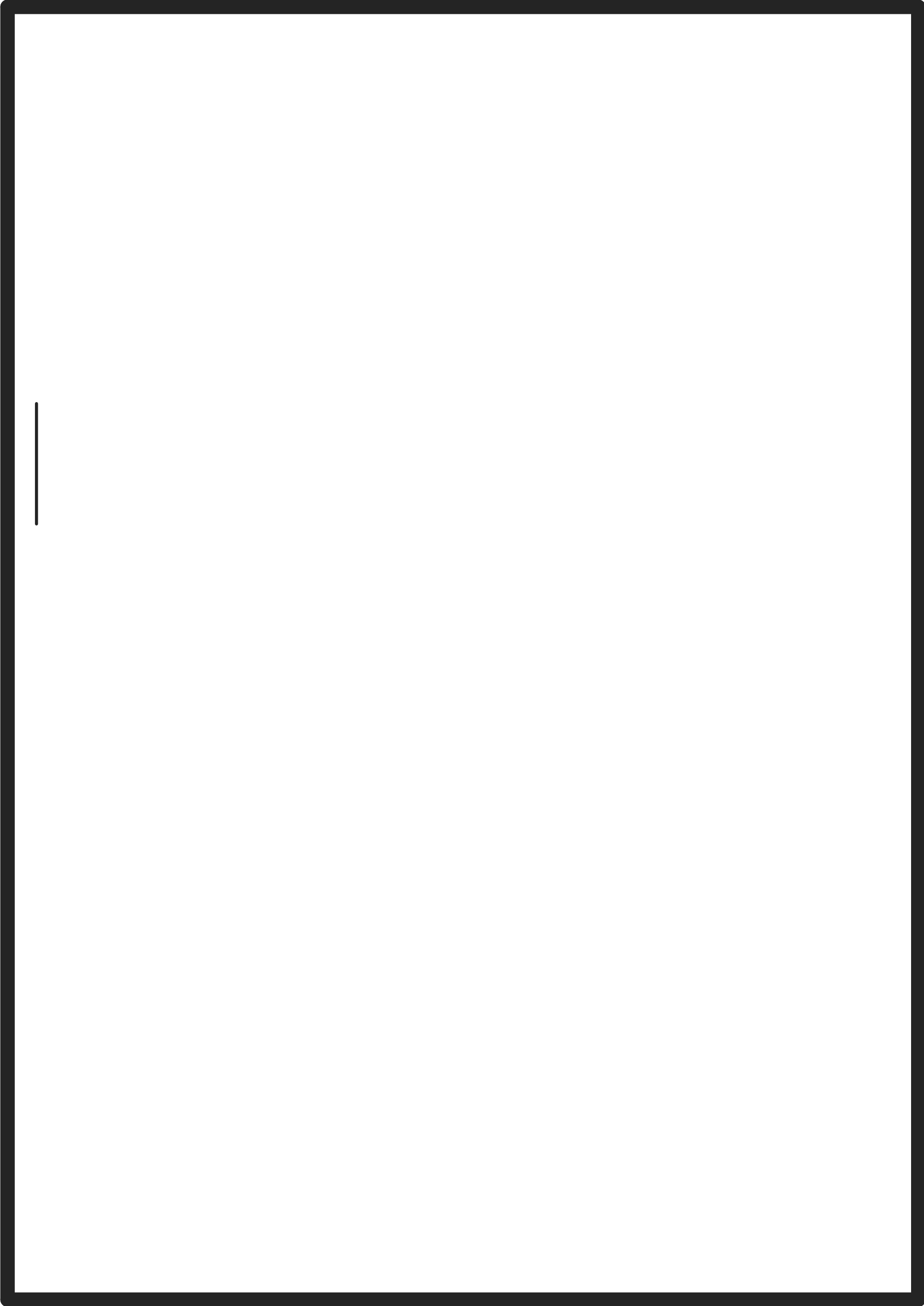
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #:	REV: 1.0	SVN REV: Not In VersionControl
LAYER NAME = M1Board Outline	TID #: TIDM-1CHP-DTECT-CO		
PLOT NAME = Top Layer	GENERATED : 5/6/2016 10:29:43 AM	TEXAS INSTRUMENTS	



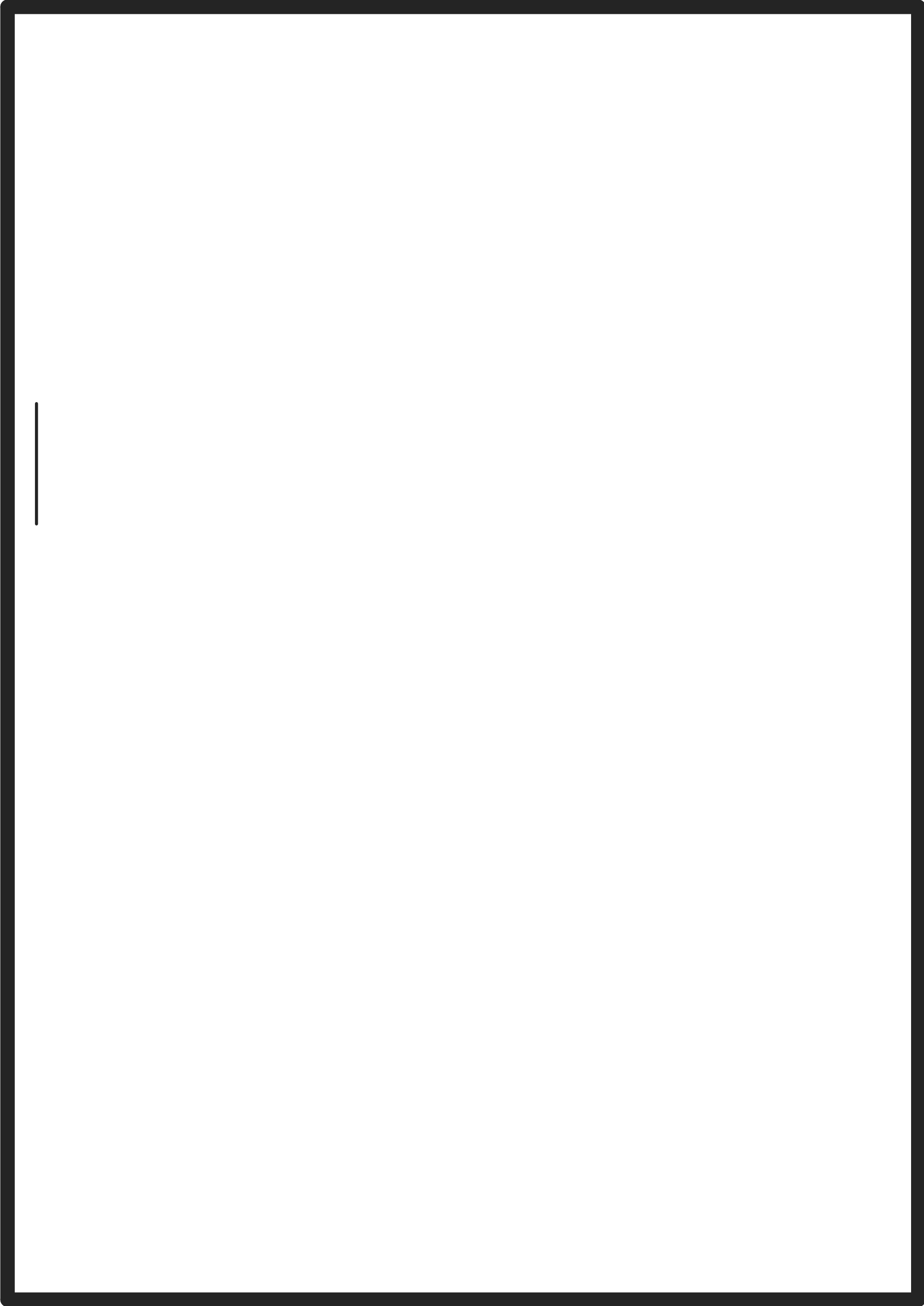
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #:	REV: 1.0	SVN REV: Not In VersionControl
LAYER NAME = M1 Board Outline	TID #: TIDM-1CHP-DTECT-CO		
PLOT NAME = Signal Layer 1	GENERATED : 5/6/2016 10:29:48 AM		TEXAS INSTRUMENTS



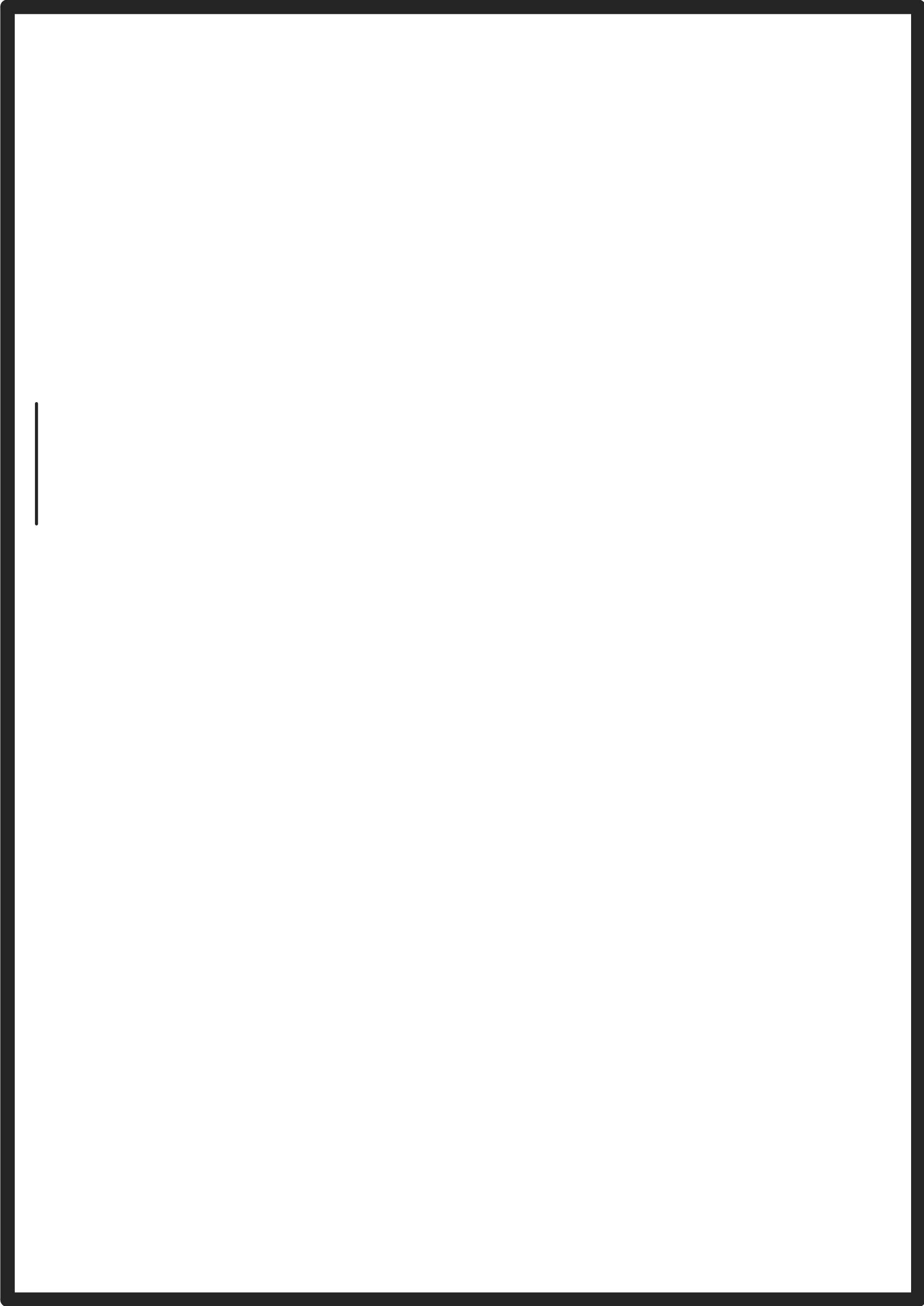
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #:	REV: 1.0	SVN REV: Not In VersionControl
LAYER NAME = M1 Board Outline	TID #: TIDM-1CHP-DTECT-CO		
PLOT NAME = Signal Layer 3	GENERATED : 5/6/2016 10:29:50 AM		TEXAS INSTRUMENTS



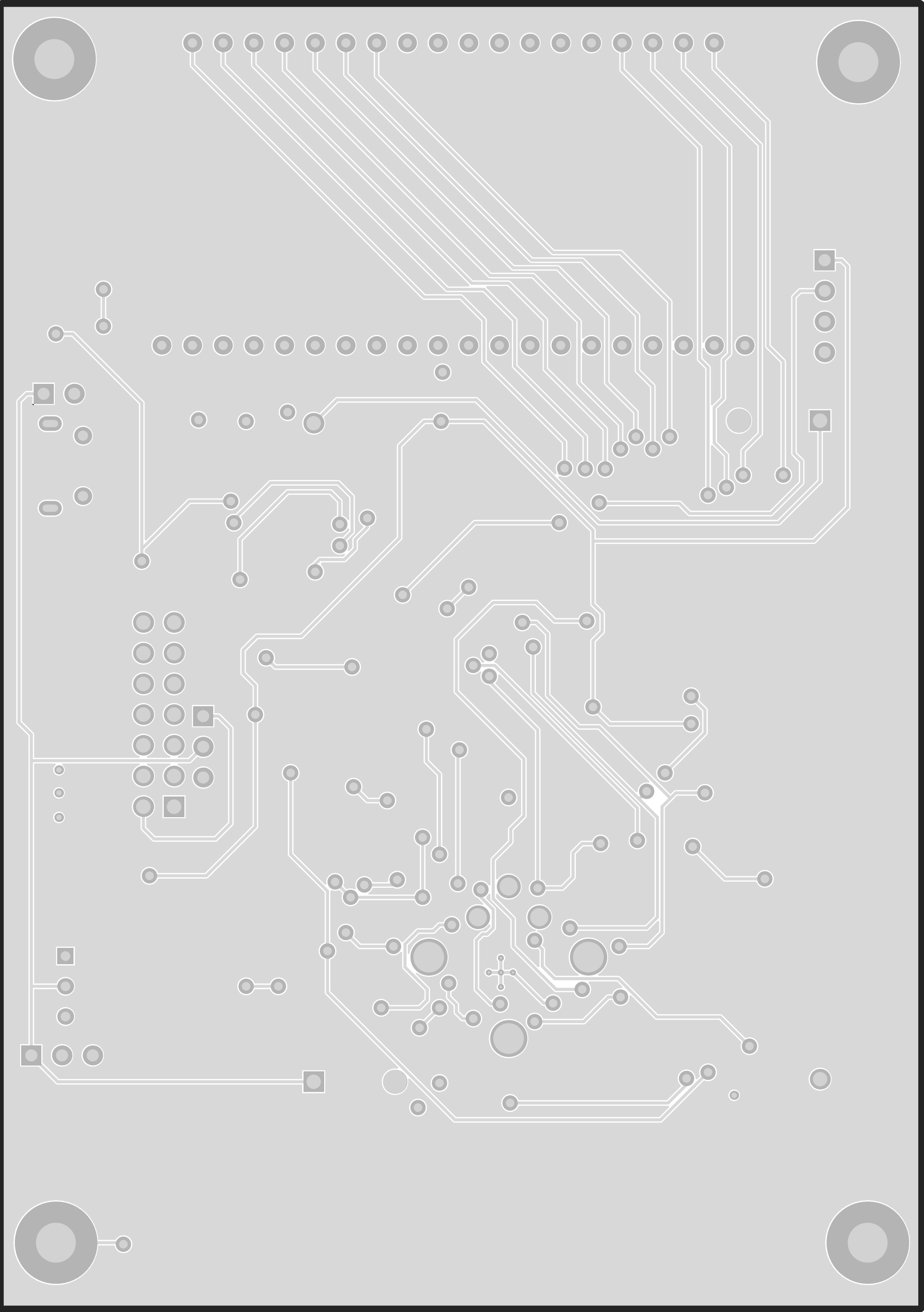
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #:	REV: 1.0	SVN REV: Not In VersionControl
LAYER NAME = M1 Board Outline	TID #: TIDM-1CHP-DTECT-CO		
PLOT NAME = Signal Layer 4	GENERATED : 5/6/2016 10:29:54 AM		TEXAS INSTRUMENTS



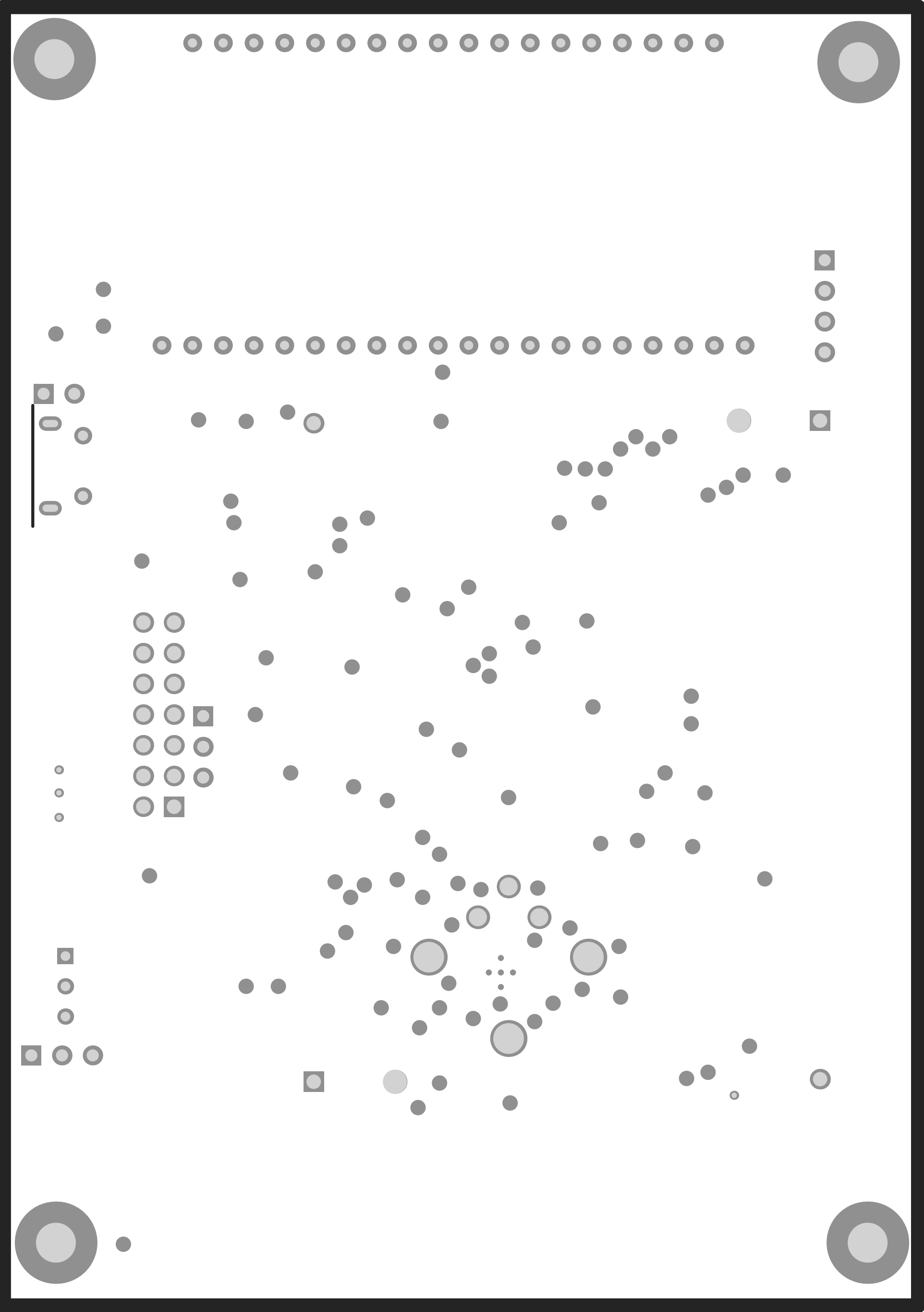
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #:	REV: 1.0	SVN REV: Not In VersionControl
LAYER NAME = M1 Board Outline	TID #: TIDM-1CHP-DTECT-CO		
PLOT NAME = Signal Layer 5	GENERATED : 5/6/2016 10:29:58 AM		TEXAS INSTRUMENTS



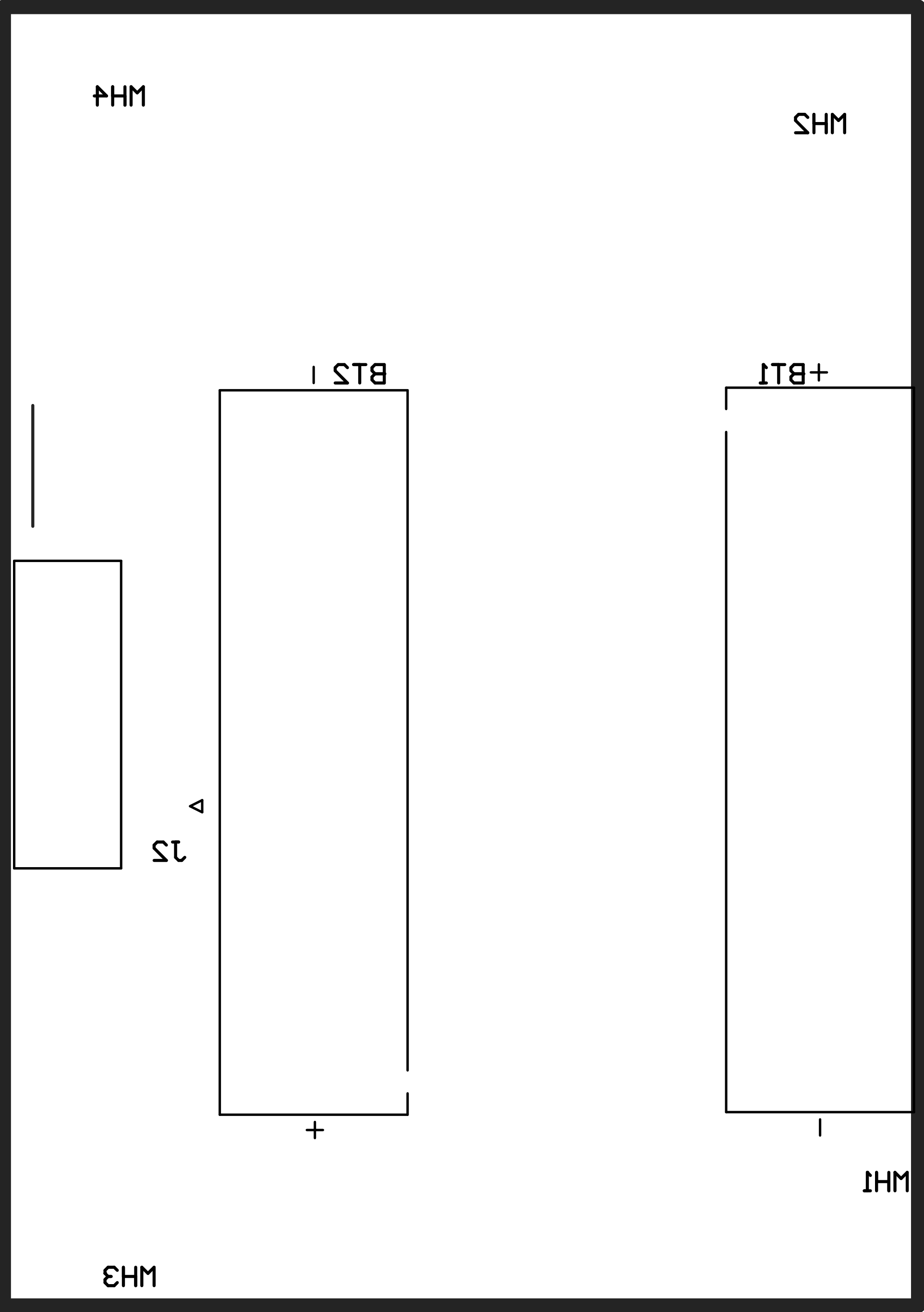
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #:	REV: 1.0	SVN REV: Not In VersionControl
LAYER NAME = M1 Board Outline	TID #: TIDM-1CHP-DTECT-CO		
PLOT NAME = Signal Layer 6	GENERATED : 5/6/2016 10:30:02 AM		TEXAS INSTRUMENTS



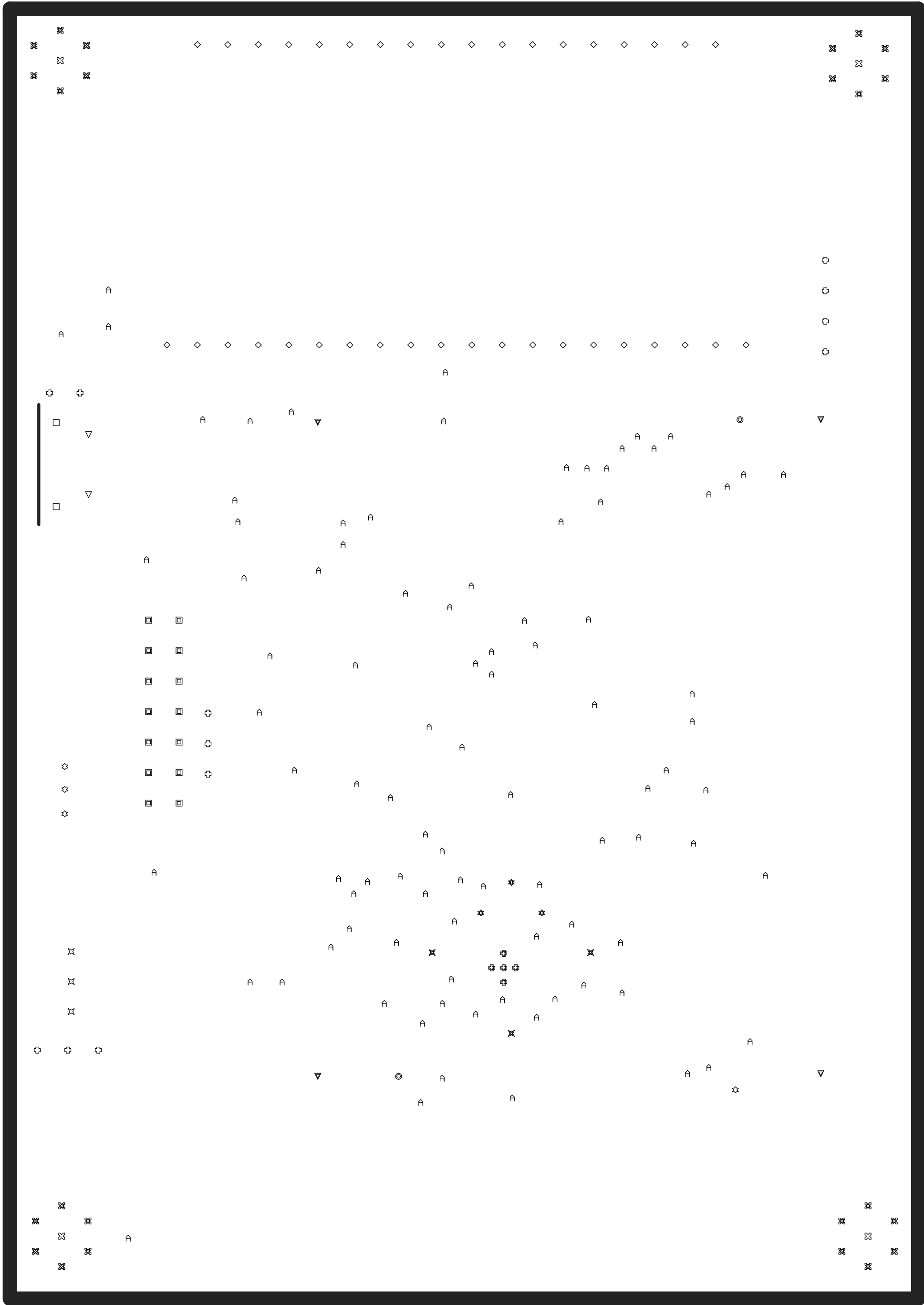
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #:	REV: 1.0	SVN REV: Not In VersionControl
LAYER NAME = 11 Board Outline	TID #: TIDM-1CHP-DTECT-CO		
PLOT NAME = Bottom Layer	GENERATED : 5/6/2016 10:30:06 AM		TEXAS INSTRUMENTS



ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #:	REV: 1.0	SVN REV: Not In VersionControl
LAYER NAME = Bottom Solder Mask	TID #: TIDM-1CHP-DTECT-CO		
PLOT NAME = Bottom Solder Mask	GENERATED : 5/6/2016 10:30:10 AM		TEXAS INSTRUMENTS



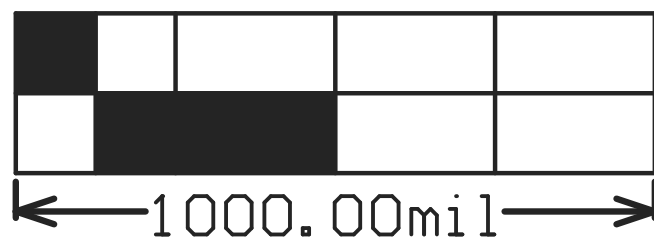
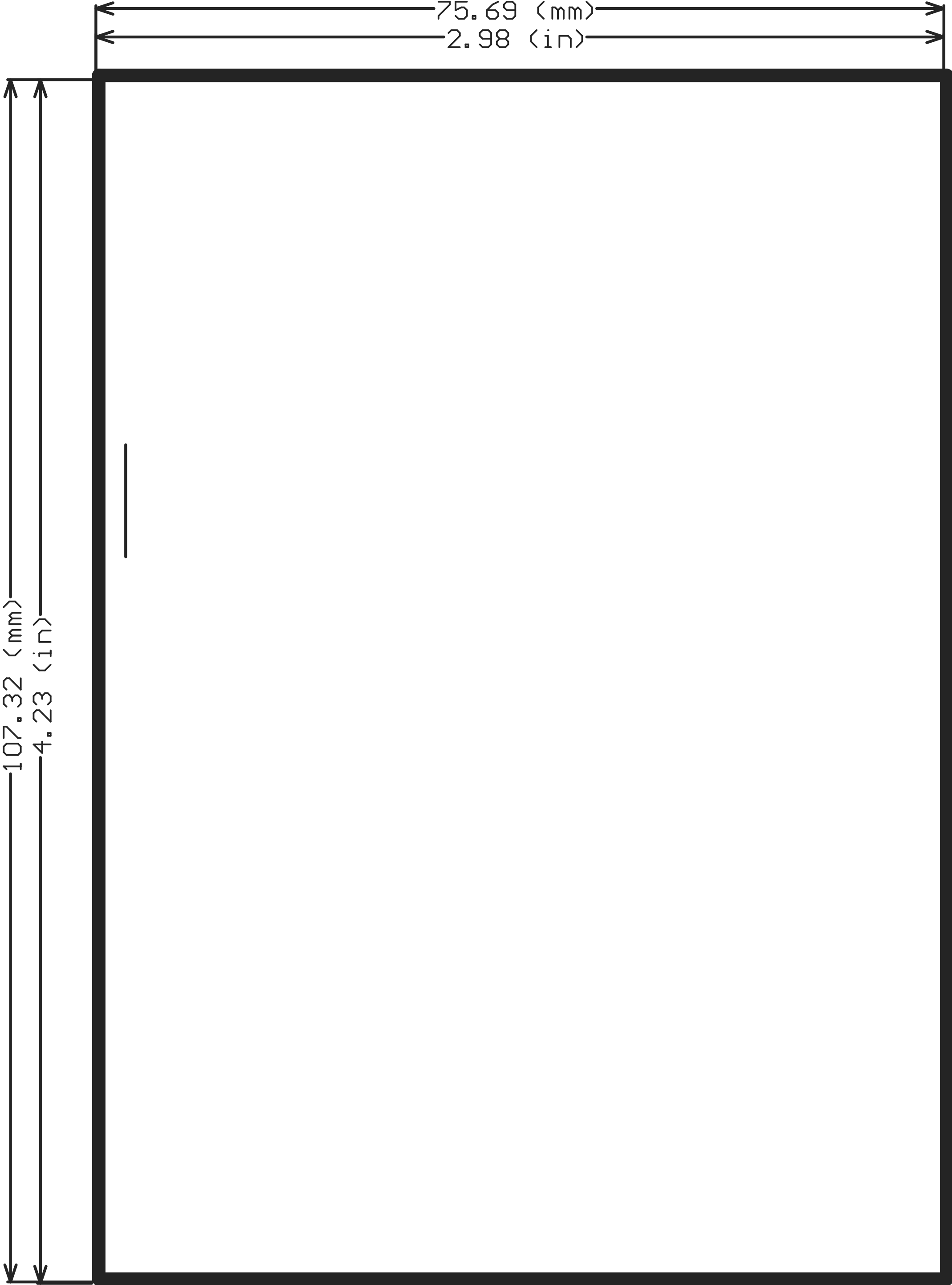
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #:	REV: 1.0	SVN REV: Not In VersionControl
LAYER NAME = Bot Board Outline	TID #: TIDM-1CHP-DTECT-CO		
PLOT NAME = Bottom Overlay	GENERATED : 5/6/2016 10:30:14 AM		TEXAS INSTRUMENTS



Symbol	Quantity	Finished Hole Size	Plated	Hole Type	Drill Layer Pair	Hole Tolerance
⦿	2	77.95mil (1.980mm)	NPTH	Round	Top Layer - Bottom Layer	
⊕	5	7.87mil (0.200mm)	PTH	Round	Top Layer - Bottom Layer	
☆	4	17.72mil (0.450mm)	PTH	Round	Top Layer - Bottom Layer	
⊗	24	19.69mil (0.500mm)	PTH	Round	Top Layer - Bottom Layer	
⌒	94	28.00mil (0.711mm)	PTH	Round	Top Layer - Bottom Layer	
◇	38	30.00mil (0.762mm)	PTH	Round	Top Layer - Bottom Layer	
✕	3	31.50mil (0.800mm)	PTH	Round	Top Layer - Bottom Layer	
▽	2	33.46mil (0.850mm)	PTH	Round	Top Layer - Bottom Layer	
⊕	12	39.37mil (1.000mm)	PTH	Round	Top Layer - Bottom Layer	
▽	4	46.85mil (1.190mm)	PTH	Round	Top Layer - Bottom Layer	
▣	14	47.24mil (1.200mm)	PTH	Round	Top Layer - Bottom Layer	
☆	3	59.06mil (1.500mm)	PTH	Round	Top Layer - Bottom Layer	
✕	3	100.00mil (2.540mm)	PTH	Round	Top Layer - Bottom Layer	
⊗	4	129.92mil (3.300mm)	PTH	Round	Top Layer - Bottom Layer	
□	2	23.62mil (0.600mm)	PTH	Slot	Top Layer - Bottom Layer	
	214 Total					

Slot definitions : Routed Path Length = Calculated from tool start centre position to tool end centre position.
Hole Length = Routed Path Length + Tool Size = Slot length as defined in the PCB layout

ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #:	REV: 1.0	SVN REV: Not In VersionControl
LAYER NAME = Micro Drawing	TID #: TIDM-1CHP-DTECT-CO		
PLOT NAME = Drill Drawing	GENERATED : 5/6/2016	10:30:18 AM	TEXAS INSTRUMENTS



ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #:	REV: 1.0	SVN REV: Not In VersionControl
LAYER NAME = M2 Board Dimensions	TID #: TIDM-1CHP-DTECT-CO		
PLOT NAME = Board Dimensions	GENERATED : 5/6/2016 10:30:22 AM		TEXAS INSTRUMENTS