Layer Stack Up Detail for: PMP6021E2.PcbDoc

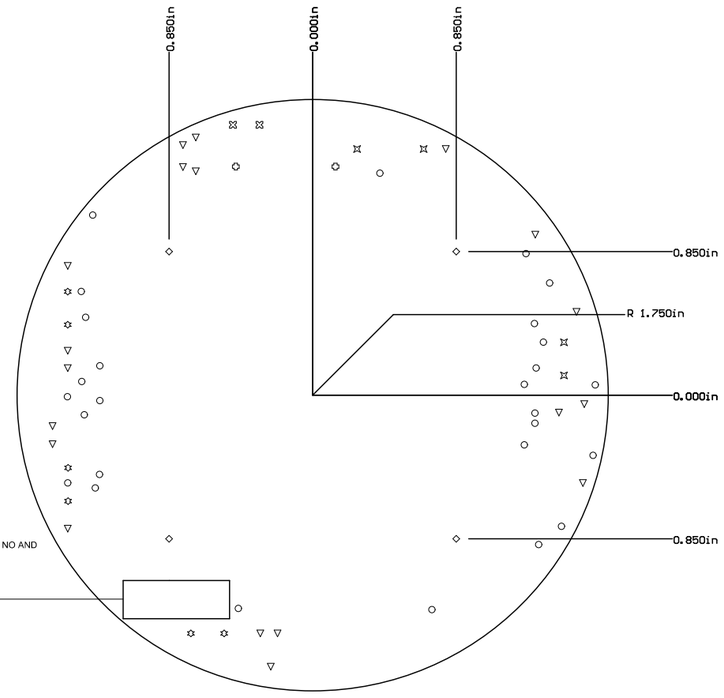
Layer Name	Gerber Document	Copper Thickness	Dielectric Material
Top Solder Mask	(.GTS)		Solder Resist
Top Layer	(.GTL)	2.2mil	
MidLayer1	(.G1)	2.8mil	FR-4
MidLayer2	(.G2)	2.8mil	FR-4
Bottom Layer	(.GBL)	2.2mil	FR-4
Bottom Solder Mask	(.GBS)		Solder Resist

DESIGN INFORMATION

BOARD SIZE (REFER ALSO ARRAY/PANEL PROFILING INFORMATION)
3500MIL X 3500MIL

Number of Layers : 4
MIN. TRACK WIDTH: 8 MIL
MIN. CLEARANCE: 8 MIL
MIN. VIA PAD SIZE: 34 MIL
MINIMUM ANNULAR RING 0.05mm (2MIL) EXTERNAL
PER IPC-D-275 CLASS 2 LEVEL C
REGISTRATION TOLERANCES: METAL +/- 5 MIL, HOLES +/- 3 MIL

MATERIAL:
[ ] FR-408 [X] FR-4 High Tg [ ] OTHER
THICKNESS: [ ] 62 MIL (1.6mm) +/-10% [X] OTHER 31MIL +/- 10%
TOLERANCE: [X] ANSI IPC-6012 TYPE 3 CLASS 2
[ ] OTHER +/-
BOW & TWIST: [X] ANSI IPC-6012 TYPE 3 CLASS 2
[ ] OTHER +/-
COPPER THICKNESS (FINISHED):
OUTER: [ ] 1.4MIL (1oz) [ ] 2MIL (1.4oz) [X] 2.8MIL (2oz)
INNER SIGNAL: [ ] 1.4MIL (1oz) [X] 2.8MIL (2oz) [ ] N/A
DRILLING:
REFERENCE: [X] AS SHOWN [X] NC_DRILL FILES
PTH MIN COPPER THICKNESS: [X] 1MIL [ ] OTHER
BOARD FINISH:
SILKSCREEN: [X] TOP [ ] BOTTOM
SILKSCREEN COLOR: [X] WHITE [ ] OTHER
SOLDER RESIST COLOR:
[X] GREEN [ ] BLUE [ ] OTHER
SURFACE FINISH: [ ] IMMERSION GOLD (ENIG) [ ] ENEPIG
[X] IMM. TIN/SILVER OR EQUIV [ ] OTHER
ARRAY/PANEL: [ ] CUT AND TRIM PER MECH LAYER 1
[ ] N.C. ROUTE [ ] V. SCORE
CERTIFICATION: MATERIALS AND WORKMANSHIP FOR ALL PCBS TO MEET OR EXCEED THE REQUIREMENTS OF:
[X] ANSI IPC-A-600F CLASS -> [ ] 1 [X] 2 [ ] 3
[X] UL 94V-0 [X] RoHS [ ] OTHER
ADDITIONAL REQUIREMENTS:
MICROSECTION: [ ] YES
BARE BOARD ELEC. TEST: [X] NONE [ ] REQUIRED [ ] PER ORDER
MANUFACTURER'S UL: [ ] RAIL [ ] METAL [X] SILK



Symbol	Hit Count	Tool Size	Plated	Hole Type
○	27	16mil (0.406mm)	PTH	Round
⌑	4	35.433mil (0.9mm)	PTH	Round
✡	6	37mil (0.94mm)	PTH	Round
◌	2	39.37mil (1mm)	PTH	Round
⊠	2	42.079mil (1.069mm)	PTH	Round
▽	19	43mil (1.092mm)	PTH	Round
◇	4	128mil (3.251mm)	NPTH	Round
	64 Total			

Drill Table

TEXAS INSTRUMENTS

PROJECT TITLE: 4-CH 230V 410 Linear
DESIGNED FOR: Public Release
FILE NAME: PMP6021E2.PcbDoc

ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: PMP6021	REV: E2	SVN REV: Not In VersionControl
LAYER NAME = [illegible]ions			
PLOT NAME = Fabrication Drawing	GENERATED : 12/15/2014 11:07:59 AM	TEXAS INSTRUMENTS	

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ENGINEER:	LAYOUT BY:
Irwin Nederbragt	Kerry Murphy
SCALE: 1.09	ALTIUM DESIGNER VERSION: 14.3.14.34663