VIN
GND
J1
2 VIN
1

C1
10µF
25V

U1
6 VIN
7 EN
8 MODE/S-CONF
GND 5
SW 4
VOS 3
FB/VSET 1
PG 2
TPS629210QDRLRQ1

MODE/S-CONF

R3
27.4k

L1
DFE201210U-1R5M=P2
SW

R1
DNP

R2
DNP

VOUT
C2
22µF
10V

J2
1
2
VOUT
GND

FID1 FID2 FID3 FID4 FID5 FID6

PCB Number: TIDA-050056
PCB Rev: E1

PCB
LOGO
Texas Instruments

PCB
LOGO
FCC disclaimer

CE Mark

PCB
LOGO
WEEE logo

Variant/Label Table	
Variant	Label Text
001	TIDA-050056

ZZ1
Label Assembly Note
This Assembly Note is for PCB labels only

ZZ2
Assembly Note
These assemblies are ESD sensitive, ESD precautions shall be observed.

ZZ3
Assembly Note
These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.

ZZ4
Assembly Note
These assemblies must comply with workmanship standards IPC-A-610 Class 2, unless otherwise specified.

Orderable:
TID #:
Number: TIDA-050056 Rev: E1
SVN Rev: Not in version control
Drawn By: Nancy Zhang
Engineer: Nancy Zhang

Designed for: Public Release   Mod. Date: 12/9/2021
Project Title: TPS629210-Q1EVM
Sheet Title:
Assembly Variant: [No Variations]   Sheet: 2 of 2
File: TIDA_050056_Hardware.SchDoc   Size: B
Contact: http://www.ti.com/support

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