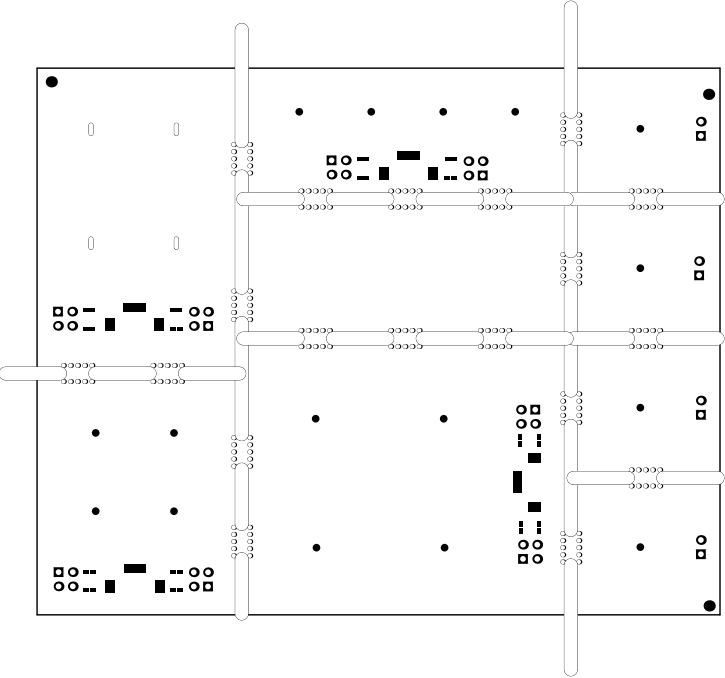
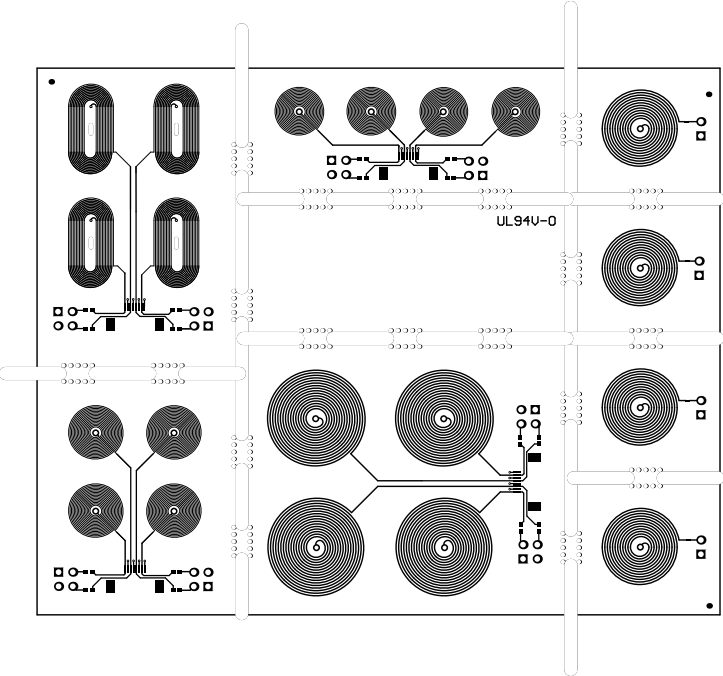


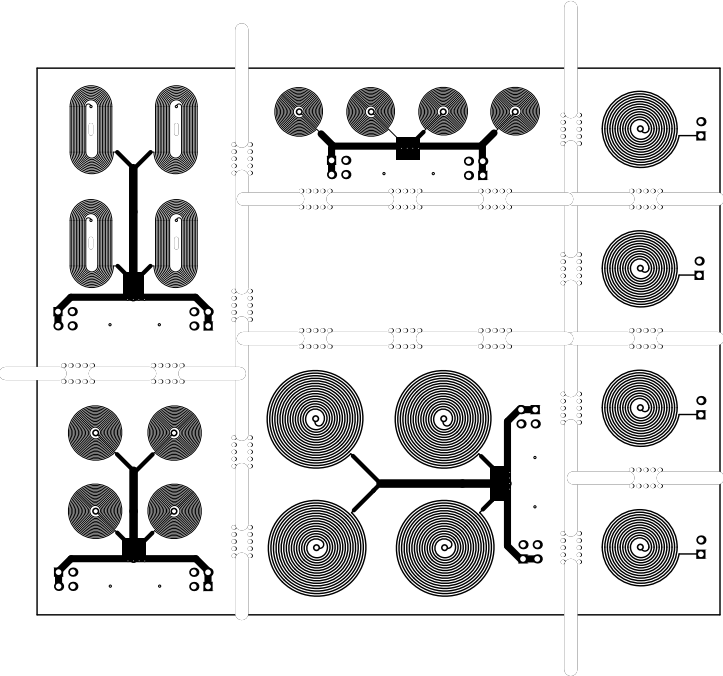
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: SU601322	REV: B	SUN REV: Not In VersionControl
LAYER NAME = Top Overlay	TID #: N/A		
PLOT NAME = Top Overlay	GENERATED : 11/3/2016 1:38:27 PM		TEXAS INSTRUMENTS



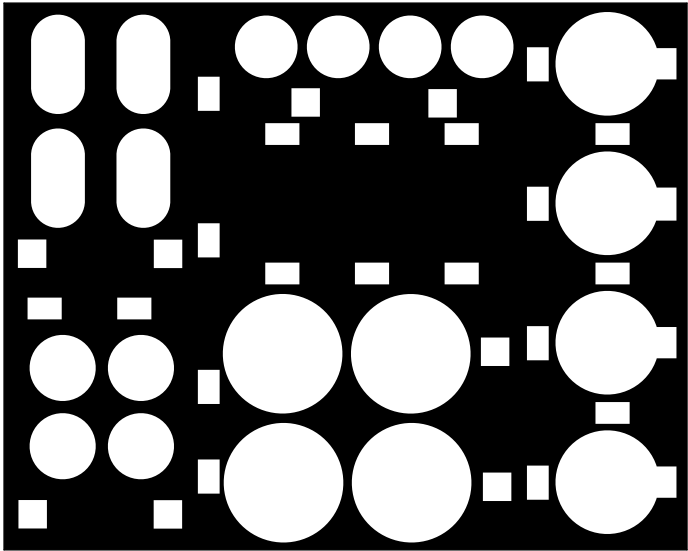
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: SU601322	REV: B	SUN REV: Not In VersionControl
LAYER NAME = Top Solder	TID #: N/A		
PLOT NAME = Top Solder Mask	GENERATED : 11/3/2016	1:38:28 PM	TEXAS INSTRUMENTS



ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: SU601322	REV: B	SUN REV: Not In VersionControl
LAYER NAME = Top Layer	TID #: N/A		
PLOT NAME = Top Layer	GENERATED : 11/3/2016	1:38:29 PM	TEXAS INSTRUMENTS



ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: SU601322	REV: B	SUN REV: Not In VersionControl
LAYER NAME = Bottom Layer	TID #: N/A		
PLOT NAME = Bottom Layer	GENERATED : 11/3/2016	1:38:30 PM	TEXAS INSTRUMENTS

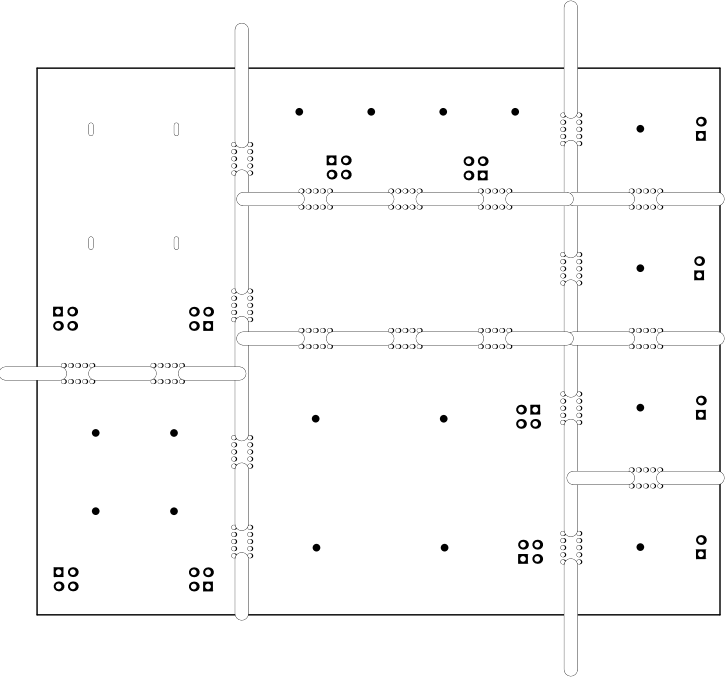


MANUFACTURING NOTES

1. Add 10 MIL thick FR4 spacer to bottom of PCB.  
Layer 'M10 Fab Notes' shows the outline of the spacer.  
Bottom of the PCB does not contain silkscreen.

DESIGN INFORMATION	
MIN. TRACK WIDTH:	5 MIL
MIN. CLEARANCE:	5MIL
MIN. VIA PAD SIZE:	8 MIL
MINIMUM ANNULAR RING 7.5MIL EXTERNAL	
PER IPC-D-275 CLASS 2 LEVEL C	
REGISTRATION TOLERANCES: METAL +/- 5 MIL, HOLES +/- 3 MIL	
HOLE SIZE TOLERANCE (UNLESS OTHERWISE SPECIFIED): +/- 3 MIL	
MATERIAL:	
<input type="checkbox"/> FR-408	<input checked="" type="checkbox"/> FR-4 High Tg <input type="checkbox"/> OTHER
THICKNESS: <input checked="" type="checkbox"/> 41MIL +/-10%	<input type="checkbox"/> OTHER
TOLERANCE:	<input checked="" type="checkbox"/> ANSI IPC-6012 TYPE 3 CLASS 2
	<input type="checkbox"/> OTHER +/-
BOW & TWIST:	<input checked="" type="checkbox"/> ANSI IPC-6012 TYPE 3 CLASS 2
	<input type="checkbox"/> OTHER +/-
DRILLING:	
REFERENCE: <input checked="" type="checkbox"/> AS SHOWN	<input checked="" type="checkbox"/> NC_DRILL FILES
PTH COPPER THICKNESS: <input checked="" type="checkbox"/> 20-30 um	<input type="checkbox"/> OTHER
BOARD FINISH:	
SILKSCREEN: <input checked="" type="checkbox"/> TOP	<input type="checkbox"/> BOTTOM
SILKSCREEN COLOR: <input checked="" type="checkbox"/> WHITE	<input type="checkbox"/> OTHER
SOLDER RESIST COLOR: <input checked="" type="checkbox"/> GREEN	<input type="checkbox"/> OTHER
	<input checked="" type="checkbox"/> MATTE <input type="checkbox"/> SEMI-GLOSS
SURFACE FINISH: <input checked="" type="checkbox"/> IMMERSION GOLD (ENIG) <input type="checkbox"/> ENERPIG	
<input type="checkbox"/> IMM. TIN/SILVER OR EQUIV	<input type="checkbox"/> OTHER
ARRAY/PANEL:	
<input type="checkbox"/> CUT AND TRIM PER M1 BOARD OUTLINE	<input type="checkbox"/> V. SCORE
<input checked="" type="checkbox"/> N.C. ROUTE	
CERTIFICATION: MATERIALS AND WORKMANSHIP FOR ALL PCBs TO MEET OR EXCEED THE REQUIREMENTS OF:	
<input checked="" type="checkbox"/> ANSI IPC-A-600F CLASS ->	<input type="checkbox"/> 1 <input checked="" type="checkbox"/> 2 <input type="checkbox"/> 3
<input checked="" type="checkbox"/> RoHS	<input type="checkbox"/> OTHER PER ORDER
ALL BOARDS MUST MEET OR EXCEED UL94-V0 REQUIREMENTS. PCB MUST BEAR THE UL94V-0 UL REGISTERED MATERIAL ID NUMBER	
ADDITIONAL REQUIREMENTS:	
MICROSECTION: <input type="checkbox"/> YES	
BARE BOARD ELEC. TEST: <input type="checkbox"/> NONE <input checked="" type="checkbox"/> REQUIRED <input type="checkbox"/> PER ORDER	

ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: SU60I322	REV: B	SUN REV: Not In VersionControl
LAYER NAME = M10B6abdn8tue\$ine	TID #: N/A		
PLOT NAME = Bottom Spacer	GENERATED : 11/3/2016 1:38:31 PM		TEXAS INSTRUMENTS



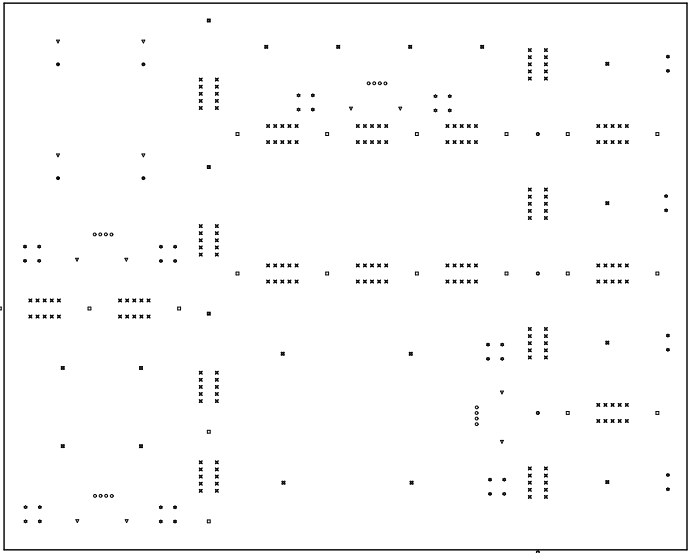
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: SU601322	REV: B	SUN REV: Not In VersionControl
LAYER NAME = Bottom Solder	TID #: N/A		
PLOT NAME = Bottom Solder Mask	GENERATED : 11/3/2016	1:38:32 PM	TEXAS INSTRUMENTS



ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: SU601322	REV: B	SUN REV: Not In VersionControl
LAYER NAME = M10 Fab Notes	TID #: N/A		
PLOT NAME = Bottom Overlay	GENERATED	: 11/3/2016 1:38:33 PM	TEXAS INSTRUMENTS

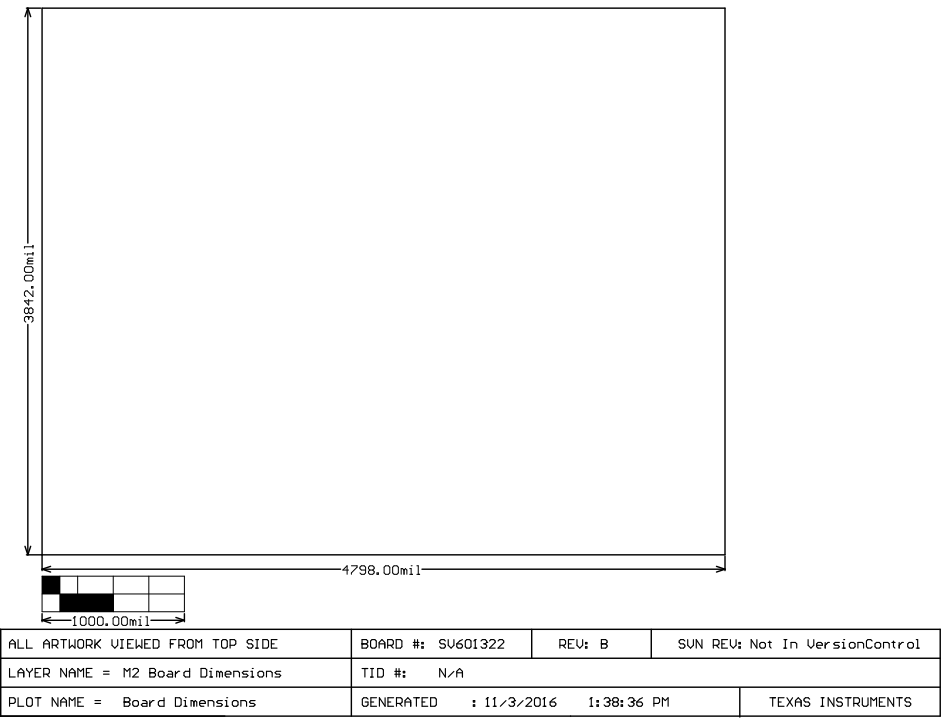
Symbol	Quantity	Finished Hole Size	Plated	Hole Type	Drill Layer Pair	Hole Tolerance	Hole Length
⊠	190	31.00mil (0.787mm)	NPTH	Round	Top Layer - Bottom Layer		-
○	16	6.00mil (0.152mm)	PTH	Round	Top Layer - Bottom Layer		•
▽	12	8.00mil (0.203mm)	PTH	Round	Top Layer - Bottom Layer		-
⊠	16	28.00mil (0.711mm)	PTH	Round	Top Layer - Bottom Layer		-
☆	40	40.00mil (1.016mm)	PTH	Round	Top Layer - Bottom Layer		•
⊙	4	31.00mil (0.787mm)	NPTH	Slot	Top Layer - Bottom Layer		96.00mil (2.286mm)
□	19	93.00mil (2.362mm)	NPTH	Slot	Top Layer - Bottom Layer		475.00mil (12.065mm)
⊕	5	93.00mil (2.362mm)	NPTH	Slot	Top Layer - Bottom Layer		825.00mil (20.955mm)
⊞	3	93.00mil (2.362mm)	NPTH	Slot	Top Layer - Bottom Layer		875.00mil (22.225mm)
	305 Total						

Slot definitions : Routed Path Length = Calculated from tool start centre position to tool end centre position.  
Hole Length = Routed Path Length - Tool Size + Slot length as defined in the PCB layout



ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: SU601322	REV: B	SUN REV: Not In VersionControl
LAYER NAME = Drill Drawing	TID #: N/A		
PLOT NAME = Drill Drawing	GENERATED : 11/3/2016 1:38:34 PM		TEXAS INSTRUMENTS





ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: SU601322	REV: B	SUN REV: Not In VersionControl
LAYER NAME = M2 Board Dimensions	TID #: N/A		
PLOT NAME = Board Dimensions	GENERATED : 11/3/2016 1:38:36 PM	TEXAS INSTRUMENTS	