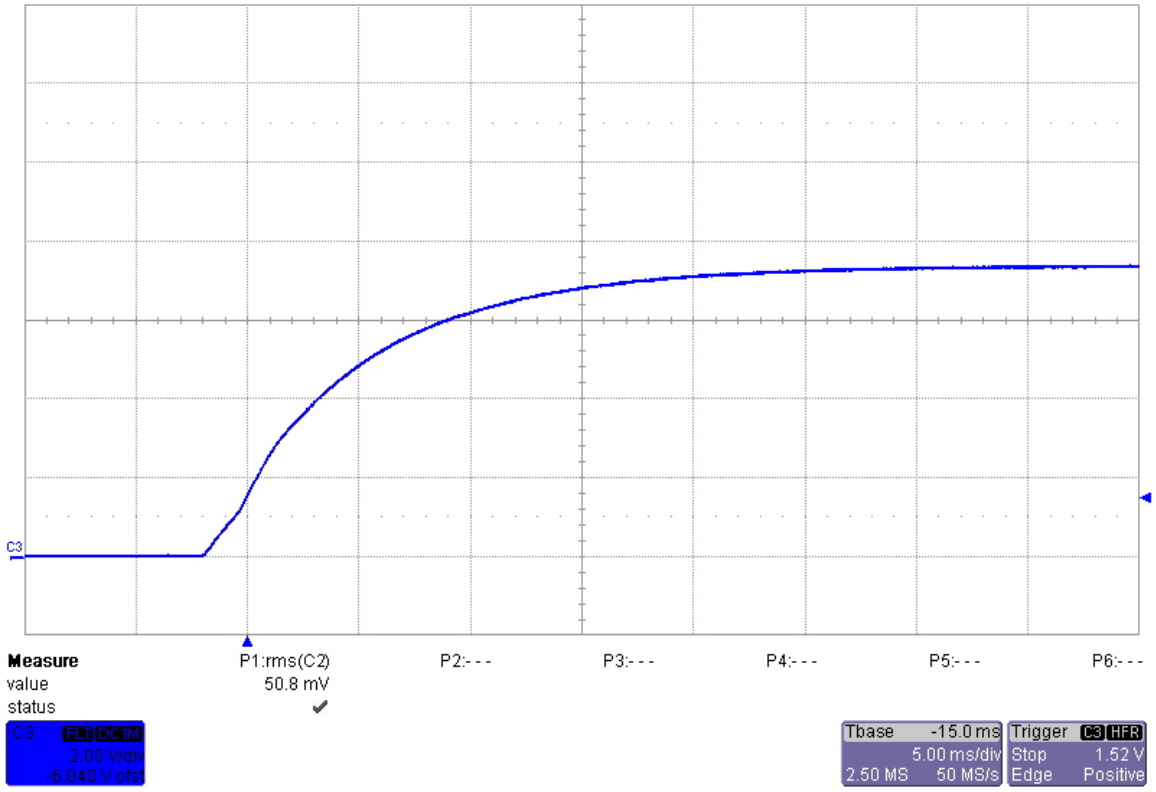
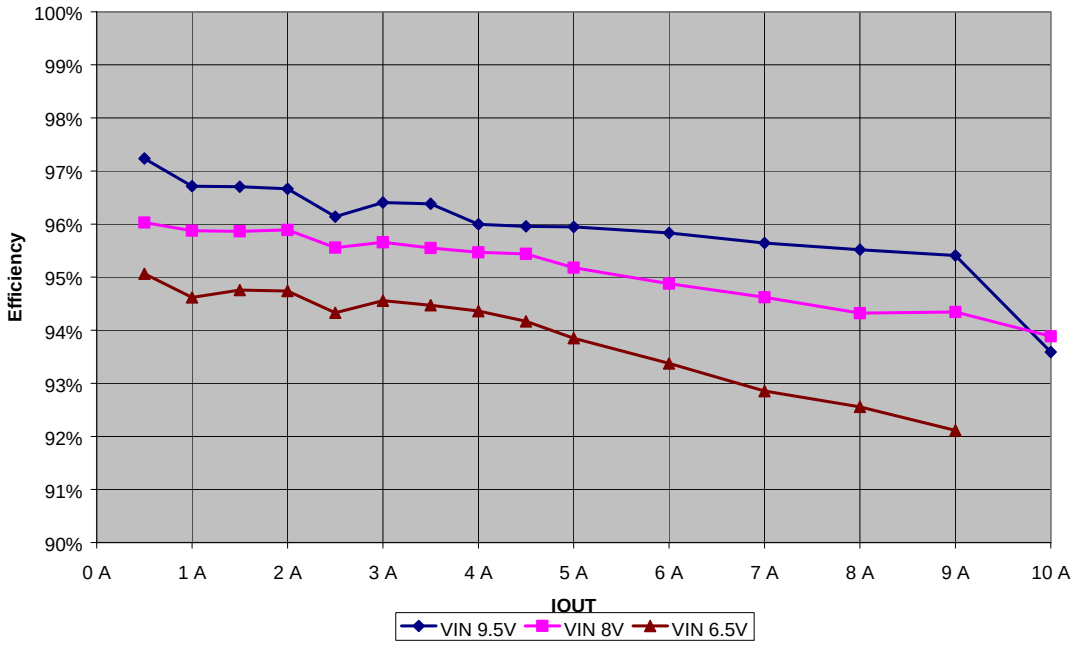
1 Startup

The startup waveform is shown in the figure below. The input voltage was set at 8V, with 10A load on the output.



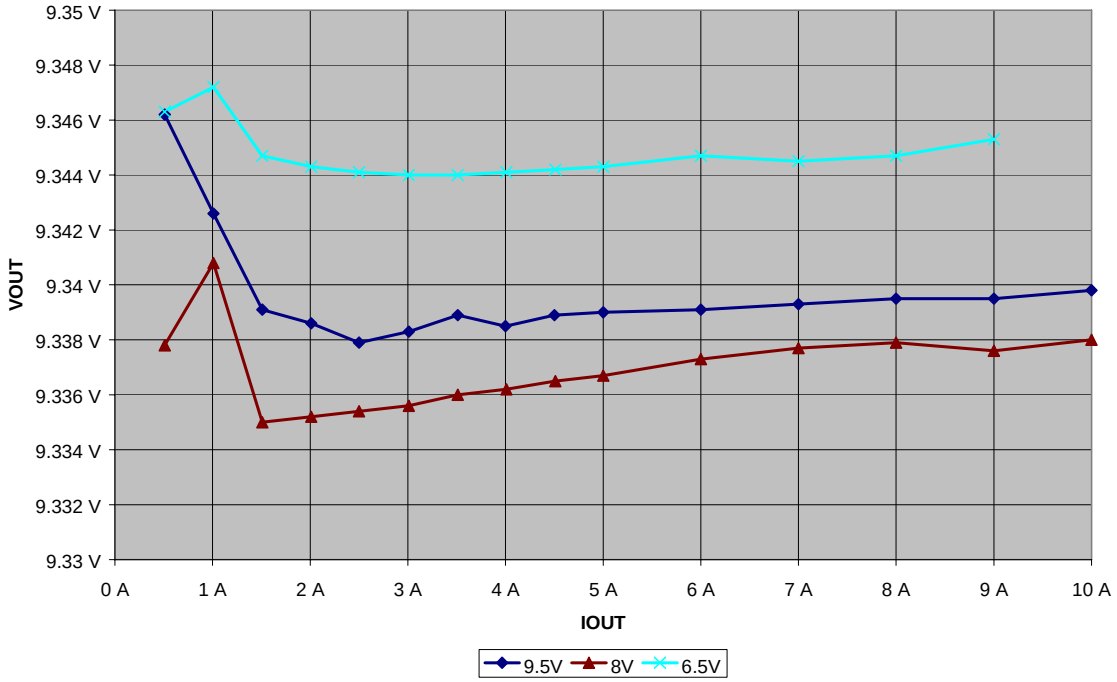
2 Efficiency

The efficiency is shown in the figure below.



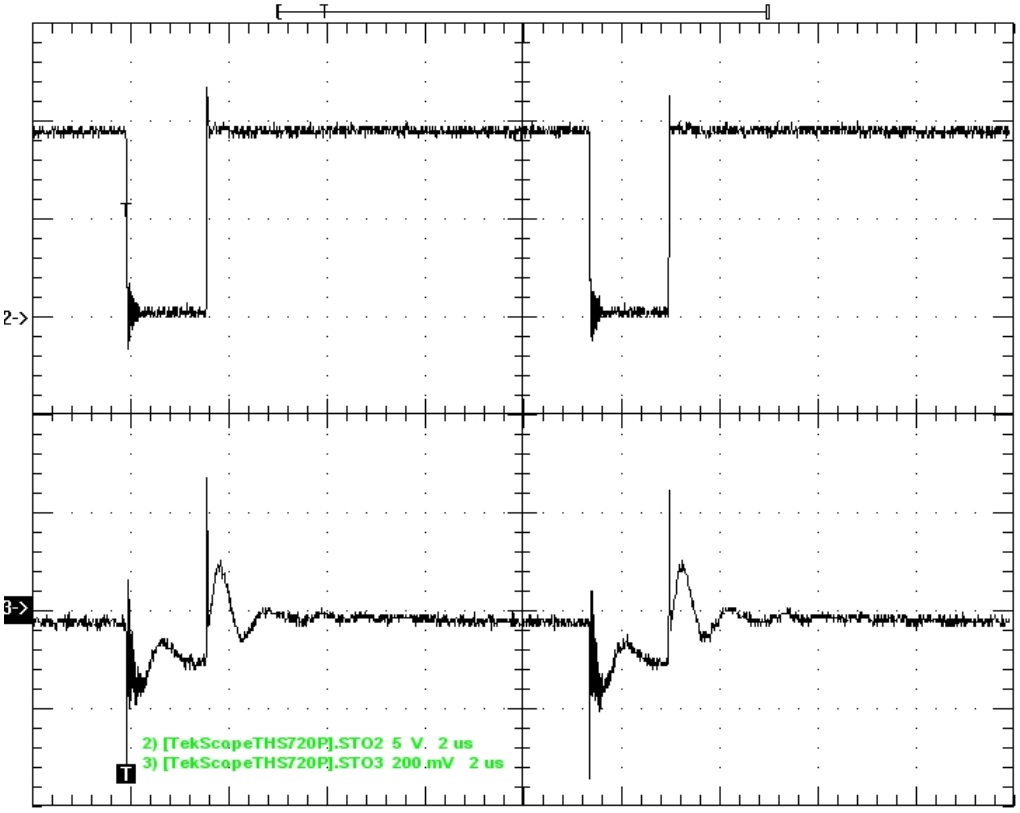
3 Load Regulation

The load regulation of the output is shown in the graph below.



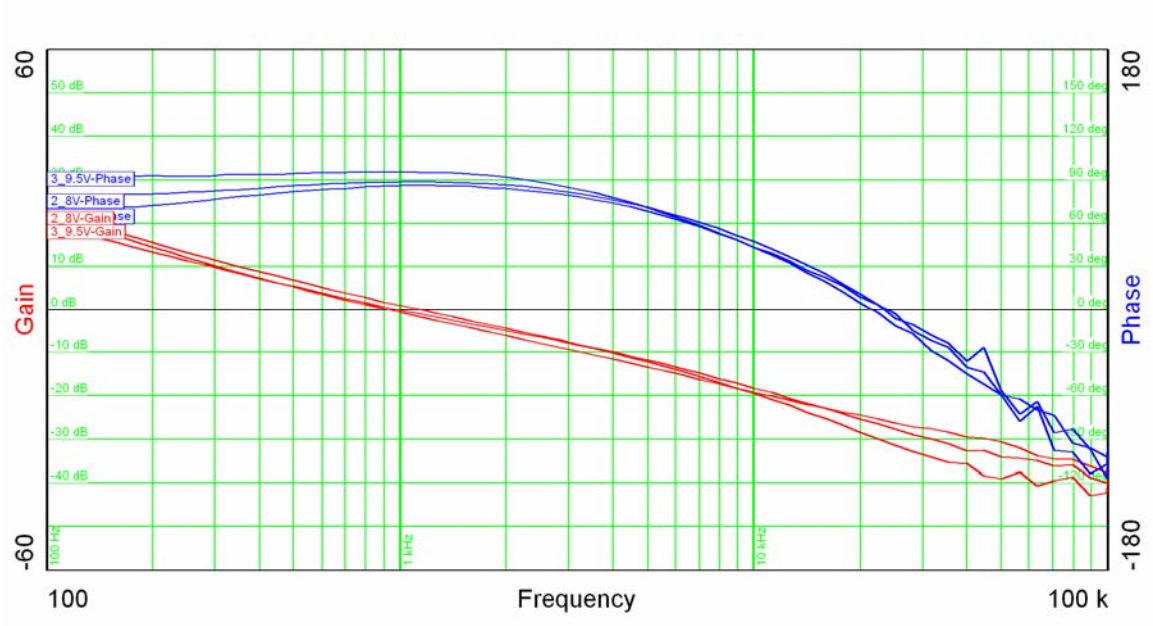
4 Output Ripple Voltage

The output ripple voltage is shown in the figure below. The image was taken with a 8 A load and 8V at the input – upper channel is switchnode, lower channel is output (AC-coupled).



5 Control Loop Frequency Response

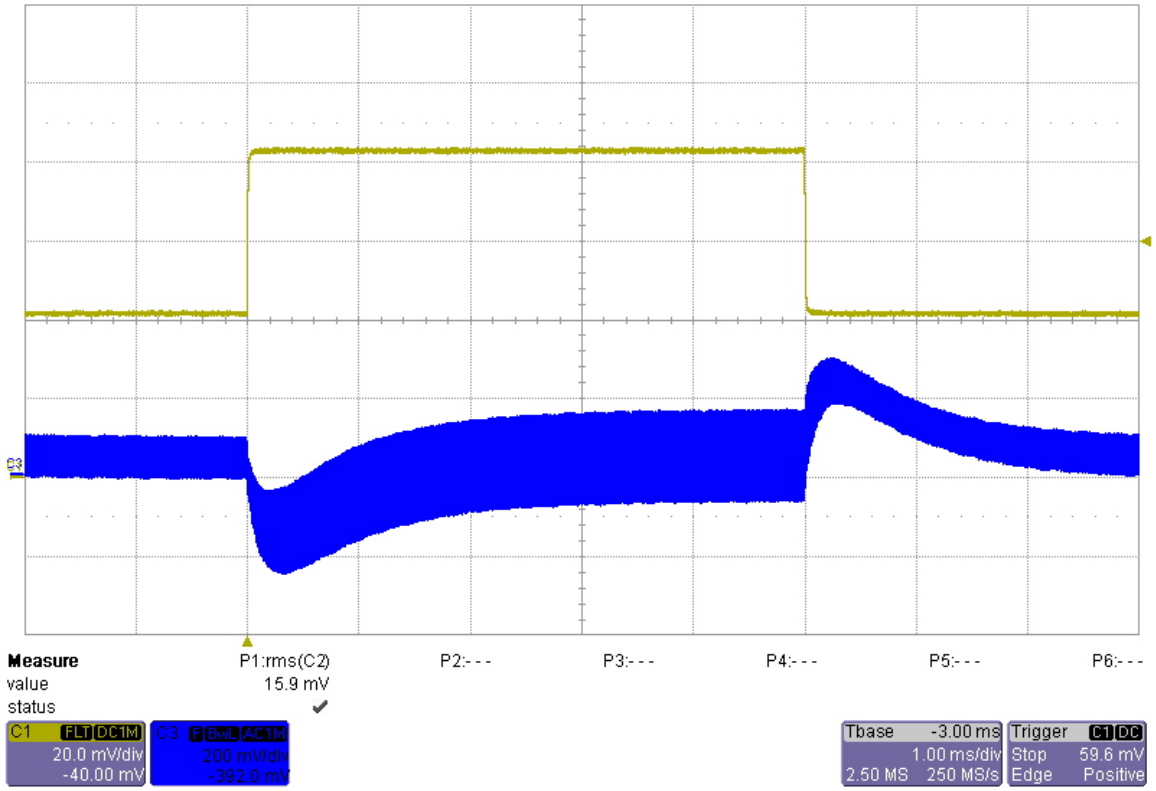
The figure below shows the loop response with different input voltages.



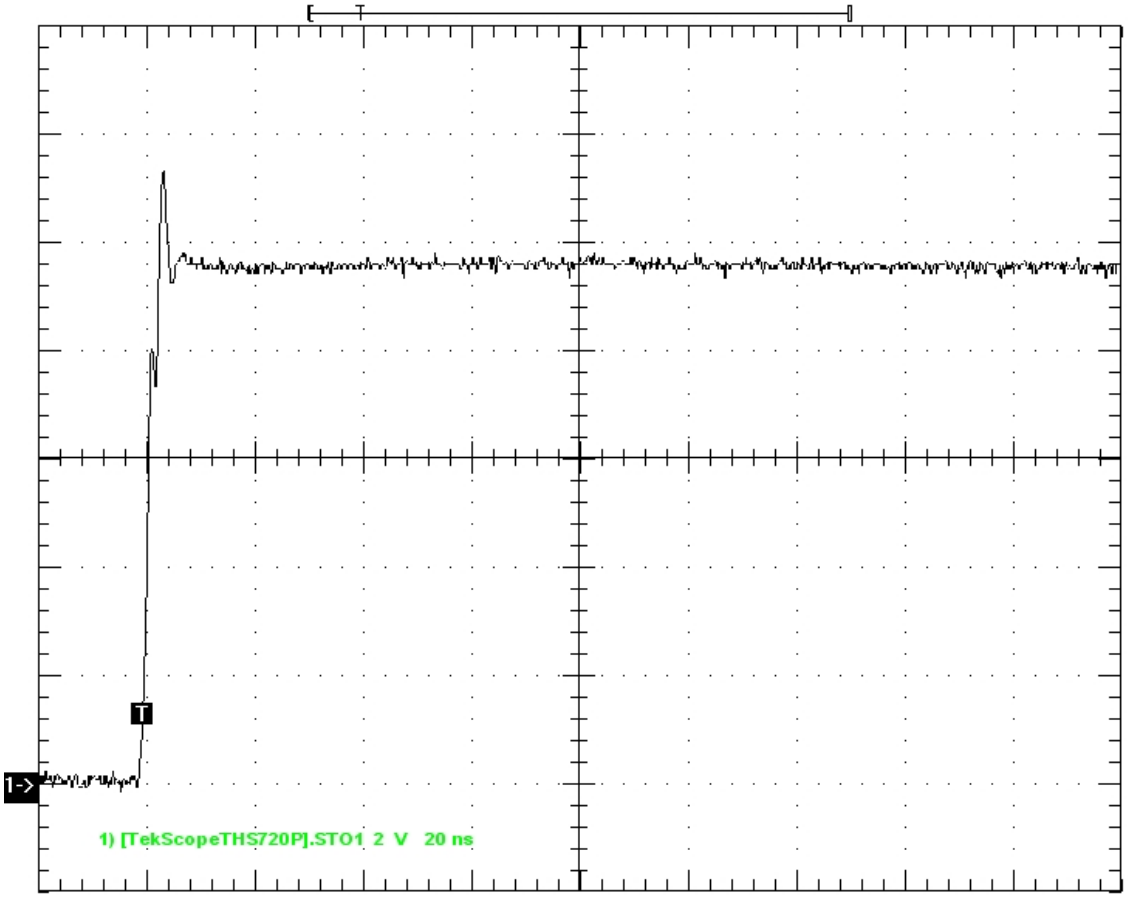
6 Load Transients

The figure below shows the response to load transients. The input voltage was set to 8V. The load is switching from 4 A to 8 A.

Channel 1 shows the waveform at the switchnode



7 Switch Node Waveform – rising edge w/ short leads:



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