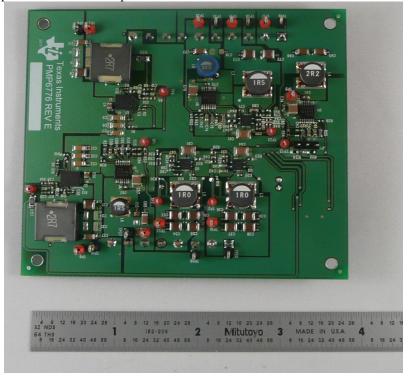
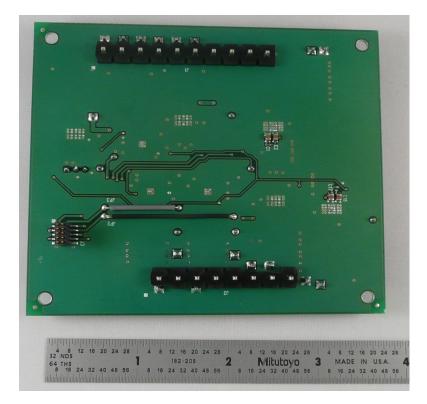


1 Photo

The photographs below show the top and bottom view of the PMP6776 Rev F demo board.







2 Thermal Images

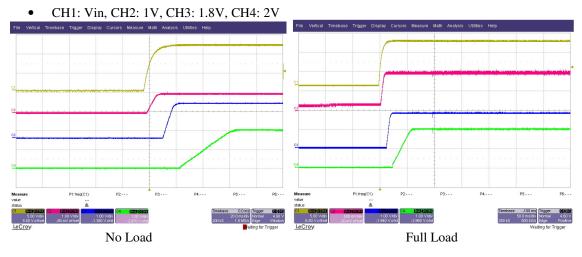
The images below show thermal images of the board. The ambient temperature was 25° C with no forced air flow.



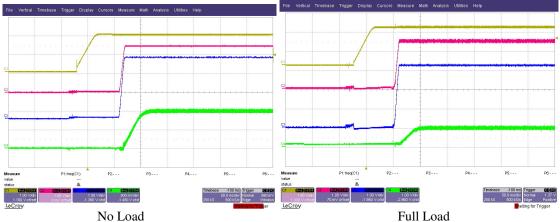


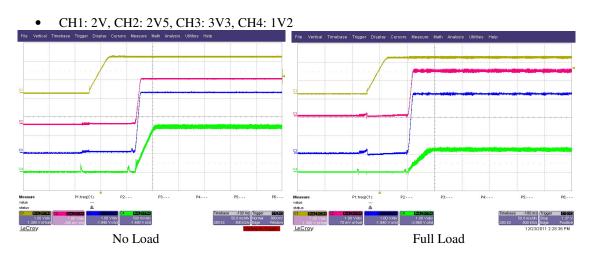
3 Start-up Timing

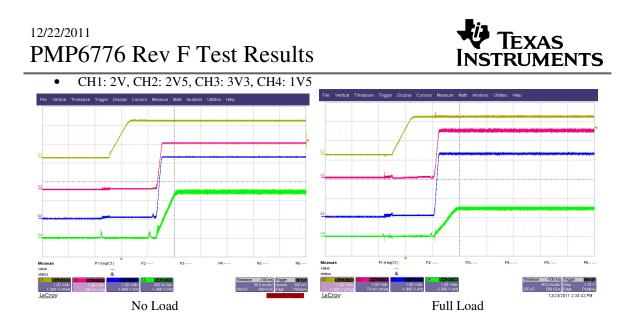
The output voltages at startup are shown in the images below.



• CH1: 2V, CH2: 2V5, CH3: 3V3, CH4: 1V MGT



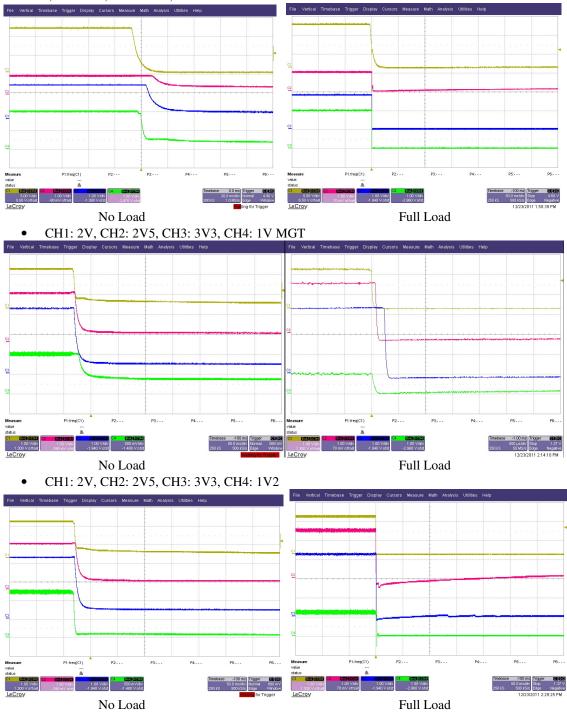


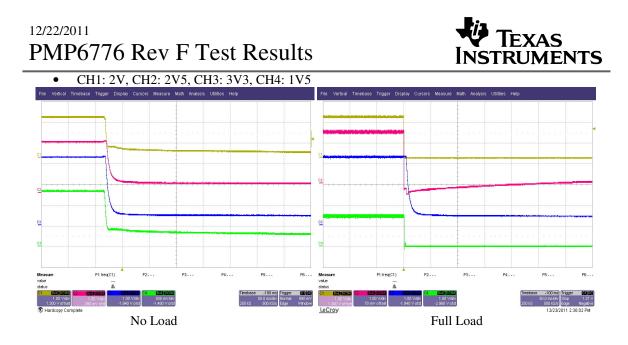




4 Turn-off Timing

The output voltages at turn-off are shown in the images below. CH1: Vin, CH2: 1V, CH3: 1.8V, CH4: 2V



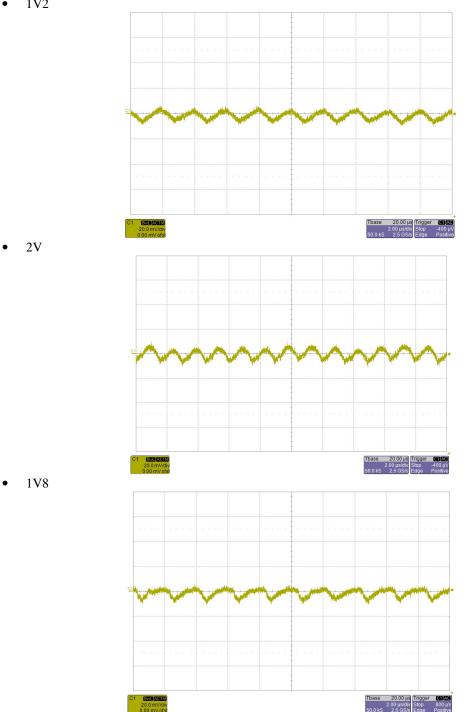


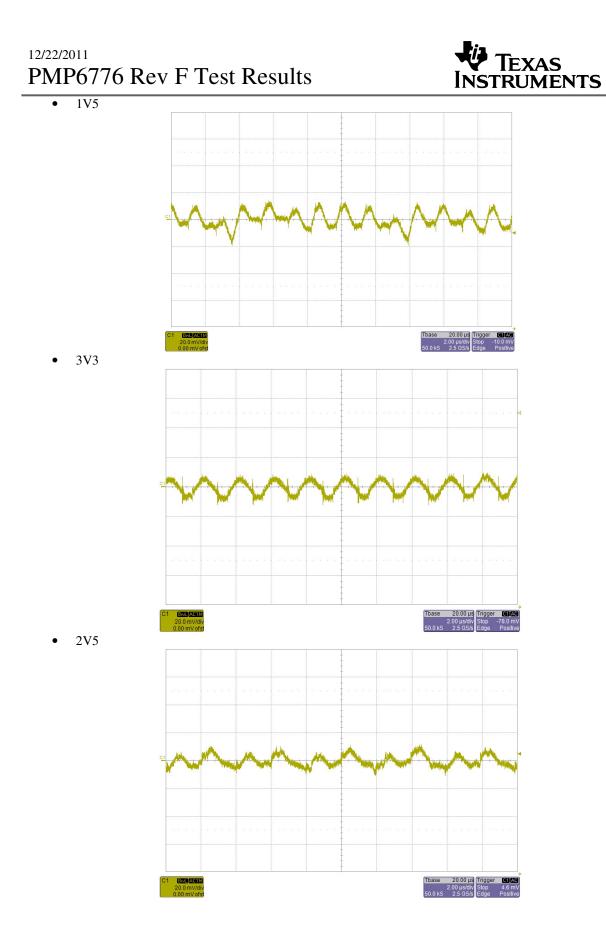


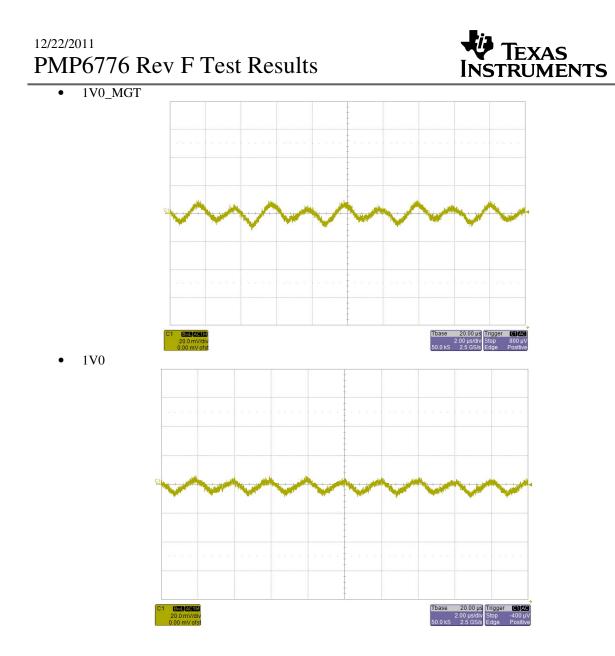
Ripple and Noise 5

The output voltage ripples at full load condition are shown in the images below.

• 1V2





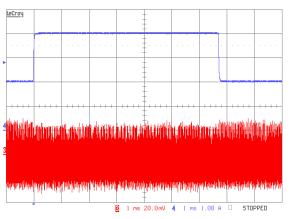




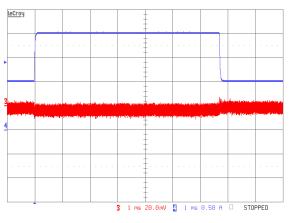
6 Dynamic Response

The dynamic responses of each output are shown in the images below.

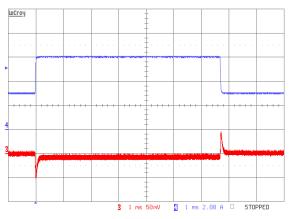
• 1V2: 2A to 4A transient.



• 2V: 1A to 2A transient.



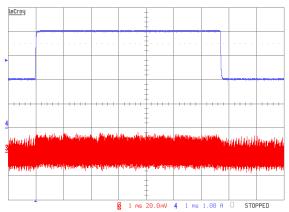
• 1V8: 3A to 6A transient.



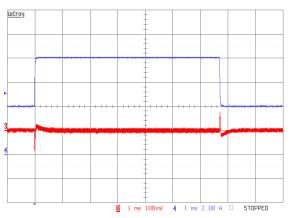
PMP6776 Rev F Test Results

• 1V5: 2A to 4A transient.

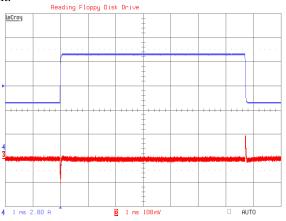




• 3V3: 4A to 8A transient.



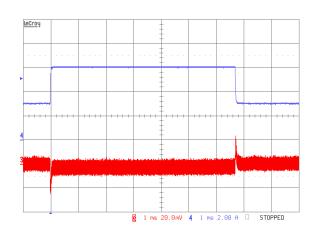
• 2V5: 4A to 8A transient.



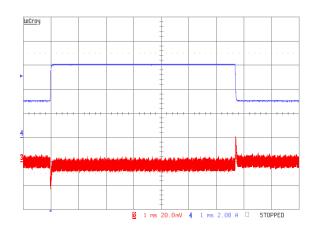
12/22/2011 PMP6776 Rev F Test Results

• 1V0_MGT







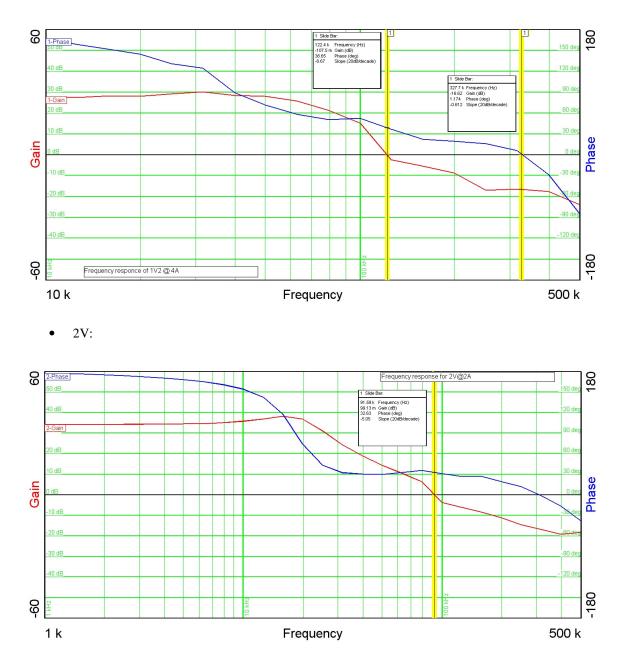


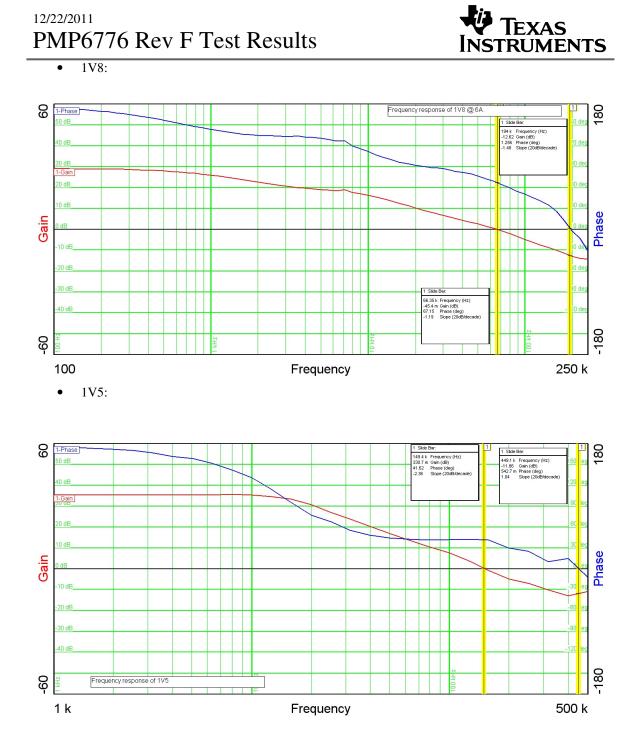


7 Frequency Response

The frequency responses of each rail with maximum load condition are shown as below.

• 1V2

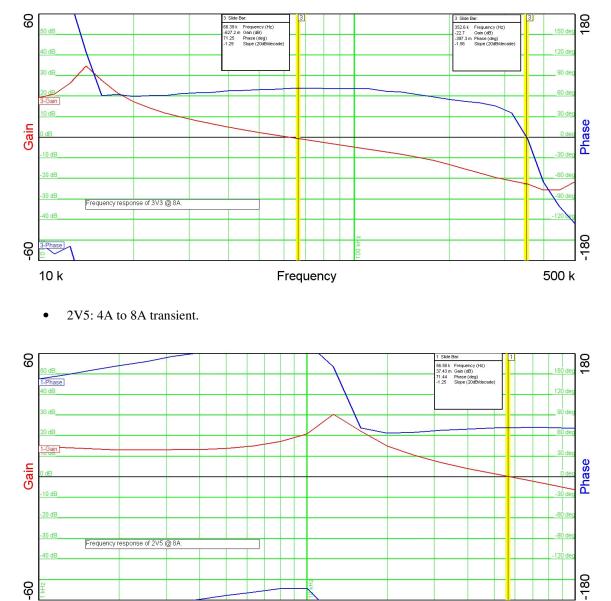






PMP6776 Rev F Test Results

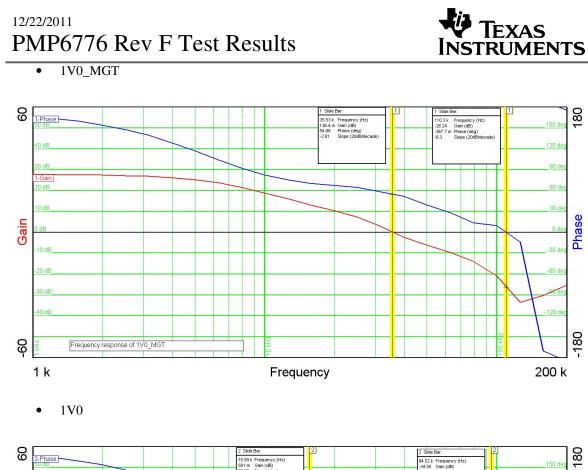
• 3V3:

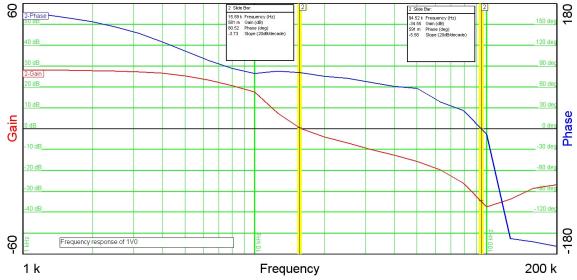


Frequency

1 k

100 k





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