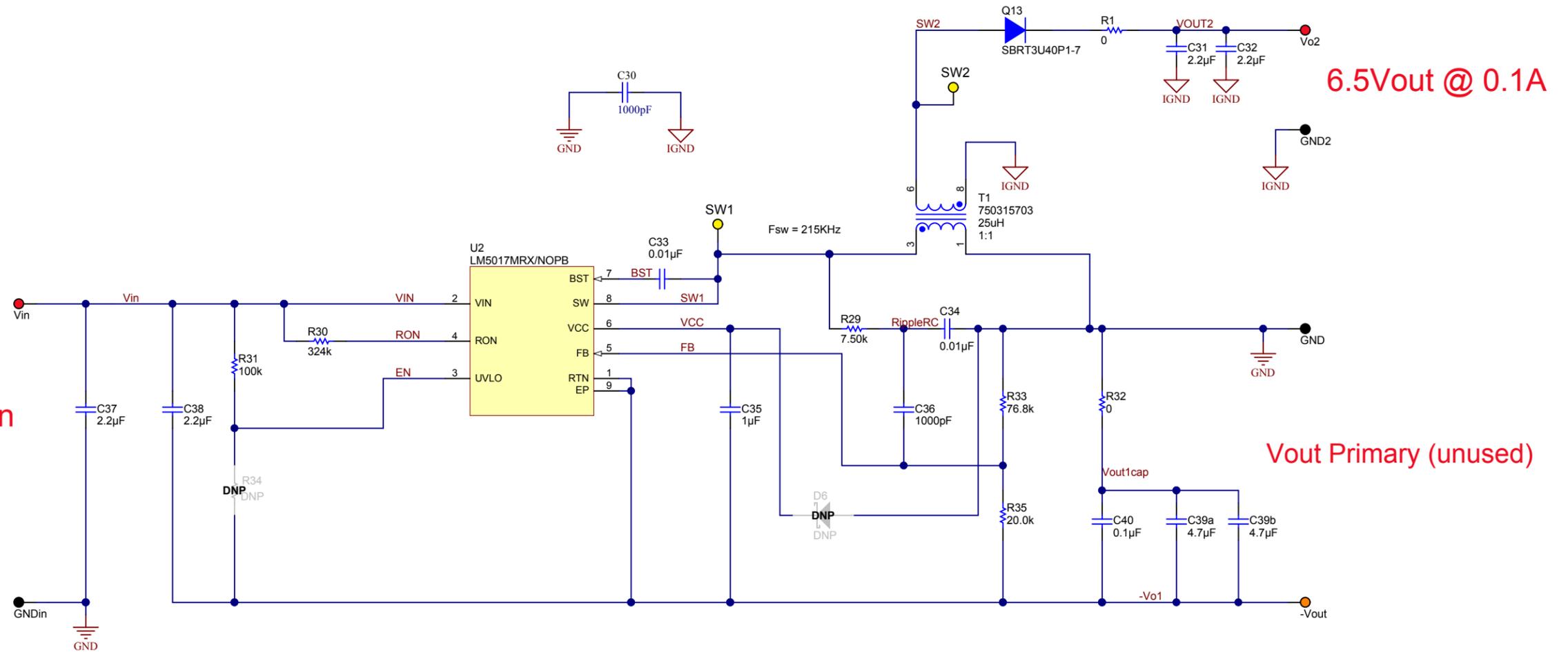


Input Voltage = 9Vin to 15Vin



***NOTES:**

1. This design is built on the PMP9291 Rev4 PCB.
2. C39a and C39b were soldered, stacked on top of one another at the C39 footprint on the PCB.
3. The primary output is not used.
4. An LM25017 regulator IC can be used in place of the LM5017. This has not been tested.
5. The UVLO function of the IC will enable the device at the nominal threshold. Once started it will not disable the device. This is due to the inverting buck-boost configuration. A separate level-shifted signal may be provided to pull the UVLO to "-Vout" in order to disable the device.

Orderable: N/A	Designed for:	Mod. Date: 11/12/2015
TID #: PMP12003	Project Title: LM5017 Isolated Fly-Buck-Boost Converter	
Number: PMP12003	Rev: A	Sheet Title: Schematic
SVN Rev: Version control disabled	Assembly Variant: 001	Sheet: 1 of 2
Drawn By:	File: PMP12003_BlankSheet_SchDoc	Size: B
Engineer: Hrag Kasparian	Contact: http://www.ti.com/support	

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H1 1
NY PMS 440 0025 PH

H2 1
NY PMS 440 0025 PH

H3 1
NY PMS 440 0025 PH

H4 1
NY PMS 440 0025 PH

H5
1902C

H6
1902C

H7
1902C

H8
1902C

DNP
FID1

DNP
FID2

DNP
FID3

PCB Assembly
PMP9291 Rev4 PCB

Label Table	
Variant	Label Text
001	

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Orderable: N/A	Designed for:	Mod. Date: 11/12/2015	
TID #: PMP12003	Project Title: LM5017 Isolated Fly-Buck-Boost Converter	Sheet: 2 of 2	
Number: PMP12003	Rev: A	Sheet Title: Hardware	http://www.ti.com
SVN Rev: Version control disabled	Assembly Variant: 001	Size: B	
Drawn By:	File: PMP12003_TID_Hardware.SchDoc	Contact: http://www.ti.com/support	
Engineer: Hrag Kasparian			© Texas Instruments 2015

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