

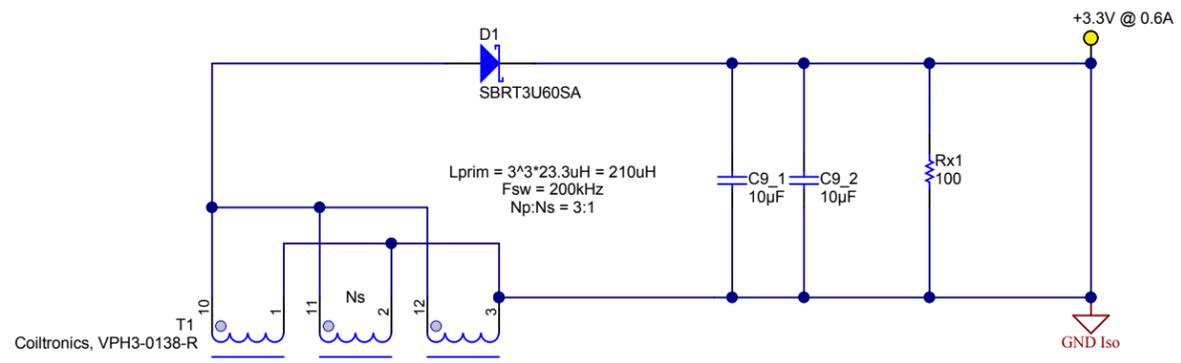
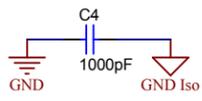
A

A

Parts designator with a X doesn't have dedicated footprint on PCB

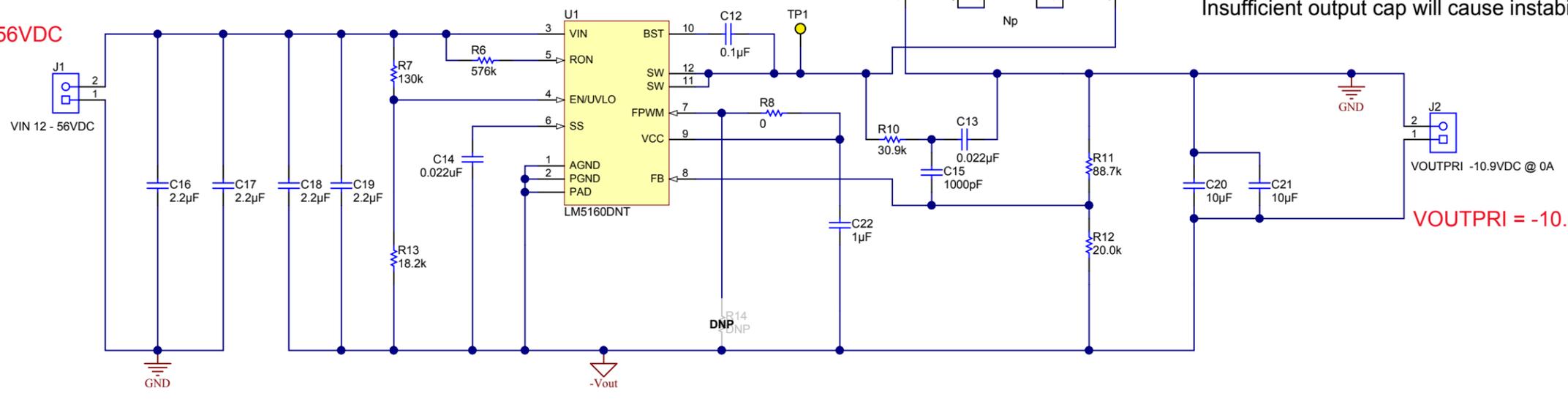
VOUTSEC = 3.3VDC @ 0.6A

PCB Build on PMP10564 Universal Board



Insufficient output cap will cause instability at light load

Input Voltage = 12V to 56VDC



VOUTPRI = -10.9VDC @ 0A

C

C

D

D

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Orderable: N/A	Designed for: Public Release	Mod. Date: 4/18/2016
TID #: PMP20210	Project Title: LM5160 Flyback	
Number: PMP20210	Rev: A	Sheet Title:
SVN Rev: Version control disabled	Assembly Variant: 001	Sheet 1 of 2
Drawn By:	File: PMP20210_REVA.SchDoc	Size: B
Engineer: Xinyu Dai	Contact: <a href="http://www.ti.com/support">http://www.ti.com/support</a>	



H1 NY PMS 440 0025 PH  
 H2 NY PMS 440 0025 PH  
 H3 NY PMS 440 0025 PH  
 H4 NY PMS 440 0025 PH

H5 1902C  
 H6 1902C  
 H7 1902C  
 H8 1902C

DNP FID1  
 DNP FID2  
 DNP FID3

PCB Number: PMP20210  
 PCB Rev: A

PCB LOGO  
 Texas Instruments

Label Table	
Variant	Label Text
001	

ZZ2  
**Assembly Note**  
 These assemblies are ESD sensitive, ESD precautions shall be observed.

ZZ3  
**Assembly Note**  
 These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.

ZZ4  
**Assembly Note**  
 These assemblies must comply with workmanship standards IPC-A-610 Class 2, unless otherwise specified.

Orderable: N/A	Designed for: Public Release	Mod. Date: 4/12/2016
TID #: PMP20210	Project Title: LM5160 Flyback	
Number: PMP20210	Rev: A	Sheet Title:
SVN Rev: Version control disabled	Assembly Variant: 001	Sheet: 2 of 2
Drawn By:	File: PMP20210_REVA_Hardware.SchDoc	Size: B
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