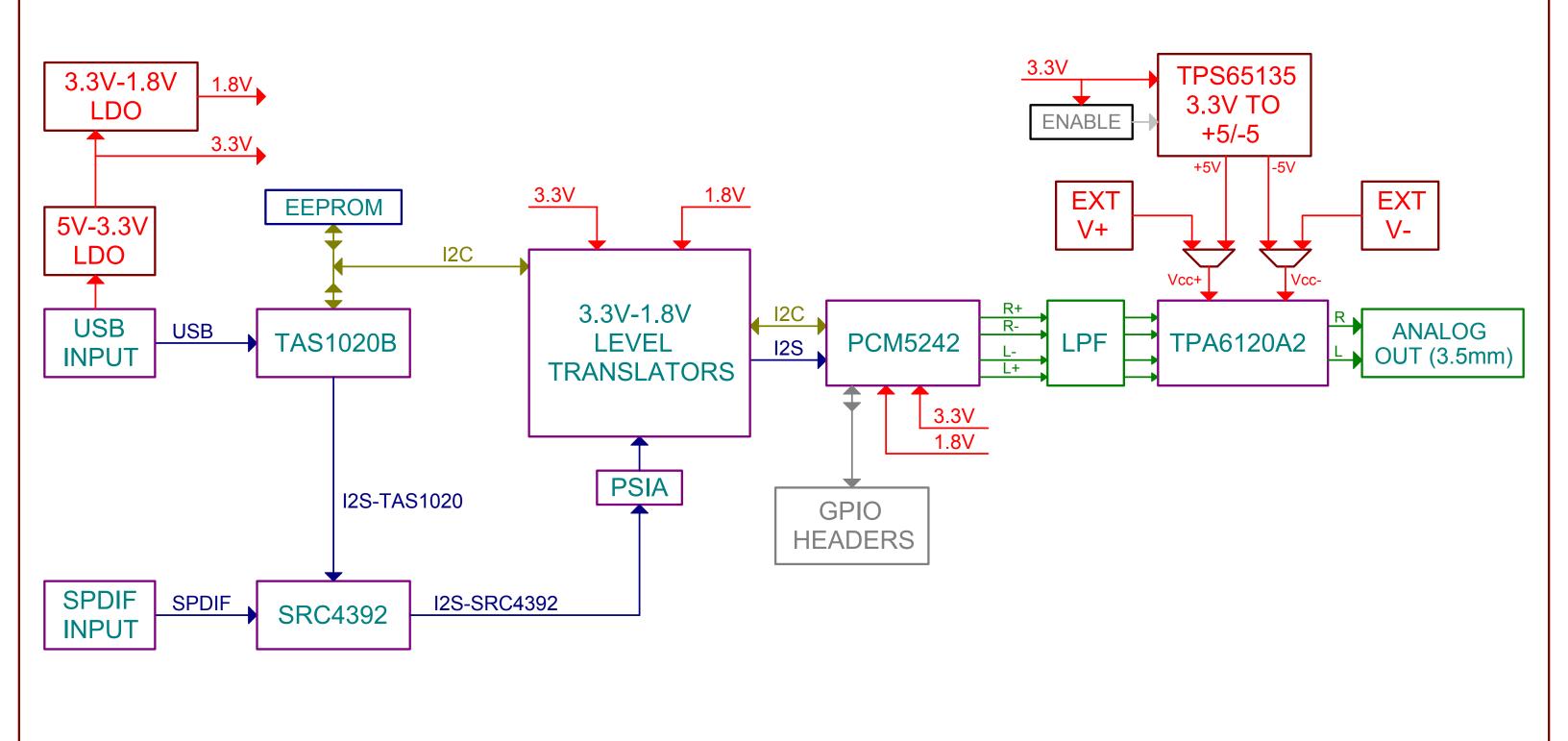
PCM5242RHB+TPA6120A2RGY QFN REFERENCE DESIGN



TEXAS INSTRUMENTS

PAGE INFO: BLOCK DIAGRAM

DESIGN LEAD DAVID WILSON, DAFYDD ROCHE, ROMAN WANG

TIDA-00385

DATE JUNE 18, 2014

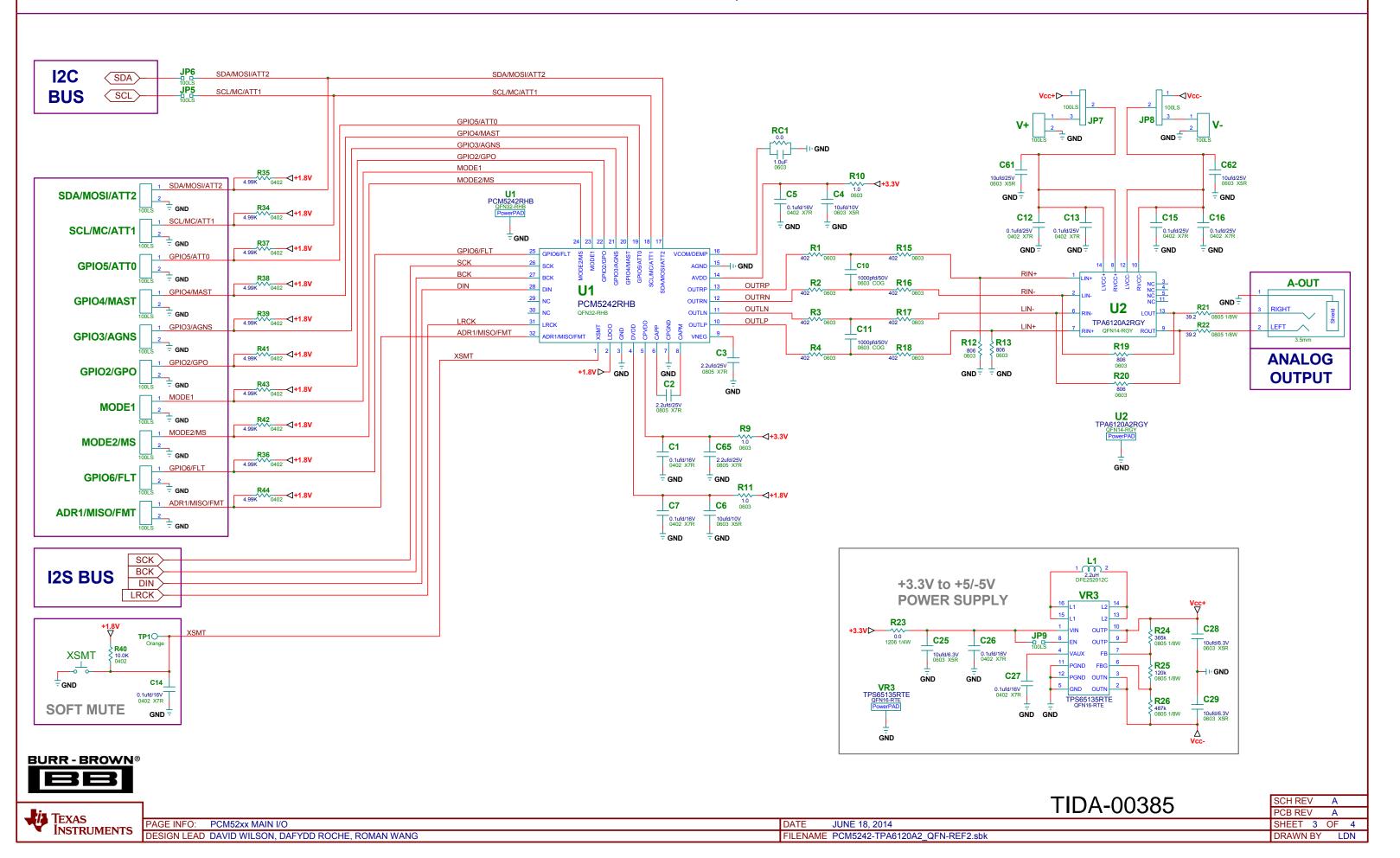
SCH REV A
PCB REV A
SHEET 1 OF 4

FILENAME PCM5242-TPA6120A2_QFN-REF2.sbk

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PCM5242RHB+TPA6120A2RGY QFN REFERENCE DESIGN **SPDIF INPUT** SPDIF S **DECOUPLING** R50 75 0603 0.1ufd/16V 0402 X7R C31 RX2-0.1ufd/16V 0402 X7R 0.1ufd/16V 0402 X7R SDA-USB —ОRXCKI +1.8V C48 OPTO-IN VCC GND Case 1.00K R52 0402 1.0ufd/16V 0603 X7R SRC-READY R53 GND GND GND GND C56 RXIN 0.1ufd/16V 0402 X7R 5V to 3.3V LDO PLR135/T10 RX-LOCK R55 C17 C18 ___C19 C23 0.1ufd/16V 0402 X7R 0.1ufd/16V 0402 X7R C37 10ufd/10V 0603 X5R U3 0.1ufd/16V 0402 X7R SRC4392IPFBR **PSIA CLOCK SELECT TRANSLATORS** JUMPER 1-2 FOR PASS-THRU ___C33 0.1ufd/16V 0402 X7R **I2S BUS** -SCK GND 74AUP1G125DRL SOT553-DRL5 LRCLK-SRC C34 C35 BUF2 SDOUT-SRC 10ufd/10V 0603 X5R 0.1ufd/16V 0402 X7R JP2 SDOUT-USB + GND LRCLK-USB BCK SCLK-USB 74AUP1G125DRL SOT553-DRL5 BUF3 1 0E VCC 5 2 A 74AUP1G125DRL ¹—∏JP3 **RESET** EEPROM +3.3V - LRCK RESET 0.1ufd/16V 0402 X7R BUF4 _∏JP4 OSC1 +3.3V R82 DIN 74AUP1G125DRL R83 **USB INPUT** R85 47.0 0402 USB R73 27.4 0402 **I2C BUS** C42 C43 47pfd/50V 0402 47pfd/50V 0402 SDA-USB 6 SDA_B SDA_A 3 SDA SDA TAS1020BPFB - GND **POWER SUPPLIES HARDWARE** ___C20 C22 USB-LOCK TPS73618DBVT 1.8V/400mA \bigcirc \bigcirc \bigcirc \bigcirc C21 GND GND GND GND **BUMPONS** TIDA-00385 SCH REV A TEXAS INSTRUMENTS PAGE INFO: USB / SAMPLE RATE CONVERTER JUNE 18, 2014 DESIGN LEAD DAVID WILSON, DAFYDD ROCHE, ROMAN WANG

PCM5242RHB+TPA6120A2RGY QFN REFERENCE DESIGN



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