

LMR23625CFPDRREVM User's Guide

The Texas Instruments LMR23625CFPEVM evaluation module (EVM) helps designers evaluate the operation and performance of the LMR23625 WSON wide-input Simple Switcher® buck regulator. This document describes the setup and the input and output connections of the EVM. Included are the board layout, schematic, and bill of materials.

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Introduction www.ti.com

1 Introduction

The LMR23625 is a 36 V, 2.5 A step-down synchronous regulator. With a wide input range from 4 V to 36 V, it is suitable for a wide range of applications from automotive to industry for power conditioning from unregulated sources. The LMR23625CFPEVM evaluation board is designed to provide the design engineer to evaluate the LMR23625 WSON series operation and performance.

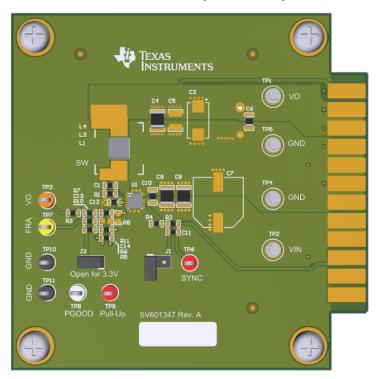


Figure 1. LMR23625CFPEVM Board

EVM Features

- 4 V to 36 V Input Voltage Range
- Jumper Selectable Output Options (5 V or 3.3 V)
- Up to 2.5 A Output Current
- Switching Frequency 2100 kHz
- Frequency Synchronization to External Clock
- Hiccup Mode Short Current Protection

The EVM contains one DC-DC converter (See Table 1)

Table 1. Device and Package Configurations

CONVERTER	EVM	IC	PACKAGE
U1	LMR23625CFPEVM	LMR23625CFPDRR	WSON-12



www.ti.com Setup

2 Setup

This section describes the jumpers and connectors on the EVM and how to properly connect, set up and use the LMR23625CFPEVM.

2.1 Input/Output Connector Description

VIN — **Terminal TP2** – is the power input terminal for the converter. Adjacent to it is the GND reference ground. Use this terminal to attach the EVM to a cable harness.

VOUT — **Terminal TP1** – is the regulated output voltage for the converter. Adjacent to it is the GND reference ground.

GND — **Terminal TP4**, **TP5** – are the ground reference for the converter. Use these terminals to attach the EVM to a cable harness.

EN — **Jumper J1** – is used to enable the switch-mode converter. The device will be enabled when the respective jumper is high or floating, and disabled when low. The EVM default system UVLO is 6.2 V (typical), it also can be programmed by changing R2 or R4. Refer to LMR23625 datasheet for enable and adjustable undervoltage lockout.

SYNC — **Terminal TP6** – is used to synchronize the switching frequency to external clock. Refer to datasheet for detail application information.

PGOOD — **Terminal TP8, TP9** – TP8 is used to monitor the power good flag. This flag indicates whether the output voltage has reached its regulation point. This pin is an open-drain output that requires a pull-up resistor to the appropriate logic voltage (any voltage less than 12 V). A pre-installed resistor R5 of 100 k Ω is tied to the PGOOD pin and brought out to the Pull-Up test point TP9. Supply an appropriate voltage to TP9, or tie it directly to the VOUT terminal to observe the PGOOD flag operation.

Testpoint — **TP3**, **TP7**, **TP10**, **TP11** – these are test points used for input/output voltage measurements and loop response measurements.

2.2 Adjusting the Output Voltage

The default setting output voltage is 5.0 V. Open J2 will change output voltage from 5 V to 3.3 V.

If other outputs need to be configured, then: open J2 and adjust the feedback resistors using Equation 1

$$V_{OUT} = V_{REF} \times (1 + (R7 / R11))$$

where



Board Layout www.ti.com

3 Board Layout

Figure 2 to Figure 5 show the board layout for the LMR23625CFPEVM. The PCB consists of a 4-layer design. There are 2-oz copper planes on the top and bottom and 1-oz copper mid-layer planes to dissipate heat with an array of thermal vias under the thermal pad to connect to all four layers.

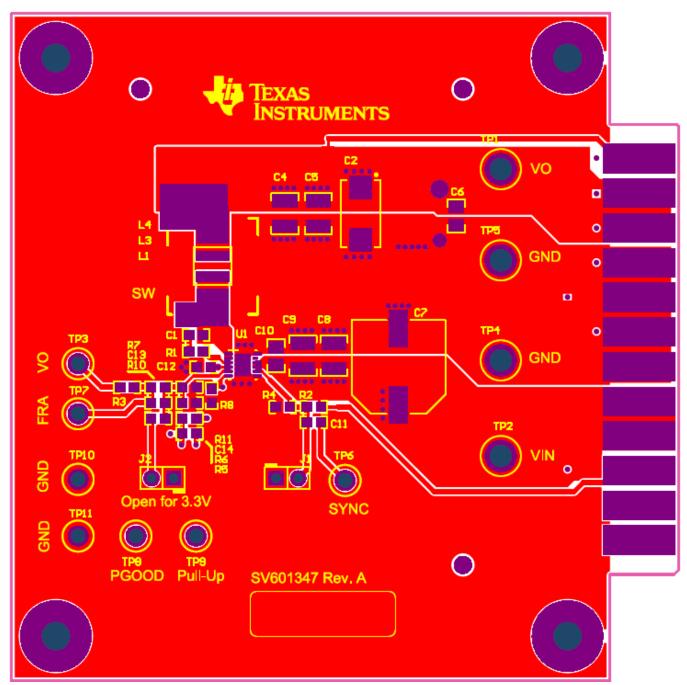


Figure 2. Top Layer



www.ti.com Board Layout

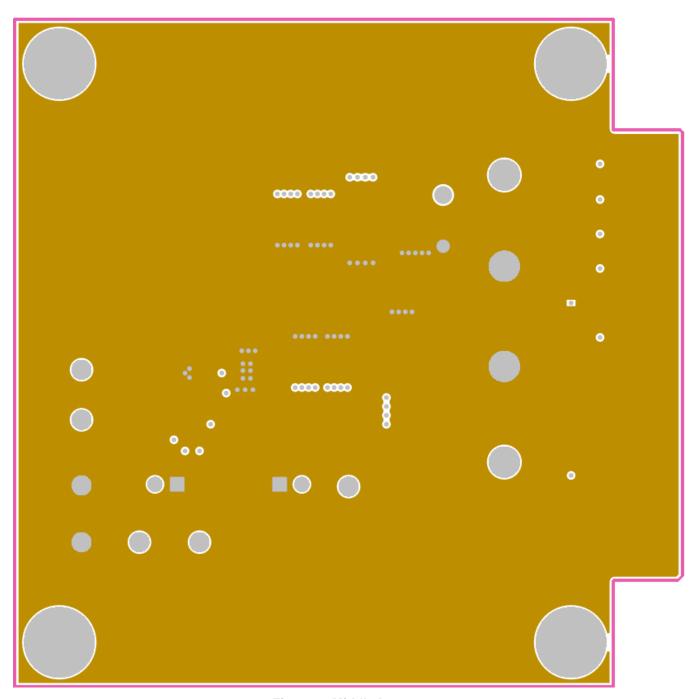


Figure 3. Middle Layer 1



Board Layout www.ti.com

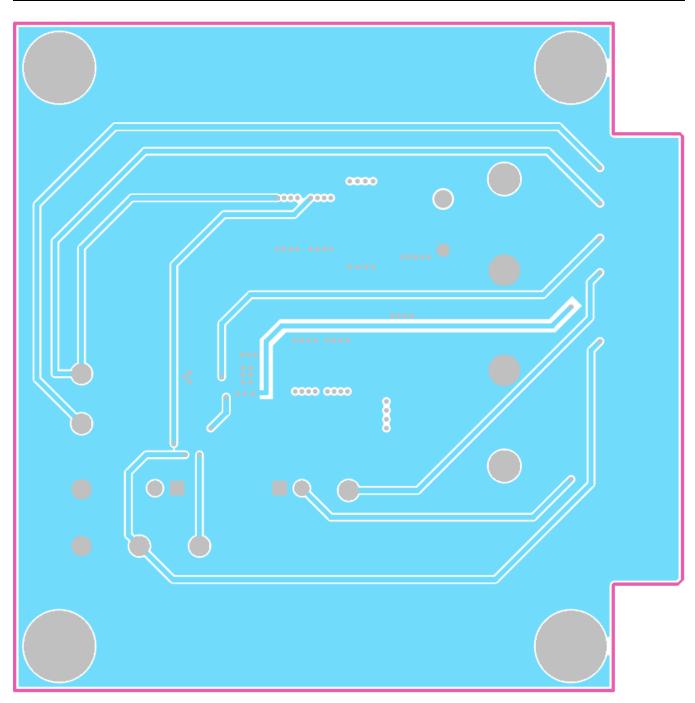


Figure 4. Middle Layer 2



www.ti.com Board Layout

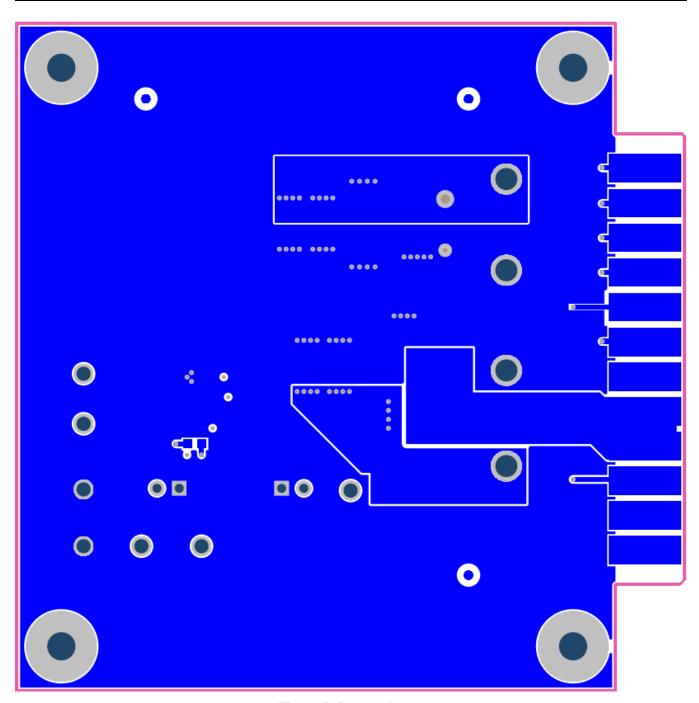


Figure 5. Bottom Layer



4 Schematic and Bill of Materials

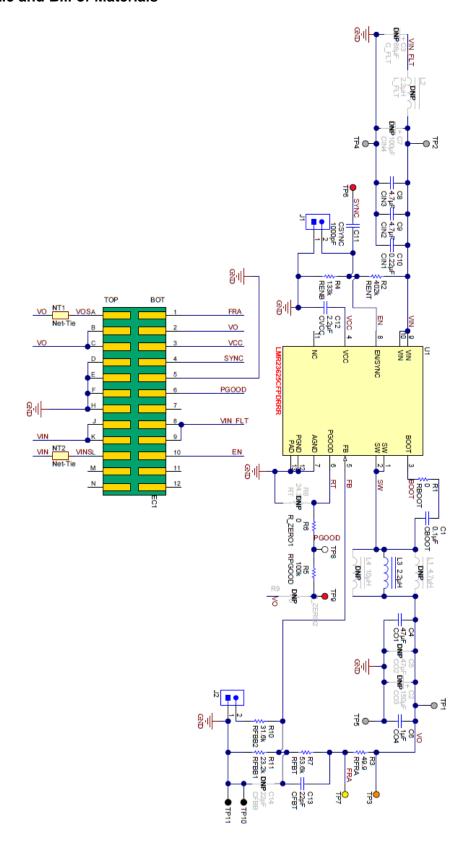


Figure 6. LMR23625CFPEVM Schematic



Table 2. LMR23625CFPEVM Bill of Materials (BOM)

Designator	Description	PartNumber	Quantity
C1	CAP, CERM, 0.1 µF, 25 V, +/- 10%, X7R, 0603	GRM188R71E104KA01D	1
C4	CAP, CERM, 47 µF, 10 V, +/- 20%, X7R, 1210	GRM32ER71A476ME15L	1
C6	CAP, CERM, 1 μF, 25 V, +/- 10%, X7R, 0805	GRM219R71E105KA88D	1
C8, C9	CAP, CERM, 4.7 µF, 50 V, +/- 10%, X7R, 1210	GRM32ER71H475KA88L	2
C10	CAP, CERM, 0.22 μF, 50 V, +/- 10%, X7R, 0805	GRM21BR71H224KA01L	1
C11	CAP, CERM, 1000 pF, 50 V, +/- 10%, X7R, 0603	GRM188R71H102KA01D	1
C12	CAP, CERM, 2.2 µF, 16 V, +/- 10%, X7R, 0603	GRM188Z71C225KE43	1
C13	CAP, CERM, 22 pF, 50 V, +/- 5%, C0G/NP0, 0603	GRM1885C1H220JA01D	1
J1, J2	Header, 100mil, 2x1, Gold, TH	TSW-102-07-G-S	2
L3	Inductor, Shielded, 2.2 µH, 4.7 A, 0.029 ohm, SMD	74438356022	1
LBL1	Thermal Transfer Printable Labels, 0.650" W x 0.200" H - 10,000 per roll	THT-14-423-10	1
R1, R6	RES, 0, 5%, 0.1 W, 0603	CRCW06030000Z0EA	2
R2	RES, 402 k, 1%, 0.1 W, 0603	CRCW0603402KFKEA	1
R3	RES, 49.9, 1%, 0.1 W, 0603	CRCW060349R9FKEA	1
R4	RES, 133 k, 1%, 0.1 W, 0603	CRCW0603133KFKEA	1
R5	RES, 100 k, 1%, 0.1 W, 0603	CRCW0603100KFKEA	1
R7	RES, 53.6 k, 1%, 0.1 W, 0603	CRCW060353K6FKEA	1
R10	RES, 31.6 k, 1%, 0.1 W, 0603	CRCW060331K6FKEA	1
R11	RES, 23.2 k, 1%, 0.1 W, 0603	CRCW060323K2FKEA	1
SH-J1, SH-J2	Shunt, 100mil, Flash Gold, Black	SPC02SYAN	2
TP1, TP2, TP4, TP5	Terminal, Turret, TH, Double	1502-2	4
TP3	Test Point, Compact, Orange, TH	5008	1
TP6, TP9	Test Point, Compact, Red, TH	5005	2
TP7	Test Point, Compact, Yellow, TH	5009	1
TP8	Test Point, Compact, White, TH	5007	1
TP10, TP11	Test Point, Compact, Black, TH	5006	2
U1	SIMPLE SWITCHER 36V 2.5 A Synchronous Step- Down Converter, DRR0012D (WSON-12)	LMR23625CFPDRRR	1

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- Increase the separation between the equipment and receiver.
- · Connect the equipment into an outlet on a circuit different from that to which the receiver is connected.
- Consult the dealer or an experienced radio/TV technician for help.

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3.2.1 For EVMs issued with an Industry Canada Certificate of Conformance to RSS-210 or RSS-247

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