

SN54HC08-DIE

ZHCSB52-JUNE 2013

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抗辐射航空级裸片,四路2输入正与门

查询样品: SN54HC08-DIE

特性

- 宽运行电压范围
- 输出可驱动多达 10 个低功耗肖特基晶体管逻辑电路 (LSTTL) 负载
- 低功耗
- t_{pd}典型值 = 8ns
- 低输入电流

说明

此 SN54HC08-DIE 器件包含 4 个独立 2 输入与门。 每个门在正逻辑中执行 Y = A • B 或 Y = A + B 布尔函数。

ORDERING INFORMATION ⁽¹⁾							
PRODUCT	PACKAGE DESIGNATOR	PACKAGE	ORDERABLE PART NUMBER	PACKAGE QUANTITY			
SN54HC08V	TD	Doro dia in woffla $\operatorname{pack}^{(2)}$	SN54HC08VTDF1	100			
		Dare die in wante pack	SN54HC08VTDF2	10			

(1) For the most current package and ordering information, see the Package Option Addendum at the end of this document, or see the TI web site at www.ti.com.

(2) Processing is per the Texas Instruments space production baseline and is in compliance with the Texas Instruments Quality Control System in effect at the time of manufacture. Electrical screening consists of DC parametric and functional testing at room temperature only. Unless otherwise specified by Texas Instruments AC performance and performance over temperature is not warranted. Visual Inspection is performed in accordance with MIL-STD-883 Test Method 2010 Condition B at 75X minimum.



Please be aware that an important notice concerning availability, standard warranty, and use in critical applications of Texas Instruments semiconductor products and disclaimers thereto appears at the end of this data sheet.

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BOND PAD

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This integrated circuit can be damaged by ESD. Texas Instruments recommends that all integrated circuits be handled with appropriate precautions. Failure to observe proper handling and installation procedures can cause damage.

ESD damage can range from subtle performance degradation to complete device failure. Precision integrated circuits may be more susceptible to damage because very small parametric changes could cause the device not to meet its published specifications.

BOND PAD



BARE DIE INFORMATION

BACKSIDE

SN54HC08-DIE

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DESCRIPTION	PAD NUMBER	X MIN	Y MIN	X MAX	Y MAX	
1A	1	166.5	413.1	267.3	513.9	
1B	2	166.5	155.7	267.3	256.5	
1Y	3	504.9	155.7	605.7	256.5	
2A	4	645.3	155.7	746.1	256.5	
2B	5	902.7	155.7	1003.5	256.5	
2Y	6	1097.1	299.7	1197.9	400.5	
GND	7	1097.1	440.1	1197.9	540.9	
3Y	8	1097.1	580.5	1197.9	681.3	
3A	9	1090.8	888.3	1191.6	989.1	
3B	10	834.3	888.3	935.1	989.1	
4Y	11	693.9	888.3	794.7	989.1	
4A	12	375.3	888.3	476.1	989.1	
4B	13	166.5	888.3	267.3	989.1	
V _{CC}	14	166.5	632.7	267.3	733.5	

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PACKAGING INFORMATION

Orderable Device	Status (1)	Package Type	Package Drawing	Pins	Package Qty	Eco Plan (2)	Lead finish/ Ball material (6)	MSL Peak Temp (3)	Op Temp (°C)	Device Marking (4/5)	Samples
SN54HC08VTDF1	ACTIVE			0	100	RoHS & Green	Call TI	N / A for Pkg Type	25 to 25		Samples
SN54HC08VTDF2	ACTIVE			0	10	RoHS & Green	Call TI	N / A for Pkg Type	25 to 25		Samples

⁽¹⁾ The marketing status values are defined as follows:

ACTIVE: Product device recommended for new designs.

LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

PREVIEW: Device has been announced but is not in production. Samples may or may not be available.

OBSOLETE: TI has discontinued the production of the device.

⁽²⁾ RoHS: TI defines "RoHS" to mean semiconductor products that are compliant with the current EU RoHS requirements for all 10 RoHS substances, including the requirement that RoHS substance do not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, "RoHS" products are suitable for use in specified lead-free processes. TI may reference these types of products as "Pb-Free".

RoHS Exempt: TI defines "RoHS Exempt" to mean products that contain lead but are compliant with EU RoHS pursuant to a specific EU RoHS exemption.

Green: TI defines "Green" to mean the content of Chlorine (CI) and Bromine (Br) based flame retardants meet JS709B low halogen requirements of <=1000ppm threshold. Antimony trioxide based flame retardants must also meet the <=1000ppm threshold requirement.

⁽³⁾ MSL, Peak Temp. - The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

⁽⁴⁾ There may be additional marking, which relates to the logo, the lot trace code information, or the environmental category on the device.

⁽⁵⁾ Multiple Device Markings will be inside parentheses. Only one Device Marking contained in parentheses and separated by a "~" will appear on a device. If a line is indented then it is a continuation of the previous line and the two combined represent the entire Device Marking for that device.

⁽⁶⁾ Lead finish/Ball material - Orderable Devices may have multiple material finish options. Finish options are separated by a vertical ruled line. Lead finish/Ball material values may wrap to two lines if the finish value exceeds the maximum column width.

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PACKAGE OPTION ADDENDUM

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