TPS60151 5V 与 140mA 电荷泵

1 特性

- 输入电压范围：2.7V 至 5.5V
- 5V 固定输出电压
- 最大输出电流：140mA
- 1.5MHz 开关频率
- 在无负载情况下具有典型值 90μA 的静态电流（跳跃模式）
- 输出反向电流保护
- X2 电荷泵
- 硬件启用和禁用功能
- 内置软启动
- 内置欠压锁定保护
- 热保护和过流保护
- 2mm x 2mm 6 引脚 SON 封装，高度为 0.8mm

2 应用

- USB On the Go (OTG)
- HDMI
- 便携式通信器件
- PCMCIA 卡
- 手机、智能电话
- 手持式仪表

3 说明

TPS60151 是一款开关电容电压转换器，能够从非稳定输入电压中输出稳定、低噪声、低纹波的 5V 输出电压。在 VIN 大于 5V 时仍然可以维持 5V 稳压。

5V 输出可以提供最低 140mA 的电流。

TPS60151 具有内置电流限制和输出反向电流保护，是 HDMI、USB OTG 和其他电池供电应用的理想选择。

在典型情况下，当负载电流低于 8mA 时，TPS60151 会在跳跃模式下运行。在跳跃运行模式下，静态电流降低到 90μA。

只有 3 个外部电容器需要生成输出电压，由此节省了 PCB 空间。

在上电和电源瞬态期间，浪涌电流受到软启动功能的限制。

TPS60151 在自然通风环境下的额定运行温度范围为 -40°C 至 85°C。该器件采用小尺寸 2mm x 2mm 6 引脚 SON 封装 (QFN)。

器件信息(1)

<table>
<thead>
<tr>
<th>器件型号</th>
<th>封装</th>
<th>封装尺寸（标称值）</th>
</tr>
</thead>
<tbody>
<tr>
<td>TPS60151</td>
<td>WSON (6)</td>
<td>2.00mm x 2.00mm</td>
</tr>
</tbody>
</table>

(1) 如需了解所有可用封装，请参阅数据表末尾的可订购产品附录。

效率与输入电压间的关系
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4 修订历史记录
注：之前版本的页码可能与当前版本有所不同。

Changes from Revision A (October 2015) to Revision B Page
• Added row for V_{OUT} spec -- 3.1 V ≤ V_{IN} < 5.5V .................................................. 5
• Added row for V_{OUT(skip)} V spec -- 3.1 V ≤ V_{IN} < 5.5V ........................................ 5

Changes from Original (August 2009) to Revision A Page
• 已添加 引脚配置和功能 部分、ESD 额定值 表、特性 说明 部分、器件功能模式、应用和实施 部分、电源建议 部分、
布局 部分、器件和文档支持 部分以及机械、封装和可订购信息 部分 ................................. 1
5 Device Comparison Table

<table>
<thead>
<tr>
<th>PART NUMBER (1)</th>
<th>OUTPUT VOLTAGE</th>
<th>PACKAGE DESIGNATOR</th>
<th>ORDERING</th>
<th>PACKAGE MARKING</th>
</tr>
</thead>
<tbody>
<tr>
<td>TPS60151</td>
<td>5 V</td>
<td>DRV</td>
<td>TPS60151DRV</td>
<td>OCN</td>
</tr>
</tbody>
</table>

(1) The DRV (2 mm × 2 mm 6-pin SON) package is available in tape on reel. Add R suffix to order quantities of 3000 parts per reel and T suffix to order quantities with 250 parts per reel.

6 Pin Configuration and Functions

### Pin Functions

<table>
<thead>
<tr>
<th>PIN</th>
<th>I/O</th>
<th>DESCRIPTION</th>
</tr>
</thead>
<tbody>
<tr>
<td>GND</td>
<td>1</td>
<td>Ground</td>
</tr>
<tr>
<td>VIN</td>
<td>2</td>
<td>Supply voltage input</td>
</tr>
<tr>
<td>VOUT</td>
<td>3</td>
<td>Output, connect to the output capacitor</td>
</tr>
<tr>
<td>CP+</td>
<td>4</td>
<td>Connect to the flying capacitor</td>
</tr>
<tr>
<td>CP−</td>
<td>5</td>
<td>Connect to the flying capacitor</td>
</tr>
<tr>
<td>ENA</td>
<td>6</td>
<td>Hardware enable/disable pin (High = Enable)</td>
</tr>
</tbody>
</table>
# 7 Specifications

## 7.1 Absolute Maximum Ratings

Over operating free-air temperature range (unless otherwise noted) \(^{(1)}\)

<table>
<thead>
<tr>
<th></th>
<th>MIN</th>
<th>MAX</th>
<th>UNIT</th>
</tr>
</thead>
<tbody>
<tr>
<td>(V_{\text{IN}})</td>
<td>–0.3</td>
<td>7</td>
<td>V</td>
</tr>
<tr>
<td>(T_{\text{A}})</td>
<td>–40</td>
<td>85</td>
<td>°C</td>
</tr>
<tr>
<td>(T_{\text{J}})</td>
<td></td>
<td>150</td>
<td>°C</td>
</tr>
<tr>
<td>(T_{\text{STG}})</td>
<td>–55</td>
<td>150</td>
<td>°C</td>
</tr>
</tbody>
</table>

\(^{(1)}\) Stresses beyond those listed under *Absolute Maximum Ratings* may cause permanent damage to the device. These are stress ratings only and functional operation of the device at these or any other conditions beyond those indicated under *Recommended Operating Conditions* is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

## 7.2 ESD Ratings

<table>
<thead>
<tr>
<th>(V_{\text{(ESD)}})</th>
<th>Electrostatic discharge</th>
<th>VALUE</th>
<th>UNIT</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>Human body model (HBM), per ANSI/ESDA/JEDEC JS-001(^{(1),(2)})</td>
<td>±2000</td>
<td>V</td>
</tr>
<tr>
<td></td>
<td>Charged-device model (CDM), per JEDEC specification JESD22-C101(^{(3)})</td>
<td>±500</td>
<td>V</td>
</tr>
</tbody>
</table>

\(^{(1)}\) JEDEC document JEP155 states that 500-V HBM allows safe manufacturing with a standard ESD control process.

\(^{(2)}\) The Human body model (HBM) is a 100 pF capacitor discharged through a 1.5 k\(\Omega\) resistor into each pin. The testing is done according JEDECs EIA/JESD22-A114.

\(^{(3)}\) JEDEC document JEP157 states that 250-V CDM allows safe manufacturing with a standard ESD control process.

## 7.3 Recommended Operating Conditions

over operating free-air temperature range (unless otherwise noted)

<table>
<thead>
<tr>
<th></th>
<th>MIN</th>
<th>NOM</th>
<th>MAX</th>
<th>UNIT</th>
</tr>
</thead>
<tbody>
<tr>
<td>(V_{\text{IN}})</td>
<td>2.7</td>
<td>5.5</td>
<td>V</td>
<td></td>
</tr>
<tr>
<td>(T_{\text{A}})</td>
<td>–40</td>
<td>85</td>
<td>°C</td>
<td></td>
</tr>
<tr>
<td>(T_{\text{J}})</td>
<td>–40</td>
<td>125</td>
<td>°C</td>
<td></td>
</tr>
<tr>
<td>(C_{\text{IN}})</td>
<td>2.2</td>
<td></td>
<td>μF</td>
<td></td>
</tr>
<tr>
<td>(C_{\text{OUT}})</td>
<td>2.2</td>
<td></td>
<td>μF</td>
<td></td>
</tr>
<tr>
<td>(C_{\text{F}})</td>
<td>1.0</td>
<td></td>
<td>μF</td>
<td></td>
</tr>
</tbody>
</table>

## 7.4 Thermal Information

<table>
<thead>
<tr>
<th>THERMAL METRIC(^{(1)})</th>
<th>TPS60151</th>
<th>UNIT</th>
</tr>
</thead>
<tbody>
<tr>
<td>(R_{\text{JA}})</td>
<td>69.1</td>
<td>°C/W</td>
</tr>
<tr>
<td>(R_{\text{JCTO}})</td>
<td>79.8</td>
<td>°C/W</td>
</tr>
<tr>
<td>(R_{\text{JB}})</td>
<td>38.6</td>
<td>°C/W</td>
</tr>
<tr>
<td>(\psi_{\text{JT}})</td>
<td>1.2</td>
<td>°C/W</td>
</tr>
<tr>
<td>(\psi_{\text{JB}})</td>
<td>38.4</td>
<td>°C/W</td>
</tr>
<tr>
<td>(R_{\text{JCBOT}})</td>
<td>9.2</td>
<td>°C/W</td>
</tr>
</tbody>
</table>

\(^{(1)}\) For more information about traditional and new thermal metrics, see the *Semiconductor and IC Package Thermal Metrics* application report, SPRA953.
7.5 Electrical Characteristics

$V_{\text{IN}} = 3.6 \text{ V}, \ T_{\text{A}} = -40^\circ \text{C} \text{ to } +85^\circ \text{C}$, typical values are at $T_{\text{A}} = 25^\circ \text{C}$, $C1 = C3 = 2.2 \mu \text{F}$, $C2 = 1 \mu \text{F}$ (unless otherwise noted)

<table>
<thead>
<tr>
<th>PARAMETER</th>
<th>TEST CONDITIONS</th>
<th>MIN</th>
<th>TYP</th>
<th>MAX</th>
<th>UNIT</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>POWER STAGE</strong></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>$V_{\text{IN}}$</td>
<td>Input voltage range</td>
<td>2.7</td>
<td>5.5</td>
<td></td>
<td>V</td>
</tr>
<tr>
<td>$V_{\text{UVLO}}$</td>
<td>Undervoltage lockout threshold</td>
<td>1.9</td>
<td>2.1</td>
<td></td>
<td>V</td>
</tr>
<tr>
<td>$I_{\text{Q}}$</td>
<td>Operating quiescent current</td>
<td></td>
<td></td>
<td></td>
<td>mA</td>
</tr>
<tr>
<td>$I_{\text{Qskip}}$</td>
<td>Skip mode operating quiescent current</td>
<td>4.7</td>
<td></td>
<td></td>
<td>mA</td>
</tr>
<tr>
<td>$I_{\text{SD}}$</td>
<td>Shutdown current</td>
<td>2.7</td>
<td></td>
<td></td>
<td>mA</td>
</tr>
<tr>
<td>$V_{\text{OUT}}$</td>
<td>Output voltage (1)</td>
<td>4.8</td>
<td>5</td>
<td>5.2</td>
<td>V</td>
</tr>
<tr>
<td>$V_{\text{OUT}(\text{skip})}$</td>
<td>Skip mode output voltage</td>
<td>4.8</td>
<td>5</td>
<td>5.15</td>
<td>V</td>
</tr>
<tr>
<td>$F_{\text{SW}}$</td>
<td>Switching frequency</td>
<td>1.5</td>
<td></td>
<td></td>
<td>MHz</td>
</tr>
<tr>
<td>$I_{\text{SS}}$</td>
<td>Soft-start time</td>
<td>150</td>
<td></td>
<td></td>
<td>$\mu \text{s}$</td>
</tr>
<tr>
<td><strong>OUTPUT CURRENT</strong></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>$I_{\text{OUT_nom}}$</td>
<td>Maximum output current</td>
<td>120</td>
<td></td>
<td></td>
<td>mA</td>
</tr>
<tr>
<td>$I_{\text{OUT_max}}$</td>
<td>Current limit</td>
<td>140</td>
<td></td>
<td></td>
<td>mA</td>
</tr>
<tr>
<td>$I_{\text{OUT_short}}$</td>
<td>Short circuit current (2)</td>
<td>80</td>
<td></td>
<td></td>
<td>mA</td>
</tr>
<tr>
<td>$V_{\text{R}}$</td>
<td>Output ripple voltage</td>
<td></td>
<td>30</td>
<td></td>
<td>mV</td>
</tr>
<tr>
<td><strong>ENABLE CONTROL</strong></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>$V_{\text{HI}}$</td>
<td>Logic high input voltage</td>
<td>1.3</td>
<td></td>
<td></td>
<td>$V_{\text{IN}}$</td>
</tr>
<tr>
<td>$V_{\text{L}}$</td>
<td>Logic low input voltage</td>
<td>-0.2</td>
<td></td>
<td>0.4</td>
<td>V</td>
</tr>
<tr>
<td>$I_{\text{HI}}$</td>
<td>Logic high input current</td>
<td>1</td>
<td></td>
<td></td>
<td>$\mu \text{A}$</td>
</tr>
<tr>
<td>$I_{\text{L}}$</td>
<td>Logic low input current</td>
<td>1</td>
<td></td>
<td></td>
<td>$\mu \text{A}$</td>
</tr>
<tr>
<td><strong>THERMAL SHUTDOWN</strong></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>$T_{\text{SD}}$</td>
<td>Shutdown temperature</td>
<td>160</td>
<td></td>
<td></td>
<td>°C</td>
</tr>
<tr>
<td>$T_{\text{RC}}$</td>
<td>Shutdown recovery</td>
<td>140</td>
<td></td>
<td></td>
<td>°C</td>
</tr>
</tbody>
</table>

(1) When in skip mode, output voltage can exceed $V_{\text{OUT}}$ spec because $V_{\text{OUT}(\text{skip})} = V_{\text{OUT}} + 0.1$.
(2) TPS60151 has internal protection circuit to protect IC when $V_{\text{OUT}}$ shorted to GND.
7.6 Typical Characteristics

**Figure 1. Quiescent Current vs Input Voltage**

**Figure 2. Maximum Output Current vs Input Voltage at Temperature**
8 Detailed Description

8.1 Overview
The TPS60151, regulated charge pump, provides a regulated output voltage for various input voltages. The TPS60151 regulates the voltage across the flying capacitor to 2.5 V and controls the voltage drop of Q1 and Q2 while a conversion clock with 50% duty cycle drives the FETs.

During the first half cycle, Q2 and Q3 transistors are turned on and flying capacitor, $C_F$, will be charged to 2.5 V ideally.

During the second half cycle, Q1 and Q4 transistors are turned on. Capacitor $C_F$ will then be discharged to output.

The output voltage can be calculated as follows:

\[
V_{\text{out}} = V_{\text{in}} - V_{Q1} + V(C_F) - V_{Q4} = V_{\text{in}} - V_{Q1} + 2.5\,\text{V} - V_{Q4} = 5\,\text{V}. \quad (\text{Ideal})
\]

The output voltage is regulated by output feedback and an internally compensated voltage control loop.
8.2 Functional Block Diagram

8.3 Feature Description

8.3.1 Enable
An enable pin on the regulator is used to place the device into an energy-saving shutdown mode. In this mode, the output is disconnected from the input and the input quiescent current is reduced to 10 μA maximum.

8.3.2 Output Reverse Current Protection
Applications like HDMI or USB OTG generally do not tolerate output reverse current that can drain power from connected devices. Special considerations were put in place to prevent that from happening. Figure 5 is a testing circuit; and, Figure 6 shows reverse current protection test results under various conditions.

Figure 5. Output Reverse Current Test Setup
Feature Description (continued)

![Graph showing reverse current test results](image)

**Figure 6. Reverse Current Test Results (Typical)**

### 8.3.3 Undervoltage Lockout

When the input voltage drops, the undervoltage lockout prevents misoperation by switching off the device. The converter starts operation again when the input voltage exceeds the threshold, provided the enable pin is high.

### 8.3.4 Thermal Shutdown Protection

The regulator has thermal shutdown circuitry that protects it from damage caused by overload conditions. The thermal protection circuitry disables the output when the junction temperature reached approximately 160°C, allowing the device to cool. When the junction temperature cools to approximately 140°C, the output circuitry is automatically re-enabled. Continuously running the regulator into thermal shutdown can degrade reliability. The regulator also provides current limit to protect itself and the load.

### 8.4 Device Functional Modes

#### 8.4.1 Soft Start

An internal soft start limits the inrush current when the device is being enabled.
Device Functional Modes (continued)

8.4.2 Normal Mode and Skip Mode Operation

The TPS60151 has skip mode operation as shown in Figure 7. The TPS60151 enters skip mode if the output voltage reaches 5 V + 0.1 V and the load current is below 8 mA (typical). In skip mode, the TPS60151 disables the oscillator and decreases the pre-bias current of the output stage to reduce the power consumption. Once the output voltage drops below threshold voltage, 5 V + 0.1 V, the TPS60151 begins switching to increase output voltage until the output reaches 5 V + 0.1 V. When the output voltage drops below 5 V, the TPS60151 returns to normal pulse width modulation (PWM) mode; thereby re-enabling the oscillator and increasing the pre-bias current of the output stage to supply output current.

The skip threshold voltage and current depend on input voltage and output current conditions.

8.4.3 Over-current Protection and Short-Circuit Protection

The TPS60151 has internal short circuit protection to protect the IC when the output is over loaded or shorted to ground. Figure 8 illustrates the protection circuit. $I_P$ is directly related to $I_{OUT}$ and the maximum $I_P$ is clamped by $IR3*k*n$. The TPS60151 ensures a current limit of 500 mA or less which is mandated by the HDMI electrical specification. To further avoid damage when output is shorted to ground, the short circuit protection circuitry senses the output voltage and adjusts $V_{bias}$ down to clamp the maximum output current to a lower value –80 mA (typical).
Device Functional Modes (continued)

Figure 8. Current Limit

Figure 9. Maximum Output Current Capability and Short Circuit Protection
9 Application and Implementation

NOTE
Information in the following applications sections is not part of the TI component specification, and TI does not warrant its accuracy or completeness. TI’s customers are responsible for determining suitability of components for their purposes. Customers should validate and test their design implementation to confirm system functionality.

9.1 Application Information
Most of today’s battery-powered portable electronics allow and/or require data transfer with a PC. One of the fastest data transfer protocols is via USB OTG. As Figure 10 shows, the USB OTG circuitry in the portable device requires a 5-V power rail and up to 140mA of current. The HDMI specification calls for a 5-V power rail that can source 55mA or more current. The TPS60151 may be used to provide a 5-V power rail in a battery powered system.

Alternatively, low-cost portable electronics with small LCD displays require a low-cost solution for providing the WLED backlight. As shown in Figure 26, the TPS60151 can also be used to drive several WLEDs in parallel, with the help of ballast resistors.

9.2 Typical Application
Figure 10 shows USB OTG circuitry.

9.2.1 Design Requirements
The design guideline provides a component selection to operate the device within the recommended operating conditions.

9.2.2 Detailed Design Procedure
9.2.2.1 Capacitor Selection

For minimum output voltage ripple, the output capacitor (C_OUT) should be a surface-mount ceramic capacitor. Tantalum capacitors generally have a higher effective series resistance (ESR) and may contribute to higher output voltage ripple. Leaded capacitors also increase ripple due to the higher inductance of the package itself. To achieve the best operation with low input voltage and high load current, the input and flying capacitors (C_IN and C_FLY, respectively) should also be surface-mount ceramic types.
Typical Application (continued)

![Capacitor diagram](image)

Figure 11. Capacitors

Generally, \( C_{FLY} \) can be calculated by the following simple equation,

\[
Q_{charging} = C \times V = C_{FLY} \times \Delta V_{CFLY},
\]

\[
Q_{discharging} = I_{discharge} \times t = 2 \times I_{LOAD(MAX)} \times \left( \frac{T}{2} \right), \text{ half duty.}
\]

Both equation should be same,

\[
: 2 \times I_{LOAD(MAX)} \times \left( \frac{T}{2} \right) = C_{FLY} \times \Delta V_{CFLY}
\]

If \( I_{LOAD} = 140 \text{ mA} \), \( f = 1.5 \text{ MHz} \), and \( \Delta V_{CFLY} = 100 \text{ mV} \), the minimum value of the flying capacitor should be 1 \( \mu \text{F} \).

Output capacitance, \( C_{OUT} \), is also strongly related to output ripple voltage and loop stability,

\[
V_{OUT(rippled)} = \frac{I_{LOAD(MAX)} \times \left( \frac{T}{2} \right)}{2 \times f \times C_{OUT}} + 2I_{LOAD(MAX)} \times ESR_{COUT}
\]

The minimum output capacitance for all output levels is 2.2 \( \mu \text{F} \) due to control stability. Larger ceramic capacitors or low ESR capacitors can be used to lower the output ripple voltage.

<table>
<thead>
<tr>
<th>VALUE</th>
<th>DIELECTRIC MATERIAL</th>
<th>PACKAGE SIZE</th>
<th>RATED VOLTAGE</th>
</tr>
</thead>
<tbody>
<tr>
<td>4.7 ( \mu \text{F} )</td>
<td>X5R or X7R</td>
<td>0603</td>
<td>10 V</td>
</tr>
<tr>
<td>2.2 ( \mu \text{F} )</td>
<td>X5R or X7R</td>
<td>0603</td>
<td>10 V</td>
</tr>
</tbody>
</table>

The efficiency of the charge pump regulator varies with the output voltage, the applied input voltage and the load current.

The approximate efficiency in normal operating mode is given by:

\[
\text{Efficiency} = \frac{PD(out)}{PD(in)} \times 100 = \frac{V_{OUT} \times I_{OUT}}{V_{IN} \times I_{IN}} \times 100 \quad (l_{IN} = 2 \times I_{OUT} + I_{Q})
\]

Quiescent current was neglected.

\[
\text{Efficiency} = \frac{V_{OUT}}{2 \times V_{IN}} \times 100 \quad (l_{IN} = 2 \times I_{OUT})
\]
9.2.3 Application Curves

Figure 12. Output Voltage vs Output Current

Figure 13. Output Voltage vs Input Voltage

Figure 14. Efficiency vs Input Voltage

*Figure 15. Load Transient Response

\[ V_{IN} = 2.7 \, V, \, I_o = 30 \, mA \text{ to } 50 \, mA \]*

*Figure 16. Load Transient Response

\[ V_{IN} = 3.6 \, V, \, I_o = 60 \, mA \text{ to } 100 \, mA \]*

*Figure 17. Output Ripple

\[ V_{CC} = 2.7 \, V, \, I_o = 0 \, mA \]*
Figure 18. Load Transient
$V_{CC} = 3.6\, \text{V},\, I_o = 0\, \text{mA}$

Figure 19. Output Ripple Voltage (Normal Mode)
$V_{IN} = 2.7\, \text{V},\, I_o = 50\, \text{mA}$

Figure 20. Output Ripple (Normal Mode)
$V_{IN} = 3.6\, \text{V},\, I_o = 100\, \text{mA}$

Figure 21. Power On
$V_{IN} = 2.7\, \text{V},\, I_o = 50\, \text{mA}$

Figure 22. Power On
$V_{IN} = 3.6\, \text{V},\, I_o = 100\, \text{mA}$

Figure 23. Enable / Disable
$V_{IN} = 2.7\, \text{V},\, I_o = 50\, \text{mA}$
9.3 System Example

9.3.1 Circuit for Driving White LEDs

Figure 26. Application Circuit for Driving White LEDs
10 Power Supply Recommendations

The TPS60151 has no special requirements for its input power supply. The input power supply's output current needs to be rated according to the supply voltage, output voltage and output current of the TPS60151.

11 Layout

11.1 Layout Guidelines

Large transient currents flow in the VIN, VOUT, and GND traces. To minimize both input and output ripple, keep the capacitors as close as possible to the regulator using short, direct circuit traces.

11.2 Layout Example

![Recommended PCB Layout](image)

*Figure 27. Recommended PCB Layout*
12 器件和文档支持

12.1 接收文档更新通知

要接收文档更新通知，请导航至 TI.com.cn 上的器件产品文件夹。单击右上角的通知我进行注册，即可每周接收产品信息更改摘要。有关更改的详细信息，请查看任何已修订文档中包含的修订历史记录。

12.2 社区资源

下列链接提供到 TI 社区资源的连接。链接的内容由各个分销商“按照原样”提供。这些内容并不构成 TI 技术规范，并且不一定反映 TI 的观点；请参阅 TI 的《使用条款》。

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设计支持 | TI 参考设计支持 可帮助您快速查找有帮助的 E2E 论坛、设计支持工具以及技术支持的联系信息。

12.3 商标

E2E is a trademark of Texas Instruments. All other trademarks are the property of their respective owners.

12.4 静电放电警告

这些装置包含有限的内置 ESD 保护。存储或装卸时，应将导线一起截短或将装置放置于导电泡棉中，以防止 MOS 门极遭受静电损伤。

12.5 术语表

SLYZ022 — TI 术语表。这份术语表列出并解释术语、缩写和定义。

13 机械、封装和可订购信息

以下页面包含机械、封装和可订购信息。这些信息是器件的最新可用数据。数据如有变更，恕不另行通知，且不会对此文档进行修订。如需获取此数据表的浏览器版本，请查阅左侧的导航栏。
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## PACKAGING INFORMATION

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<thead>
<tr>
<th>Orderable Device</th>
<th>Status (1)</th>
<th>Package Type</th>
<th>Package Drawing</th>
<th>Pins</th>
<th>Package Qty</th>
<th>Eco Plan (2)</th>
<th>Lead/Ball Finish (6)</th>
<th>MSL Peak Temp (3)</th>
<th>Op Temp (°C)</th>
<th>Device Marking (4/5)</th>
<th>Samples</th>
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<td>WSON</td>
<td>DRV</td>
<td>6</td>
<td>3000</td>
<td>Green (RoHS &amp; no Sb/Br)</td>
<td>CU NIPDAU</td>
<td>Level-1-260C-UNLIM</td>
<td>-40 to 85</td>
<td>OCN</td>
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<td>250</td>
<td>Green (RoHS &amp; no Sb/Br)</td>
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<td>Level-1-260C-UNLIM</td>
<td>-40 to 85</td>
<td>OCN</td>
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(1) The marketing status values are defined as follows:

**ACTIVE:** Product device recommended for new designs.

**LIFEBUY:** TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

**NRND:** Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

**PREVIEW:** Device has been announced but is not in production. Samples may or may not be available.

**OBSOLETE:** TI has discontinued the production of the device.

(2) **RoHS:** TI defines "RoHS" to mean semiconductor products that are compliant with the current EU RoHS requirements for all 10 RoHS substances, including the requirement that RoHS substance do not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, "RoHS" products are suitable for use in specified lead-free processes. TI may reference these types of products as "Pb-Free".

**RoHS Exempt:** TI defines "RoHS Exempt" to mean products that contain lead but are compliant with EU RoHS pursuant to a specific EU RoHS exemption.

**Green:** TI defines "Green" to mean the content of Chlorine (Cl) and Bromine (Br) based flame retardants meet JS709B low halogen requirements of <=1000ppm threshold. Antimony trioxide based flame retardants must also meet the <=1000ppm threshold requirement.

(3) **MSL, Peak Temp.** - The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

(4) **Important Information and Disclaimer:** The information provided on this page represents TI's knowledge and belief as of the date that it is provided. TI bases its knowledge and belief on information provided by third parties, and makes no representation or warranty as to the accuracy of such information. Efforts are underway to better integrate information from third parties. TI has taken and continues to take reasonable steps to provide representative and accurate information but may not have conducted destructive testing or chemical analysis on incoming materials and chemicals. TI and TI suppliers consider certain information to be proprietary, and thus CAS numbers and other limited information may not be available for release.

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Addendum-Page 1
**TAPE AND REEL INFORMATION**

*All dimensions are nominal*

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<th>Device</th>
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TAPE AND REEL BOX DIMENSIONS

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<td>182.0</td>
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*All dimensions are nominal
Images above are just a representation of the package family, actual package may vary. Refer to the product data sheet for package details.
NOTES:

1. All linear dimensions are in millimeters. Any dimensions in parenthesis are for reference only. Dimensioning and tolerancing per ASME Y14.5M.
2. This drawing is subject to change without notice.
3. The package thermal pad must be soldered to the printed circuit board for thermal and mechanical performance.
NOTES: (continued)

4. This package is designed to be soldered to a thermal pad on the board. For more information, see Texas Instruments literature number SLUA271 (www.ti.com/lit/slua271).

5. Vias are optional depending on application, refer to device data sheet. If some or all are implemented, recommended via locations are shown.
6. Laser cutting apertures with trapezoidal walls and rounded corners may offer better paste release. IPC-7525 may have alternate design recommendations.
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